

FMG12

- 1) Includes two DTC323T transistors in a single SMT package.
- 2) Low $V_{CE(sat)}$. Ideal for muting circuit.
- 3) Can be used with $I_C = 600\text{ mA}$.

Parameter	Symbol	Limits	Unit
Collector-base voltage	V_{CB0}	30	V
Collector-emitter voltage	V_{CE0}	15	V
Emitter-base voltage	V_{EB0}	5	V
Collector current	I_c	600	mA
Collector power dissipation	P_c	300 (TOTAL)	mW
Junction temperature	T_J	150	°C
Storage temperature	T_{slq}	-55~+150	°C

* 200mW per element must not be exceeded

The drawing shows the SC-74A package with the following dimensions (in mm):

- Top View:**
 - Overall width: 2.0
 - Lead spacing (between leads 1 and 2): 1.6
 - Lead width (for leads 1 and 2): 0.3
 - Lead width (for leads 3 and 4): 0.25
 - Lead width (for leads 5 and 6): 0.25
 - Lead width (for leads 7 and 8): 0.25
 - Lead width (for leads 9 and 10): 0.25
 - Lead width (for leads 11 and 12): 0.25
 - Lead width (for leads 13 and 14): 0.25
 - Lead width (for leads 15 and 16): 0.25
 - Lead width (for leads 17 and 18): 0.25
 - Lead width (for leads 19 and 20): 0.25
 - Lead width (for leads 21 and 22): 0.25
 - Lead width (for leads 23 and 24): 0.25
 - Lead width (for leads 25 and 26): 0.25
 - Lead width (for leads 27 and 28): 0.25
 - Lead width (for leads 29 and 30): 0.25
 - Lead width (for leads 31 and 32): 0.25
 - Lead width (for leads 33 and 34): 0.25
 - Lead width (for leads 35 and 36): 0.25
 - Lead width (for leads 37 and 38): 0.25
 - Lead width (for leads 39 and 40): 0.25
 - Lead width (for leads 41 and 42): 0.25
 - Lead width (for leads 43 and 44): 0.25
 - Lead width (for leads 45 and 46): 0.25
 - Lead width (for leads 47 and 48): 0.25
 - Lead width (for leads 49 and 50): 0.25
 - Lead width (for leads 51 and 52): 0.25
 - Lead width (for leads 53 and 54): 0.25
 - Lead width (for leads 55 and 56): 0.25
 - Lead width (for leads 57 and 58): 0.25
 - Lead width (for leads 59 and 60): 0.25
 - Lead width (for leads 61 and 62): 0.25
 - Lead width (for leads 63 and 64): 0.25
 - Lead width (for leads 65 and 66): 0.25
 - Lead width (for leads 67 and 68): 0.25
 - Lead width (for leads 69 and 70): 0.25
 - Lead width (for leads 71 and 72): 0.25
 - Lead width (for leads 73 and 74): 0.25
 - Lead width (for leads 75 and 76): 0.25
 - Lead width (for leads 77 and 78): 0.25
 - Lead width (for leads 79 and 80): 0.25
 - Lead width (for leads 81 and 82): 0.25
 - Lead width (for leads 83 and 84): 0.25
 - Lead width (for leads 85 and 86): 0.25
 - Lead width (for leads 87 and 88): 0.25
 - Lead width (for leads 89 and 90): 0.25
 - Lead width (for leads 91 and 92): 0.25
 - Lead width (for leads 93 and 94): 0.25
 - Lead width (for leads 95 and 96): 0.25
 - Lead width (for leads 97 and 98): 0.25
 - Lead width (for leads 99 and 100): 0.25
- Side View:**
 - Overall height: 1.1
 - Lead height: 0.8
 - Lead thickness: 0.15
 - Lead pitch: 0.3
 - Lead width: 0.6
 - Lead length: 0.25

Each lead has same dimensions

Part No.	FMG12
Package	SMT5
Marking	G12
Code	T108
Basic ordering unit (pieces)	3000

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CBO}	30	-	-	V	$I_{C0}=50\mu A$
Collector-emitter breakdown voltage	BV_{CEO}	15	-	-	V	$I_{C0}=1mA$
Emitter-base breakdown voltage	BV_{EBO}	5	-	-	V	$I_E=50\mu A$
Collector cutoff current	I_{CBO}	-	-	0.5	μA	$V_{CB}=20V$
Emitter cutoff current	I_{EBO}	-	-	0.5	μA	$V_{EB}=4V$
Collector-emitter saturation voltage	$V_{CE(sat)}$	-	0.04	0.08	V	$I_C/I_E=50mA/2.5mA$
DC current transfer ratio	h_{FE}	100	250	600	-	$V_{CE}=5V, I_C=50mA$
Transition frequency	f_T	-	200	-	MHz	$V_{CE}=10V, I_E=50mA, f=100MHz$
Output ON resistance	R_{on}	-	0.55	-	Ω	$V_I=7V, R_L=1k\Omega, f=1kHz$
Input resistance	R_i	1.54	2.2	2.86	k Ω	-

*1 Measured using pulse current *2 Transition frequency of mounted transistor