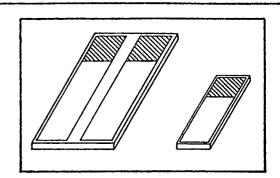


# SILICON PHOTOVOLTAIC CELLS

## **FEATURES**

- Fast response
- High reliability, long life
- Protective, humidity resistant coating
- Operating Temperature Range: −65°C to 125°C
- Ideal for moderate reverse bias voltage operation
- Short circuit current linear over wide ranges of illumination
- Low capacitance, high speed 800 material or high open circuit voltage, 700 material.



# **GENERAL DESCRIPTION**

The Silonex series of open silicon photovoltaic cells feature low cost, high reliability and linear short circuit current over wide ranges of illumination. These cells are widely used for light sensing and power generation because of their stability and high efficiency. They are particularly suited to power conversion applications due to their low internal impedance, relatively high shunt impedance, stability and humidity resistant characteristics. Open silicon cells also provide a reliable, inexpensive detector for instrumentation and light beam sensing applications. Silonex cells are generally of N on P construction but reverse polarity P on N devices can also be provided.

## CHARACTERISTICS PER CELL'

SILONEX PART NO.	SHORT CIRCUIT CURRENT (µA) 0 H 500 FTC* TYP.	OPEN CIRCUIT VOLTAGE (V) @ H 500 FTC* TYP.	DARK CURRENT (µA) @ V <sub>B</sub> 1V MAX.	CAPACITANCE (ZERO BIAS) (PF) TYP.	REVERSE BLAS (V) MAX.	ACTIVE AREA (cm²)	CURRENT	MAT'L TYPE
NSL-6134	180	.42	3	1600	5	.052	30	700
NSL-6135	200	.38	2	200	10	.052	20	800
NSL-6136	115	.40	3	820	5	.026	25	700
NSL-6122	130	.35	2	110	10	.026	20	800

\*The light source is Tungsten Lamp at a color temperature of 2870 K All characteristics are for 25°C free air unless otherwise noted.

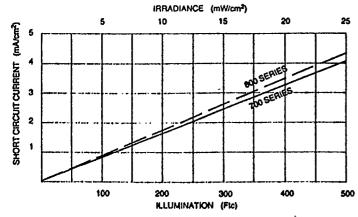


Fig. 1: SHORT CIRCUIT CURRENT CHARACTERISTICS The short circuit current is extremely linear over wide ranges of illumination,

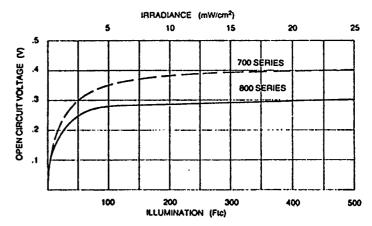
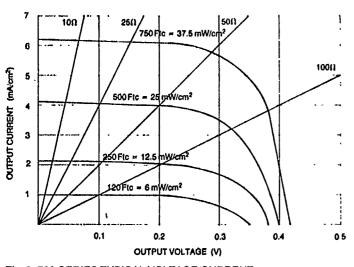


Fig. 2: OPEN CIRCUIT VOLTAGE CHARACTERISTICS Open circuit voltage is generally independent of active area and varies logarithmically with linear variations of illumination.



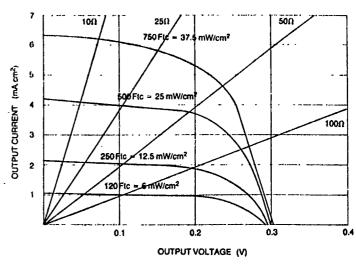
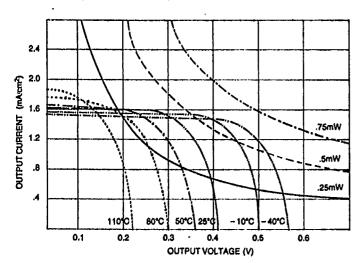


Fig. 3: 700 SERIES TYPICAL VOLTAGE/CURRENT CHARACTERISTICS

FIG. 4: 800 SERIES TYPICAL VOLTAGE/CURRENT CHARACTERISTICS

The output current developed into a load is a function of illumination level, the load resistance and photosensitive active area.



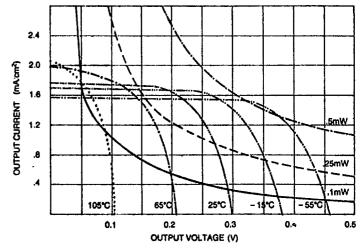


Fig. 5: EFFECTS OF TEMPERATURE OF 700 SERIES I - V Characteristic: 200 Ftc (10 mW/cm²)

Fig. 6: EFFECTS OF TEMPERATURE OF 800 SERIES I - V Characteristic: 200 Ftc (10 mW/cm²)

Optimum power transfer is obtained with a load impedance which results in a voltage and current combination that yields maximum power output. Output current for maximum power and short circuit current normally remain constant over the operating temperature range of the cell.

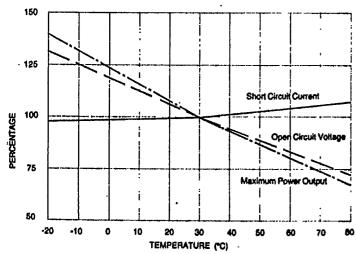


Fig. 7: TYPICAL OUTPUT VARIATIONS OF 700 SERIES @ 200 Ftc (10 mW/cm²)

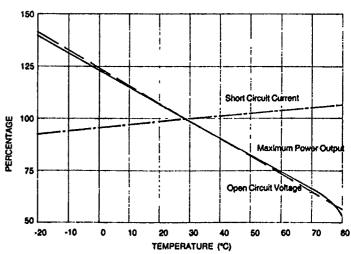


Fig. 8: TYPICAL OUTPUT VARIATIONS OF 800 SERIES @ 200 Ftc (10 mW/cm²)

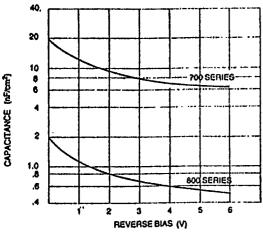


Fig. 9: CAPACITANCE AS FUNCTION OF REVERSE BIAS

Junction capacitance, which is constant per unit area of
the cell at a given reverse voltage, has a major effect on the
cell response time. Response time depends on load
resistance, illumination level as well as junction capacitance. Best control over response time can be achieved

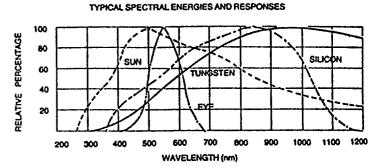
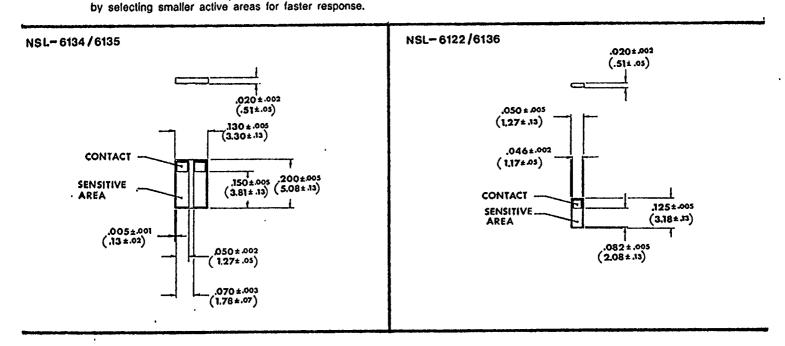


Fig. 10: TYPICAL SPECTRAL ENERGIES & RESPONSES

\_\_\_\_\_\_HUMAN EYE ...... SUNLIGHT \_\_\_\_\_\_ SILICON



#### NOTES:

All characteristics measured at 25°C free air unless otherwise noted.

Output Current Scale Factor. This parameter can be used to calculate the short circuit current of the various devices under illumination levels other than 500 Ftc and 25°C. For example, the short circuit current for the NSL-701 at 250 Ftc illumination and room temperature can be calculated by obtaining the short circuit current per unit area of 2.05 mA/cm² for the 700 series at 250 Ftc (Fig. 1) and dividing by the 701 scale factor of 10.5, giving typical output of 0.195 mA.

This scale factor could also be used for calculating typical capacitances at various reverse voltages by using the data from Fig. 9 in a similar manner. Please note that capacitance calculated in this manner would only be first order approximation as contact areas must also be considered.

The cells' front and back electrodes are solder-coated. Electrodes may be soldered using 60/40 tin-lead solder with an active flux capable of making a solder joint quickly to minimize heat, as excessive soldering temperature can cause contact damage. Whenever possible, it is recommended that all soldering be done by Silonex since it is possible to degrade the cells' characteristics with improper handling.