TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

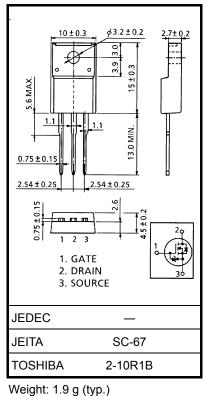
2SK3310

Switching Regulator Applications

- Low drain-source ON resistance: R_{DS (ON)} = 0.48 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 4.3 S (typ.)
- Low leakage current: I_{DSS} = 100 μ A (max) (V_{DS} = 450 V)
- Enhancement model: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|--|----------------|------------------|------------|------|
| Drain-source voltage | | V _{DSS} | 450 | V |
| Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | V _{DGR} | 450 | V |
| Gate-source voltage | | V _{GSS} | ±30 | V |
| Drain current | DC (Note 1) | I _D | 10 | А |
| Drain current | Pulse (Note 1) | I _{DP} | 40 | А |
| Drain power dissipation (Tc = 25° C) | | PD | 40 | W |
| Single pulse avalanche energy (Note 2) | | E _{AR} | 222 | mJ |
| Avalanche current | | I _{AR} | 10 | А |
| Repetitive avalanche energy (Note 3) | | E _{AR} | 4 | mJ |
| Channel temperature | | T _{ch} | 150 | °C |
| Storage temperature range | | T _{stg} | –55 to 150 | °C |



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|--|------------------------|-------|------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 3.125 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 62.5 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 3.7 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Unit: mm

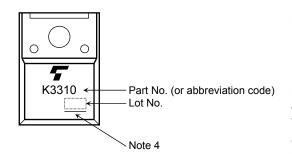
Electrical Characteristics (Ta = 25°C)

| Chara | cteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|------------------------------|---------------|----------------------|---|-----|------|------|------|
| Gate leakage curr | ent | I _{GSS} | $V_{GS}=\pm 25~V,~V_{DS}=0~V$ | | | ±10 | μA |
| Gate -source brea | kdown voltage | V (BR) GSS | $I_G=\pm 10~\mu\text{A},~V_{DS}=0~\text{V}$ | ±30 | | _ | V |
| Drain cut-off curre | nt | I _{DSS} | $V_{DS} = 450 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | _ | | 100 | μA |
| Drain-source brea | kdown voltage | V (BR) DSS | $I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$ | 450 | | _ | V |
| Gate threshold vo | Itage | V _{th} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$ | 3.0 | | 5.0 | V |
| Drain-source ON r | resistance | R _{DS (ON)} | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$ | | 0.48 | 0.65 | Ω |
| Forward transfer a | admittance | Y _{fs} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$ | 1.5 | 4.3 | | S |
| Input capacitance | | C _{iss} | | | 920 | | |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ | | 12 | | pF |
| Output capacitance | | C _{oss} | | | 140 | _ | |
| Switching time | Rise time | tr | $V_{GS}^{10 \text{ V}} \downarrow_{DD} = 5 \text{ A} \\ V_{GS}^{0 \text{ V}} \downarrow_{DD} = 5 \text{ A} \\ R_{L} = 40 \Omega \\ V_{DD} \approx 200 \text{ V} $ Duty $\leq 1\%$, $t_{W} = 10 \mu s$ | | 25 | | |
| | Turn-on time | t _{on} | | | 35 | _ | - ns |
| | Fall time | tf | | _ | 10 | _ | |
| | Turn-off time | t _{off} | | _ | 60 | _ | |
| Total gate charge | | Qg | | | 23 | | |
| Gate-source charge | | Q _{gs} | $V_{DD} \simeq 360 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$ | | 9 | | nC |
| Gate-drain charge | | Q _{gd} | | | 14 | | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | — | _ | | 10 | А |
| Pulse drain reverse current (Note 1) | I _{DRP} | — | _ | _ | 40 | А |
| Forward voltage (diode) | V _{DSF} | $I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | — | -1.7 | V |
| Reverse recovery time | t _{rr} | $I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$ | _ | 280 | _ | ns |
| Reverse recovery charge | Q _{rr} | dl _{DR} /dt = 100 A/μs | _ | 2.7 | _ | μC |

Marking

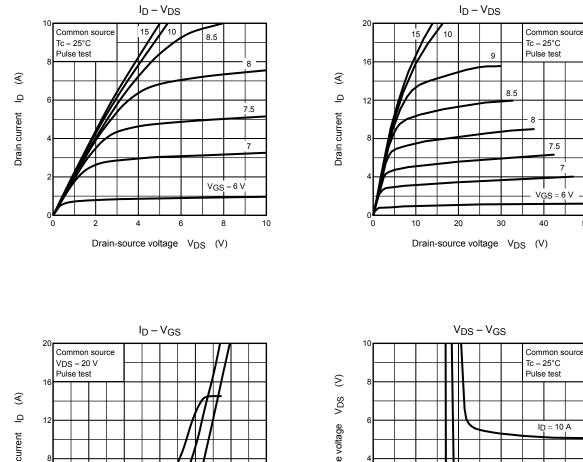


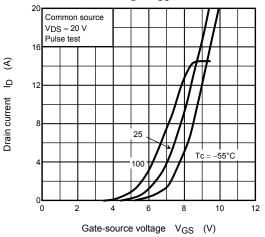
Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] Please contact your TOSHIBA sales representative for details as to

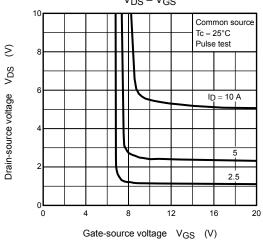
environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

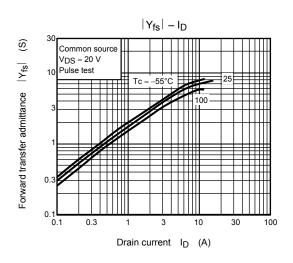
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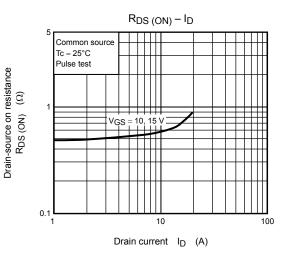
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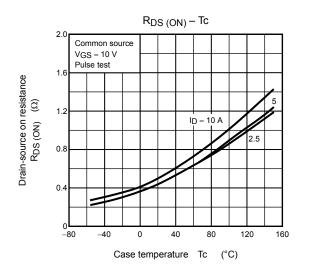


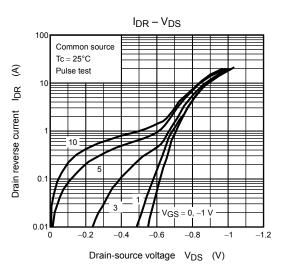


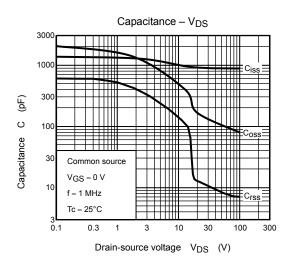


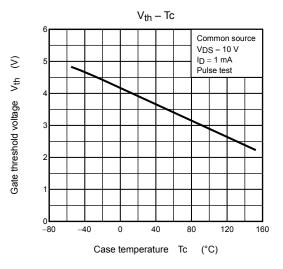


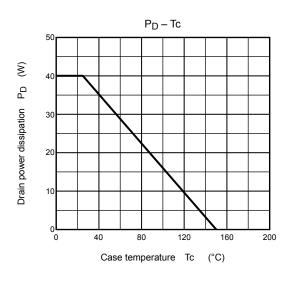
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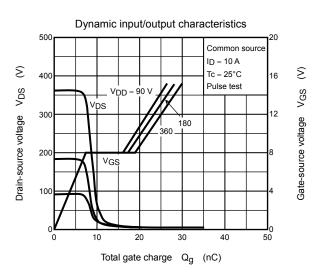






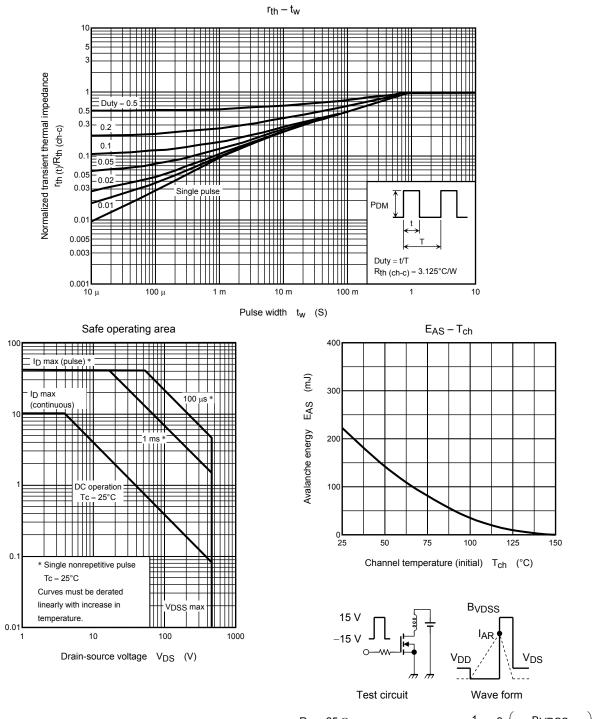






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Drain current ID



| $R_G = 25 \Omega$ | $FAS = \frac{1}{1} \cdot L \cdot l^2$ | $\left(\frac{BVDSS}{BVDSS-VDD}\right)$ | |
|-----------------------------|---------------------------------------|--|--|
| $V_{DD} = 90 V, L = 3.7 mH$ | 2 | BVDSS-VDD | |

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