# mos integrated circuit $\mu PD1704C-023$

# PLL FREQUENCY SYNTHESIZER AND CONTROLLER FOR MOBILE FM AND MW RADIO

The  $\mu$ PD1704C-023 (42-pin DIP) is a CMOS LSI with a built-in PLL frequency synthesizer and controller which can receive FM and MW broadcasts in the U.S.A., Australia, and Japan. It is possible to construct a multifunction, high performance FM/MW digital tuning system for car stereos, home stereos, radio cassettes, etc., by combining this unit with the special prescaler  $\mu$ PB553AC.

#### **FEATURES**

- LED/FIP dynamic display
- Preset memory: FM 6 channels, MW 6 channels
- Read-out and write-in keys are independent (6 keys + 6 keys)
- Auto-tuning (Seek action), manual tuning, plus scan function with 5 second hold
- Control output can be used for LOC/DX, LOUDNESS, METAL, Dolby NR\*, STEREO, AMS, etc.
- AGCCUT signal output to prevent erroneous action during auto-tuning
- Complete leading mute effectuated by band switching signal output from controller side as countermeasure against pop noise
- Realizing feather-touch operation of all switches as well as band switching.
- Check sound output when keys operated (connected to external oscillator)
- Dimmer function
- 12-hour display clock function

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#### **FUNCTION**

#### Receiving frequency, Channel spacing, Reference frequency, Intermediate frequency

	ITEM	RECEIVING	CHANNEL	REFERENCE	INTERMEDIATE
DISTRICT	BAND	FREQUENCY	SPACING	FREQUENCY	FREQUENCY
U.S.A.	FM	87.7 to 107.9 MHz	200 kHz	25 kHz	10.700, 10.725 MHz
	MW	530 to 1620 kHz	10 kHz	10 kHz	450 kHz
	FM	87.7 to 107.9 MHz	100 kHz	25 kHz	107.00, 10.725 MHz
Australia	MW	531 to 1602 kHz	9 kHz	9 kHz	450 kHz
Japan	FM	76.1 to 89.9 MHz	100 kHz	25 kHz	−10.700, −10.675 MHz
	MW	531 to 1602 kHz	9 kHz	9 kHz	450 kHz

#### **Radio Function**

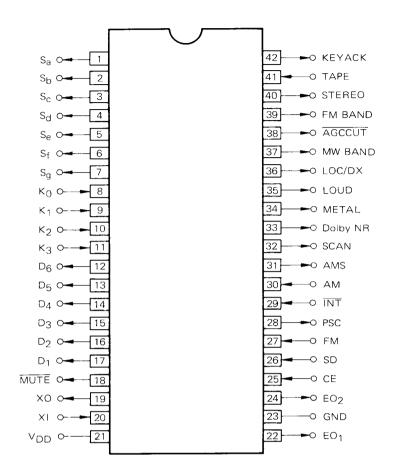
- (1) SEEK UP, SEEK DOWN (Sawtooth wave mode)
- (2) MANUAL UP, MANUAL DOWN (Sawtooth wave mode)
- (3) SCAN (UP direction only)
- (4) Preset memory: FM 6 channels, MW 6 channels
- (5) Last station memory: FM 1 channel, MW 1 channel
- (6) LOC/DX control output
- (7) LOUDNESS control output
- (8) STEREO control output (effective for both FM, MW)
- (9) AGCCUT signal output during auto-tuning
- (10) Preset memory display (dot display)
- (11) Preset memory write-in 6 independent keys (ME1 to ME6)

#### Cassette Tape Function

- (1) METAL control output
- (2) Dolby NR control output
- (3) AMS control output
- (4) LOUDNESS control output

#### **Clock Function**

- (1) 12-hour display (no AM/PM display)
- (2) Hours and minutes can be adjusted independently (HRADJ, MINADJ)



#### PIN DESCRIPTION

PIN NO.	SYMBOL	NAME	DESCRIPTION	OUTPUT TYPE
1 to 7	S <sub>a</sub> to S <sub>g</sub>	Segment Output	Active-high pins for dynamic display segment signal output and key return signal source.	P-ch open drain
8 to 11	K <sub>0</sub> to K <sub>3</sub>	Key-return Signal Input	These are the input pins for the key-return signal from the key matrix. In order to reliably carry out a low-level read, use about a 33 k $\Omega$ pull-down resistor at these pins.	Input
12 to 17	D <sub>6</sub> to D <sub>1</sub>	Digit Output	Active-high pins for dynamic display column signal output. $D_1$ , $D_2$ , and $D_3$ aer also used as the key-return signal source.	CMOS push-pull
18	MUTE	Mute Output	Active-low pin for MUTE output to eliminate pop noise when unlocking PLL and turning TAPE pin ON and OFF.  When the CE pin is at low level (back-up status), low level occurs unconditionally.  For details, refer to MUTE output timing chart.	CMOS push-pull
19 20	XO XI	X'tal	4.5 MHz is used at the connection pins for the quartz oscillator.	Input (XI), CMOS push-pull (XO)
21	V <sub>DD</sub>	Power Supply	$5~V~\pm 10~\%$ is supplied through this device power source pin during operation. When the clock is not being used (NOCLOCK is ON), a voltage drop of up to 2.5 V can occur on back-up. $V_{\mbox{\scriptsize DD}}$ rise time must be less than 500 ms, and if the rising is extremely long the internal power-ON reset circuit may not be activated normally. In addition, malfunction may also occur when the voltage is raised from medium potential though it is required to rise from 0 V.	_
22 24	EO <sub>1</sub> EO <sub>2</sub>	Error Output	These are charge pump outputs for a phase comparator which constructs PLL. The same signal is output simultaneously at EO $_1$ and EO $_2$ , so it is immaterial whether the connection is made to the FM or MW low pass filter (LPF).	CMOS 3-state
23	GND	Ground	Connect to system GND.	_
25	CE	Chip Enable	This is a device selection sigal input pin.  Normally high level when device is operated; low level when device is not being used.  High level Normal operation  Low level (With clock; when NOCLOCK = OFF)  Memory maintenance, display OFF, PLL action halted, only clock function operating internally.  (No clock; when NOCLOCK = ON)  Memory maintenance, display OFF, all internal operations halted, 10 µA MAX. back-up current at that time.  However, high level below 134 µs and low level are not received. Also, when rising, always rise from 0 V. From the medium level, it is possible for the internal reset circuit to be incorrectly activated.	Input

PIN NO.	SYMBOL	NAME			OUTPUT TYPE			
26	SD	Station detector	sta SC	tive-high input pin fo tion is received during AN). gh level voltage must be				
27	FM	FM VCO Signal Input	pre	out pin for Local FM conscaler $\mu$ PB553AC. Cunce AC amplifier is inco				
28	PSC	Pulse swallowing control		is is a pulse swallow eical prescaler μPB553A			PSC pin fo	CMOS push-pull
29	ÎNT	Interrupt		is is an external interrued, apply a pull-up at V		owever, be	cause it is no	Input
30	АМ	AM VCO Signal Input		outs a local MW outp			current with	Input
			an	tional function output d the effective output shown in the following TAPE Input Output	obtained vary	according		
				LOUD	0	0	0	
31	AMS	AMS		LOC/DX	×	0	0	
33 34	Dolby NR METAL	Dolby NR METAL		STEREO	X	0	0	CMOS
35	LOUD	LOUD		METAL	0	×	×	push-pull (I/O)
36 40	LOC/DX STEREO	LOC/DX STEREO		Dolby NR	0	×	×	
40	STEREO	STEREO		AMS	0	×	X	
			A B	<ul> <li>: possible key input</li> <li>X : key input ignor</li> <li>riginal output is recove</li> <li>iso, at the time of powe</li> <li>ecause this optional function according to the</li> <li>pove titles can be used.</li> </ul>	h			
32	SCAN	SCAN signal	D	uring SCAN action only	y, high-level outp	out occurs	so can be use	d CMOS push-pull
37	MW BAND	MW control	lo	W tuner control outpow-level (radio mode) at an this, signal is low-lev	t the TAPE pin a			

PIN NO.	SYMBOL	NAME	DESCRIPTION	OUTPUT TYPE
38	AGCCÚT	AGCCUT signal output	Only when SEEK UP, SEEK DWN, and SCAN keys are depressed (during auto-tuning), the AGCCUT signal indicated below is output (active-low). As a result, STOP becomes more reliable during auto-tuning.  MUTE output  SEEK UP, SEEK DWN, or SCAN key  AGCCUT output  SD=Low Low Low Low Low High 250 to 375 ms  This signal can also be used as the auto-tuning indicator output.	CMOS push-pull (I/O)
39	FM BAND	FM control	FM tuner control output pin. High-level is output only when low-level (radio mode) at TAPE pin and FM selection. Other than this, signal is low-level.	CMOS push-pull (I/O)
41	TAPE	Tape mode	This input pin is for setting tape mode (high-level) or radio mode (low-level).	CMOS push-pull (I/O)
42	KEYACK	Key input	Only when the controller receives key input, high-level is output for about 40 ms. It is possible to get a confirming sound during key input by connecting to an external generator.	CMOS push-pull (I/O)

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#### 1. KEY MATRIX

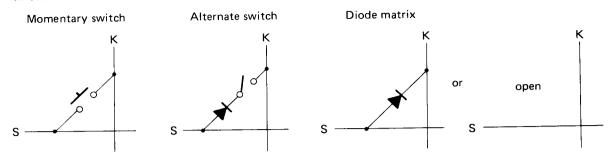
#### 1.1 CONFIGURATION OF KEY MATRIX

Input	K <sub>3</sub> (11)	K <sub>2</sub> (10)	K <sub>1</sub> (9)	K <sub>0</sub> (8)
S <sub>a</sub> (1)	MAN UP	MAN DWN	HRADJ	MINADJ
S <sub>b</sub> (2)	SEEK UP	SEEK DWN	SCAN	DISP
S <sub>c</sub> (3)	M1	M2	М3	M4
S <sub>d</sub> (4)	M5	M6	ME5	ME6
S <sub>e</sub> (5)	ME1	ME2	ME3	ME4
S <sub>f</sub> (6)	LDC/DX	LOUD	METAL	Dolby NR
S <sub>g</sub> (7)	ST	AMS	BAND	_
D <sub>1</sub> (17)		_	DIM	PRIO
D <sub>2</sub> (16)	FMSEL1	FMSEL2	MWSEL	NOCLOCK
D <sub>3</sub> (15)	<b>IF</b>	M/R	BLANK	_

( ): Pin No.

į	Momentary switch
	Alternate switch
	Diode matrix
_	Open

#### 1.2 SWITCH CONNECTION



**Note:** The signal from the diode switch is read only when power is first fed to V<sub>DD</sub>, or when voltage at the CE pin goes from low to high. In other cases, it is ignored. Momentary and Alternate switches can be changed at anytime.

#### 1.3 DESCRIPTION OF KEY MATRIX

#### 1.3.1 Initialization Diode Switch

There are a total of seven initialization switches. Their signals are read only when power is first fed to the  $V_{DD}$  pin (Power-ON Reset), and when voltage at the CE pin changes from low to high (CE reset). At any other time the status of these switches is ignored.

The switch setting is determined by whether the diode at the matrix nodal point is closed (ON) or open (OFF).

(1) Area setting switches for FM band

FMSEL1, FMSEL2

(2) Channel spacing setting switch for MW band

MWSEL

(3) Switch for setting whether or not the clock will be used.

**NOCLOCK** 

(4) Switch for setting the FM IF

ΙF

(5) Selection switch that either the momentary switch or the rotary switch to be used as the manual UP/DOWN key.

(6) Blank display setting switch (PRIO = OFF, TAPE input pin = High)

BLANK

SYMBOL				DESCR	IPTION							
	These switches are for setting the FM band for the particular territory.											
	FMSEL1	FMSEL1 FMSEL2		rea	Frequency Range	Channel Spacing						
	0	0	U.S.	Α.	87.7 to 107.9 MHz	200 kHz						
FMSEL1	1	0	Japa	n	76.1 to 89.9 MHz	100 kHz						
FMSEL2	0	1	Aust	tralia	87.7 to 107.9 MHz	100 kHz						
	* 1	1				_						
	* Do not tu	ırn both FM	SEL1 and	FMSEL2	ON simultaneously	v. The territory cannot be						
	properly s	et.										
	This switch is	for setting th	ne MW ban	d channel s	spacing.							
MWSEL	MWSEL	Frequency	y Range	Channel S	pacing							
MWSEL	0	530 to 1 6	320 kHz	10 k⊦	łz							
	1	531 to 1 6	602 kHz	9 k⊦	łz							
	This switch is	for setting w	hether or	not the clo	ck will be used.							
	NOCLOCK Clock Function		nction									
	0	0 Present										
NOCLOCK	1	Abse	nt									
	When the clo	When the clock is used (NOCLOCK = OFF), the setting of the display switching mode is										
	carried out by	carried out by a combination of the PRIO and BLANK switches.										
	This switch is	for setting th	ne FM IF.									
				(M)	⊣z)							
IF	IF	U.S.A.	Japan	Austra	alia							
	0	10.700	-10.700	10.70	00							
	1	10.725	-10.675	10.72	5							
	This switch is	used to sele	ct either t	he momen	tary switch or the r	rotary switch to be used as						
	the manual U	P/DOWN key	<i>'</i> .									
M/R	M/R	Manual UP/0	OOWN									
	0	Rotary Swite	ch									
	1	Momentary S	Switch									
	This switch is used to set whether the display is blank or shows the clock when the tape is											
	loaded (TAPE input pin = high), when not in clock priority display mode (PRIO =											
	OFF).											
	When PRIO =	OFF and TA	APE input	= high								
DLANK	BLANK	Display	y status									
BLANK	0	Clock Dis	splay									
	1	Display C	OFF (blank)									
	When in clos	k priority d	isplay mod	de (PRIO =	ON) and when t	the tape is loaded (TAPE						
	input pin =	high), the clo	ock is disp	played unce	onditionally, regar	dless of the status of the						
	BLANK switch.											

#### 1.3.2 Alternate Switch

There are two alternate switches — DIM and PRIO. Unlike the initializing switches, it is always possible to change these switches.

SYMBOL	DESCRIPTION							
	This switch changes the brightness of the display							
DIM	DIM	Display Brightness						
	0	Bright						
	1	Dark						
	This switch is	s used to set the display	priority mod					
	PRIO	Display Mode						
PRIO	0	Frequency display pri	ority					
	1	Clock display priority	1					

### 1.3.3 Momentary Switch

There are a total of 27 momentary switches.

SYMBOL		<u> </u>			DESCRIP	TION					
	selected b	is used for by the diode ry Switch (N	switch N	1/R used t		·	witch and	the rota	ary switch can be		
MAN UP MAN DWN	tinuou is relea Rotary Sv The fr	sly depresse sed (sawtoo witch (M/R	ed for mo oth wave ( = OFF) oves one	re than 0 mode). Ti	.5 second he speed or DOWN	ds, fast fo of the fas for each	rwarding st forward pulse. A	is perfor ding is 40 pulse of	r DOWN. If conmed until the key of ms per one step.  Tup to about 4 ms cur.		
SEEK UP	for the st the SD pi The auto-	This key is used for auto-tuning. In the sawtooth wave mode, a continuous search is made for the station in the UP or DOWN direction until the SD pin reaches high level. When the SD pin once reaches the high level, that frequency is maintained.  The auto-tuning action can be halted by depressing this key twice, regardless of the status of the SD pin. The auto-tuning speed is about 40 ms per one step.									
SCAN	the SD pin. The auto-tuning speed is about 40 ms per one step.  This key is used for auto-scanning. A continuous search is made for the station in the UP direction in the sawtooth wave mode until the SD pin reaches the high level. With the SD pin at the high level frequency, there is about a 5 second HOLD, then the search continues for the next station.  This action continues until any key is operated, with the exception of SCAN and ME1 to ME6. When the SCAN key is once again depressed, the auto-SCAN action halts. Also, during about 5 seconds of HOLD, ME1 to ME6 are activated to write in memory without interrupting auto-SCAN action. It is then possible to write into memory. In this case, after the M1 to M6 keys are released and after that frequency is held for about 5 seconds, the auto-SCAN, action resumes. During SCAN action outside of the HOLD period, the ME1 to ME6 keys are ineffective.										
M1	ly, and eastations.		in contai	n 6 stati	ons with	different	frequen	cies, mal	MW independent- king a total of 12 recorded.		
M2	BAND	AREA	M1	M2	МЗ	M4	M5	M6			
M3		U.S.A.	87.7	88.1	98.1	107.9	87.7	87.7			
M4	FM	Australia	87.7	88.1	98.1	107.9	87.7	87.7			
M5	(MHz)	Japan	76.1	76.1	83.0	89.9	76.1	76.1			
M6	MW	U.S.A.	530	600	1 000	1 400	1 602	530			
	(kHz)	Australia	531	594	954	1 314	1 602	531			
		Japan	531	594	954	1 314	1 620	531			

SYMBOL			DESCRI	PTION					
ME1									
ME2	This key is used for	writing in to p	reset mem	nory. On	e key corresponds to FM and MW				
ME3	indenpendently, and	each band can c	ontain 6	stations v	with different frequencies, making a				
ME4	total of 12 stations.	Call key N	11	correspo	nds to write-in key ME1				
ME5	There are respectively	6 independent	write-in ke	eys.					
ME6									
	These keys are used	to switch bands	s. They sv	witch the	e display and power source control				
BAND					ressed, the FM and MW bands are				
	switched. FM is selec								
DISP	This key is used to sw	itch the frequen	cy and clo	ck displa	ays.				
	These keys are used	to adjust the time	e while th	e clock is	being displayed. Hours and minutes				
LIDAD	can be adjusted inde	pendently. Each	time the	MINA	DJ key is pressed the figure in the				
HRADJ	minutes column moves up once, and at the same time the internal second counter is reset.								
MINADJ	Carry to the hours column does not occur. Each time the HRADJ key is pressed the								
	hours column moves up. This is a 12 hour display clock. AM and PM are not differentiated.								
	These are the option function keys. Each time a key is depressed the corresponding output								
	pin voltage reverses. When first powered on these output pins are all at the low								
	level.  As shown in the following table, the key which is effective in each mode (and the output)								
		owing table, the	key whic	h is ette	ctive in each mode (and the output)				
	varies.				1				
LOUD	Key and Output	TAPE input		put Low					
LOC/DX		High	FM	MW					
ST	LOUD	0	0						
METAL	LOC/DX ST	X							
Dolby NR	METAL	0	+ ×	×					
AMS	Dolby NR	0	×	×	-				
	AMS	0	×	X					
		ossible, output also	effective		ı				
	,	nored, output low							
	Because this accessor	y function only	has a cond	litional la	atch function according to the mode,				
objects other than the titles shown above can be used.									

#### 1.4 INITIALIZATION

- 1. The following status results when power is first fed to  $V_{DD}$ .
  - (1) The frequencies for tracking adjustment, represented in keys M1 to M6 are stored in preset memory. The lowest frequency for the band is stored in the last station memory.
  - (2) In TAPE eject status (TAPE input pin = low), FM mode results (FM output pin = high), and the contents of the FM last station memory, that is, the lowest FM frequency, are accessed.
  - (3) The output corresponding to the option key is always low-level.

				1					г	i	Γ	,
1	LOUD		LOC/DX		ST		METAL	١.	Dolby NR		AMS	1
٠,		,		ı <i>'</i>	_	,		١,		,	1	: 1

- (4) The clock setting is 1:00.
- 2. Without cutting power to VDD, when only the CE pin goes from low to high, the following status occurs.
  - (1) The contents of preset memory, last station memory, last preset memory, and last band memory are maintained. The contents of the last station memory for the last band received are accessed. In the case where the contents of finally preset memory is received, its indicator is also returned.
  - (2) The option key status is maintained and the output reverts to the previous status.

(	LOUD	] .	LOC/DX	١.	ST		METAL		Dolby NR		AMS	,
١,		'	200,211	<b>'</b>		′ ′	· · · · · · · · · · · · · · · · · · ·	ı ,		, ·		

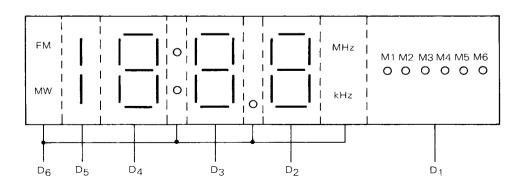
#### 2. DISPLAY

	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>
Sa	FM		a	а	а	M1
S <sub>b</sub>	D.P. MHz	b	b	b	b	M2
S <sub>c</sub>	_	С	С	С	С	МЗ
S <sub>d</sub>		_	d	d	d	M4
S <sub>e</sub>	MW	_	е	е	е	M5
S <sub>f</sub>	COLON	_	f	f	f	М6
Sg	kHz	_	g	g	g	-

Frequency and clock display Preset memory display

Lowermost

column



Uppermost

column

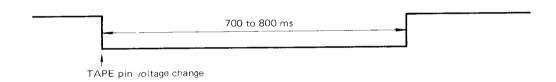
#### 3. TIMING CHARTS

MUTE Output (Active-Low) Timing Chart

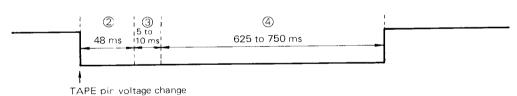
- (1) Key ON chattering prevention time (about 15 ms)
- (2) Mute leading time
- 3 Setting dividing ratio and display contents renewal time
- 4 Mute trailing time
- (5) Wait time until SD terminal signal detected
- Wait time until PLL locked (changes according to the constant of externally connected LPF)
- (1) Receiving band change (FM, MW)



- (2) TAPE pin voltage change
  - (i) Low (RADIO) → High (TAPE)



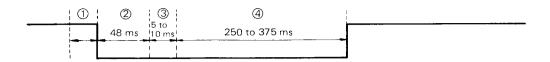
(ii) High (TAPE) → Low (RADIO)



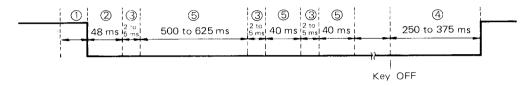
(3) Preset memory access



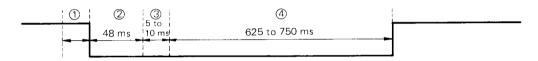
- (4) MAN UP, MAN DWN key depressed
  - (i) Key ON less than 500 ms (step feed)



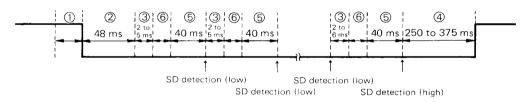
(ii) Key ON more than 500 ms (fast forwarding)



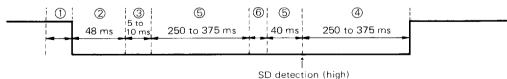
(iii) Band edge (upper limit → lower limit, lower limit → upper limit)

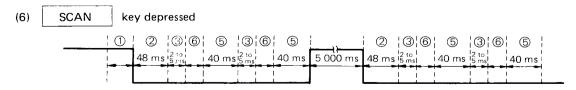


- (5) SEEK UP , SEEK DWN key depressed
  - (i) Normal

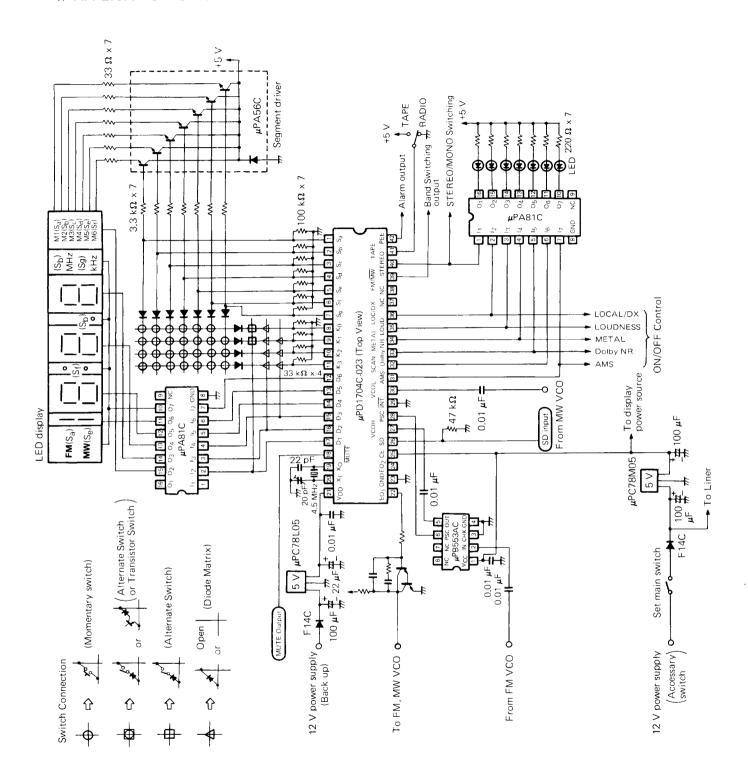


(ii) Band edge (upper limit → lower limit, lower limit → upper limit)





#### 4. APPLICATION CIRCUIT



#### 5. ELECTRICAL CHARACTERISTICS

#### **5.1 ABSOLUTE MAXIMUM RATINGS**

Supply Voltage	$V_{DD}$	-0.3 to $+6.0$	V
Input Voltage	$V_{I}$	$-0.3$ to $V_{ m DD}$	V
Output Voltage	$V_{O}$	$-0.3$ to $V_{\mbox{\scriptsize DD}}$	V
Output Sink Current	Io	10	mΑ
Operating Temperature	$T_{opt}$	-35 to +75	°C
Storage Temperature	$T_{stg}$	-55 to +125	°C

#### **5.2 RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
Supply Voltage	VDD	4.5	5.0	5.5	V	
Data Retention Voltage	VDR	2.5		5.5	V	CE=0, NOCLOCK = ON
Oscillation Stop Voltage	V <sub>DDS</sub>		3.2	3.8	٧	
Supply Voltage Rise Time	T <sub>rise</sub>			500	ms	V <sub>DD</sub> =0 → 4.5 V

5.3 DC CHARACTERISTICS (V<sub>DD</sub> = +4.5 to 5.5 V,  $T_a$  = -35 to +75 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
High Level Input Voltage	V <sub>IH1</sub>	0.8 V <sub>DD</sub>		V <sub>DD</sub>	V	SD pin
High Level Input Voltage	V <sub>IH2</sub>	0.7 V <sub>DD</sub>		V <sub>DD</sub>	V	I/O port, CE pin (Note)
High Level Input Voltage	V <sub>IH3</sub>	0.6 V <sub>DD</sub>		V <sub>DD</sub>	V	K <sub>0</sub> to K <sub>3</sub> pins
Low Level Input Voltage	V <sub>IL1</sub>	0		0.3 V <sub>DD</sub>	V	I/O port, CE pin (Note)
Low Level Input voltage	V <sub>IL2</sub>	0		0.2 V <sub>DD</sub>	V	K <sub>0</sub> to K <sub>3</sub> , SD pins
High Level Output Voltage	V <sub>OH1</sub>	4.0			V	EO <sub>1</sub> , EO <sub>2</sub> pins IOH=-0.5 mA
High Level Output Voltage	V <sub>OH2</sub>	4.0			V	D <sub>1</sub> to D <sub>6</sub> , MUTE, I/O port (Note)
High Level Output Voltage	V <sub>OH3</sub>	4.0			V	PSC pin I <sub>OH</sub> =-0.1 mA
High Level Output Voltage	V <sub>OH4</sub>	3.0			V	Sa to Sg pins 10H=-0.5 mA
Low Level Output Voltage	V <sub>OL1</sub>			0.5	٧	EO <sub>1</sub> , EO <sub>2</sub> pins, I/O port (Note) I <sub>OL</sub> =0.5 mA
Low Level Output Voltage	V <sub>OL2</sub>			0.5	٧	D <sub>1</sub> to D <sub>6</sub> , MUTE, PSC pins
High Level Input Current	+1 <sub>1H1</sub>	10	40	100	μА	K <sub>0</sub> to K <sub>3</sub> pins V <sub>IN</sub> =V <sub>DD</sub> =5.5 V
High Level Input Current	<sup>+1</sup> IH2		300		μΑ	XI pin (when Pull Down) V <sub>IN</sub> ≃V <sub>DD</sub> =5.0 V
Low Level Input Current	-IIL1		300		μΑ	AM, FM pins (when Pull Down)  V <sub>IN</sub> =0, V <sub>DD</sub> =5.0 V
Output Leakage Current	ار		10-3	1	μА	EO <sub>1</sub> , EO <sub>2</sub> pin, T <sub>a</sub> =25 °C V <sub>p-p</sub> (MIN.
Supply Current	I <sub>DD1</sub>		3		mA	Without input output current from input output pin
Supply Current	I <sub>DD2</sub>		0.6		mA	CE=0 at NOCLOCK = OFF  VDD=5.0 \
Data Retention Current	IDR			10	μΑ	CE=0 at NOCLOCK = ON, $T_a=25$ °C, $V_{DD}=5.0$ V

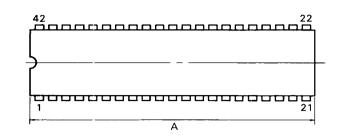
(Note) I/O ports means the KEYACK, TAPE, STEREO, FM BAND, AGCCUT, MW BAND, LOC/DX, LOUD, METAL, Dolby NR, SCAN, AMS pins.

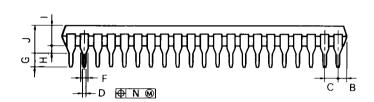
## 5.4 AC CHARACTERISTICS (V<sub>DD</sub> = +4.5 to 5.5 V, $T_a$ = -35 to +75 °C)

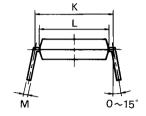
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
Operating Frequency	fin1	0.5		2.5	MHz	AM pin, $V_{in}$ =1.0 $V_{p-p}$ (MIN.), DC cut
Operating Frequency	f <sub>in2</sub>	0.5		8.8	MHz	FM pin, V <sub>in</sub> =0.8 V <sub>p-p(MIN.)</sub> , square wave form, DC cut

#### 6. PACKAGE DIMENSIONS (Unit: mm)

42 pin plastic DIP (600 mil)







P42C-100-600A,B

#### NOTES

- Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS	INCHES
Α	55.88 MAX.	2.200 MAX.
В	2.54 MAX.	0.100 MAX.
С	2.54 (T.P.)	0.100 (T.P.)
D	0.50 ±0.10	0.020 - 0.004
F	1.2 MIN.	0.047 MIN.
G	3.6 ±0.3	0.142 ±0.012
н	0.51 MIN.	0.020 MIN.
1	4.31 MAX.	0.170 MAX.
J	5.72 MAX.	0.226 MAX.
К	15.24 (T.P.)	0.600 (T.P.)
L	13.2	0.520
М	0.25-0.05	0.010 -0.003
N	0.25	0.01