TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (π-MOSV)

2SJ439

DC-DC Converter, Relay Drive and Motor Drive Applications

• 2.5 V gate drive

• Low drain–source ON resistance : R_{DS} (ON) = 0.18 Ω (typ.) • High forward transfer admittance : $|Y_{fs}| = 6.0 \text{ S}$ (typ.) • Low leakage current : $I_{DSS} = -100 \mu A$ (max) ($V_{DS} = -16 V$)

• Enhancement-mode : $V_{th} = -0.5 \sim -1.1 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-16	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	-16	V	
Gate-source voltage		V _{GSS}	±8	V	
Drain current	DC (Note 1)	I _D	- 5	Α	
	Pulse(Note 1)	I_{DP}	-20	A	
Drain power dissipation (Tc = 25°C)		P_{D}	20	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Thermal Characteristics

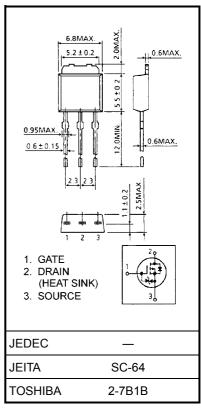
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

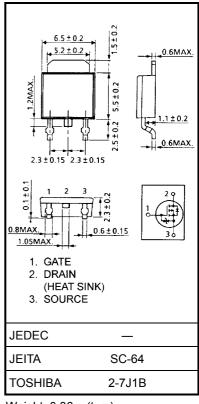
This transistor is an electrostatic sensitive device.

Please handle with caution.

Unit: mm



Weight: 0.36 g (typ.)



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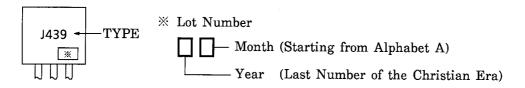
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±6.5 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = -16 V, V _{GS} = 0 V	_	_	-100	μΑ
Drain-source br	reakdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-16	_	_	V
Gate threshold v	voltage	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.5	_	-1.1	V
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = -2.5 \text{ V}, I_D = -2.5 \text{ A}$	_	0.18	0.28	Ω
			$V_{GS} = -4 \text{ V}, I_D = -2.5 \text{ A}$	_	0.14	0.2	11
Forward transfe	r admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	3.0	6.0	_	S
Input capacitano	ce	C _{iss}		_	1050	_	
Reverse transfer capacitance		C _{rss}	SS V _{DS} = −10 V, V _{GS} = 0 V, f = 1 MHz	_	120	_	pF
Output capacitance		Coss		_	460	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{OV}{\longrightarrow} I_{D} = -2.5A$ $V_{OUT} \stackrel{V}{\longrightarrow} R_{L} = 3.2\Omega$ $V_{DD} = -8V$	_	80	_	ns
	Turn-on time	t _{on}		_	100	_	
	Fall time	t _f		1	250		
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$	-	550	_	
Total gate charge (Gate-source plus gate-drain)		Qg			24	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \approx -16 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -5 \text{ A}$	_	16	_	
Gate-drain ("miller") charge		Q_{gd}]		8	_	

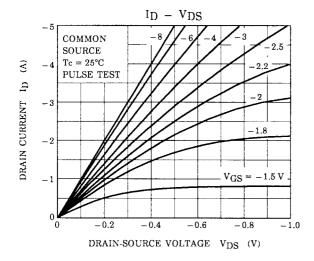
Source-Drain Ratings and Characteristics (Ta = 25°C)

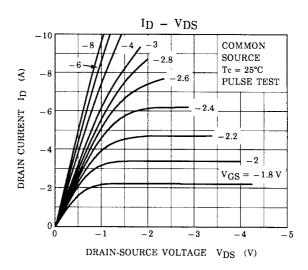
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	-5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = -5 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -5 A, V _{GS} = 0 V,dI _{DR} / dt = 50 A / μs	1	120		ns
Reverse recovery charge	Qrr	1DR - 3 Λ, VGS - 0 V, αιDR / αι - 30 Α / μs		0.12	_	μC

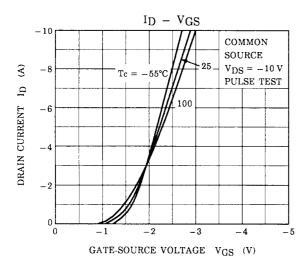
Marking

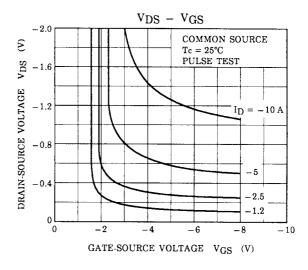


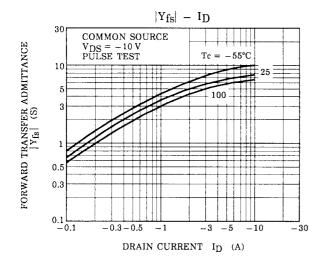
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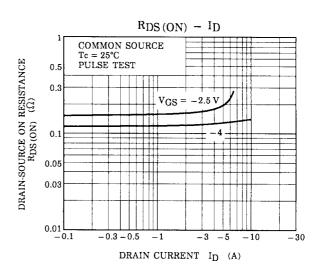


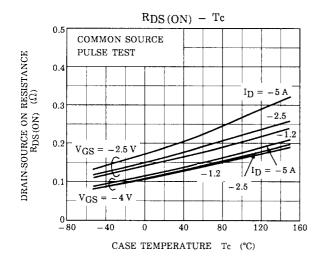


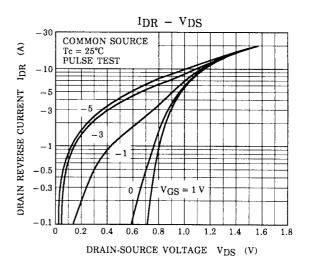


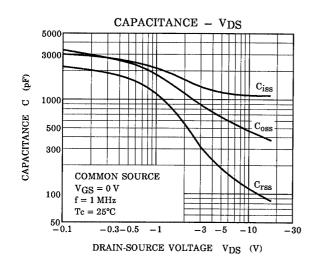


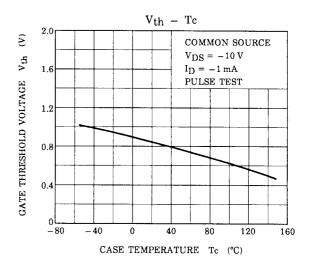


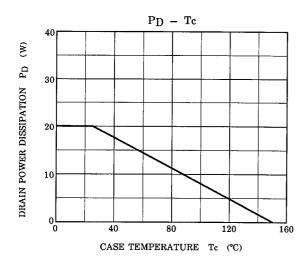




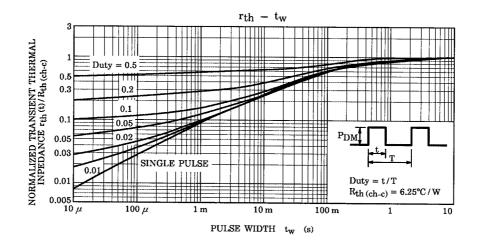




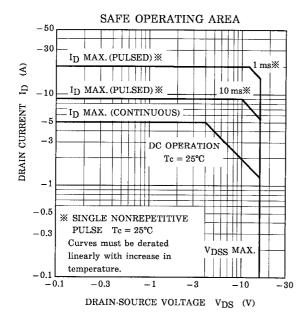




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