

**PRELIMINARY**

Notice: This is not a final specification.  
Some parametric limits are subject to change.

MITSUBISHI SEMICONDUCTOR <GaAs FET>

# MGFC39V7785A

**7.7~8.5GHz BAND 8W INTERNALLY MATCHED GaAs FET**

## DESCRIPTION

The MGFC39V7785A is an internally impedance-matched GaAs power FET especially designed for use in 7.7~8.5 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

## FEATURES

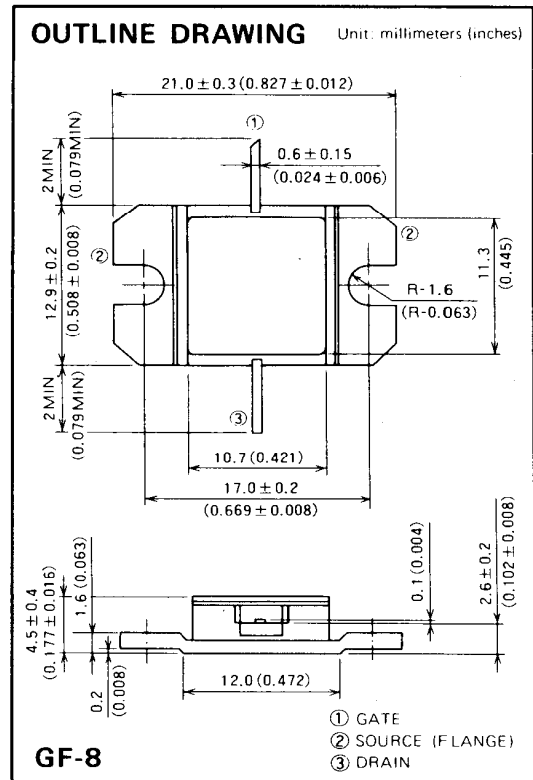
- Class A operation
- Internally matched to 50Ω system
- High output power  
 $P_{1dB} = 8W$  (TYP) @ 7.7~8.5 GHz
- High power gain  
 $G_{LP} = 7$  dB (TYP) @ 7.7~8.5GHz
- High power added efficiency  
 $\eta_{add} = 27\%$  (TYP) @ 7.7~8.5GHz,  $P_{1dB}$
- Hermetically sealed metal-ceramic package
- Low distortion [Item: -51]  
 $IM_3 = -45$  dBc (TYP) @  $P_o = 28$  (dBm) S.C.L.

## APPLICATION

- Item-01: 7.7~8.5GHz band power amplifier
- Item-51: Digital radio communication

## QUALITY GRADE

- IG



## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Symbol	Parameter	Ratings	Unit
V <sub>GD0</sub>	Gate to drain voltage	-15	V
V <sub>GSO</sub>	Gate to source voltage	-15	V
I <sub>D</sub>	Drain current	7.5	A
I <sub>GR</sub>	Reverse gate current	-20	mA
I <sub>GF</sub>	Forward gate current	42	mA
P <sub>T</sub>	Total power dissipation *1	42.8	W
T <sub>ch</sub>	Channel temperature	175	°C
T <sub>stg</sub>	Storage temperature	-65 ~ +175	°C

\*1: T<sub>c</sub> = 25°C

## RECOMMENDED BIAS CONDITIONS

- V<sub>DS</sub> = 10V
- I<sub>D</sub> = 2.4A
- R<sub>g</sub> = 50Ω
- Refer to Bias Procedure

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>DSS</sub>	Saturated drain current	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0V	—	—	7.5	A
g <sub>m</sub>	Transconductance	V <sub>DS</sub> = 3V, I <sub>D</sub> = 2.2A	—	2	—	S
V <sub>GS(off)</sub>	Gate to source cut-off voltage	V <sub>DS</sub> = 3V, I <sub>D</sub> = 20mA	—	—	-4.5	V
P <sub>1dB</sub>	Output power at 1dB gain compression	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2.4A, f = 7.7~8.5GHz	38	39	—	dBm
G <sub>LP</sub>	Linear power gain		6	7	—	dB
I <sub>D</sub>	Drain current		—	—	3.0	A
η <sub>add</sub>	Power added efficiency		—	27	—	%
IM <sub>3</sub>	3rd order IM distortion *1		-42	-45	—	dBc
R <sub>th(ch-c)</sub>	Thermal resistance *2		ΔV <sub>f</sub> method	—	—	3.5

\*1: Item-51, 2-tone test P<sub>o</sub> = 28 dBm Single Carrier Level f = 8.5GHz Δf = 10 MHz. \*2: Channel to case

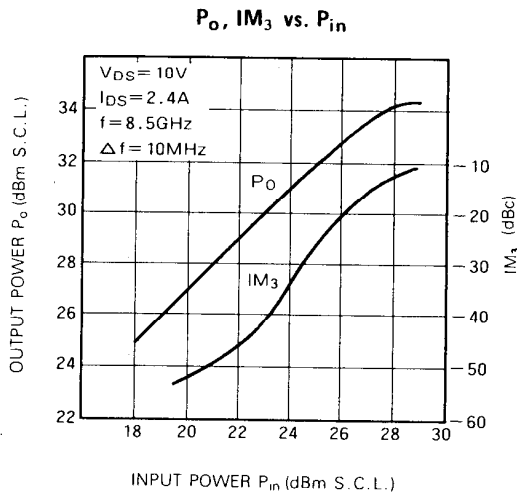
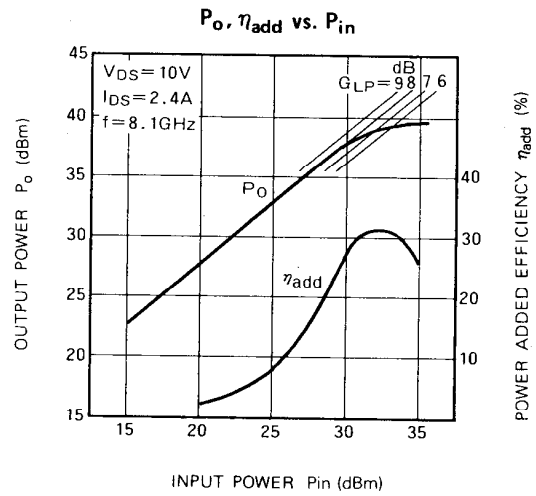
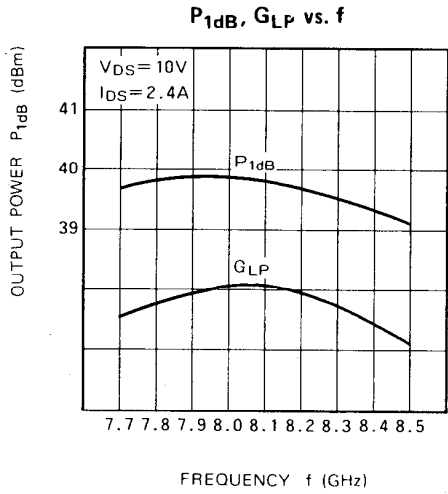
NOV. '97

**PRELIMINARY**

Notice: This is not a final specification.  
Some parametric limits are subject to change.

**7.7~8.5GHz BAND 8W INTERNALLY MATCHED GaAs FET**

**TYPICAL CHARACTERISTICS (Ta=25°C)**



**S PARAMETERS (Ta=25°C, VDS=10V, IDS=2.4A)**

f (GHz)	S Parameters (TYP.)							
	S11		S21		S12		S22	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
7.7	0.45	24	2.40	-131	0.064	178	0.23	82
7.8	0.39	11	2.45	-145	0.068	167	0.21	76
7.9	0.35	-1	2.50	-161	0.072	156	0.20	70
8.0	0.33	-8	2.52	176	0.074	146	0.20	56
8.1	0.27	-40	2.51	162	0.077	131	0.19	46
8.2	0.24	-113	2.48	145	0.071	114	0.17	21
8.3	0.26	-161	2.42	121	0.069	91	0.15	12
8.4	0.31	146	2.34	103	0.068	77	0.15	-94
8.5	0.35	129	2.26	82	0.068	63	0.15	-146