Description

The μPD72305 is a communication LSI which performs the layer-2 protocol processing in the digital subscriber system of the local communication network. This is a one-chip LSI which conforms to the basic access of ISDN and supports the LAP-D protocol prescribed in CCITT I.440 and I.441.
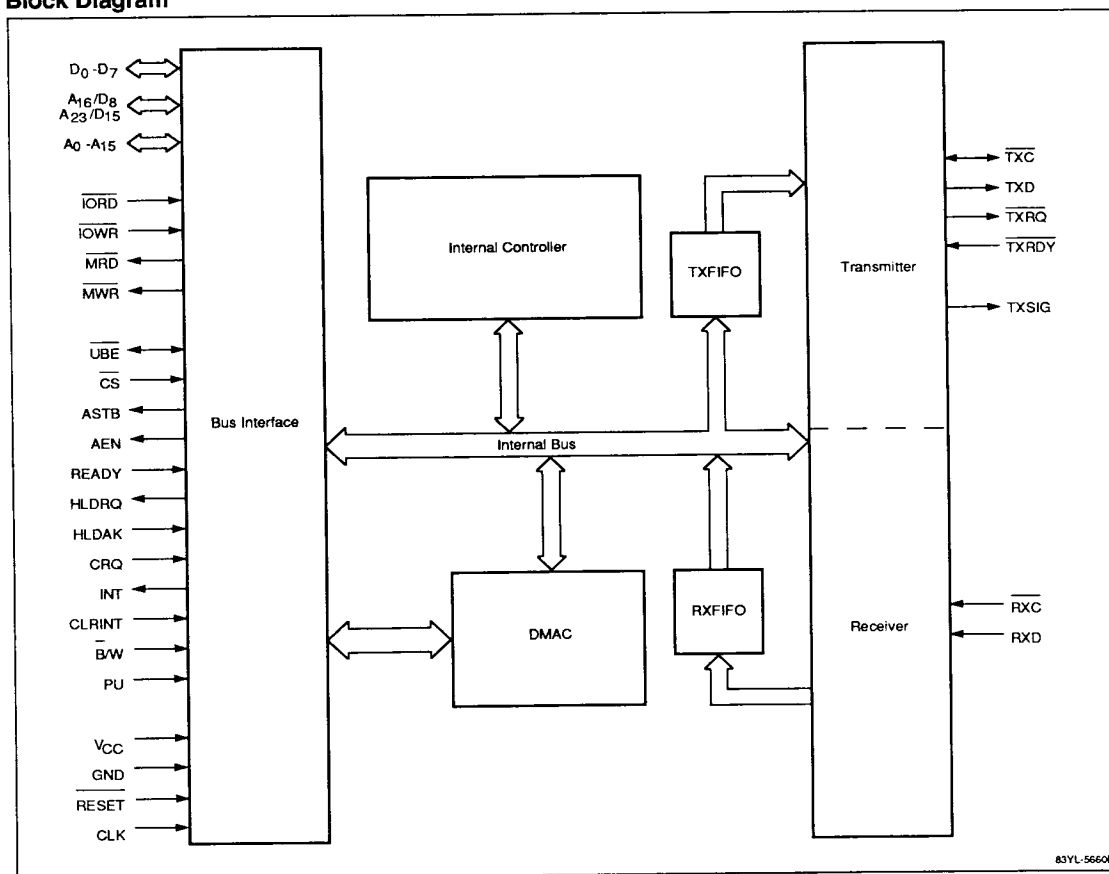
Features

□ Conforms to CCITT I.440 and I.441 (1986 version of LAP-D)
  — HDLC frame control
  — Sequence control
  — Flow control
□ Supporting basic (16 kb/s)/primary (64 kb/s)
□ Layer-1 interface:
  — Pins for multi-point contention
  — Signaling priority support control pin
□ Supports XID frame

□ Various statistical information
□ Built-in DMA
  — 24-bit address
  — Byte/word transfer enable (switching with the external pin)
□ Memory-based command
  — Link-structure command
  — Table-format status
  — Separation of specific SAP table enable by command specification
□ Network side/user side selectable
□ One-chip CMOS monolithic IC
□ +5 V single power supply

Ordering Information

Part Number	Package
μPD72305CW	64-pin plastic shrink DIP
L	68-pin PLCC

50476

Block Diagram

83YL-5660B