Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-796A (Z)

Rev.1 Nov. 2001

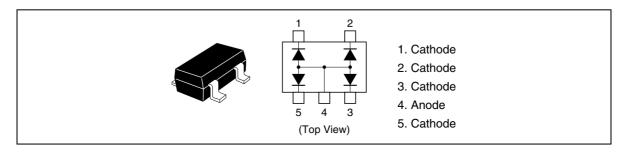
Features

- HZM5.6ZFA has four devices, and can absorb external + and -surge.
- Low capacitance (C = 8.5 pF max) and can protect ESD of signal line.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM5.6ZFA	56Z	MPAK-5

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Power dissipation	Pd *	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: Four device total, See Fig.2.

Electrical Characteristics*¹

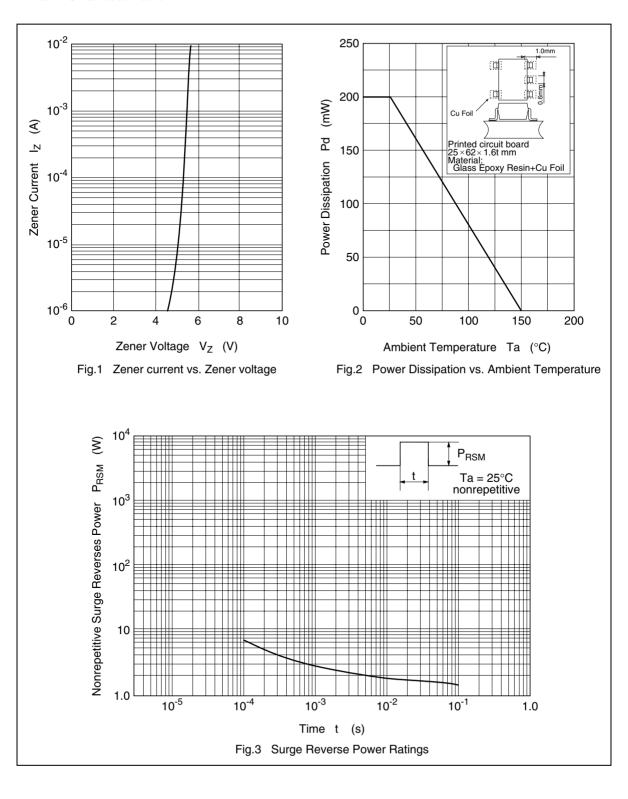
 $(Ta = 25^{\circ}C)$

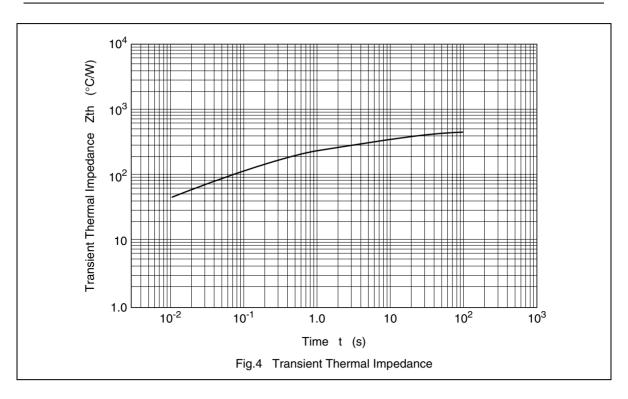
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V _z	5.31	_	5.92	V	$I_z = 5 \text{ mA}, 40 \text{ ms pulse}$
Reverse current	I _R	_	_	0.5	μΑ	V _R = 2.5 V
Capacitance	С		8.0	8.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	_	_	80	Ω	$I_z = 5 \text{ mA}$
ESD-Capability *2	_	8	_	_	kV	C = 150 pF, R = 330 Ω , Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

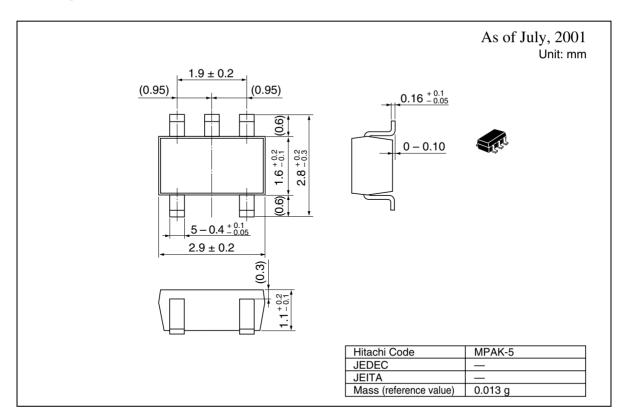
2. Failure criterion ; $I_R > 0.5 \mu A$ at $V_R = 2.5 V$.

Main Characteristic





Package Dimensions



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