

e-Front runners

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic

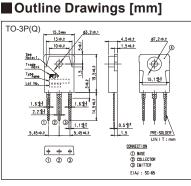
More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage $(3.0\pm0.5V)$ High avalanche durability

Applications

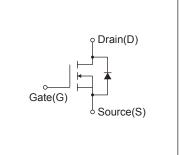
Switching regulators UPS (Uninterruptible Power Supply) **DC-DC** converters

Maximum Ratings and Characteristics

• Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Equivalent circuit schematic



Description	Symbol	Characteristics	Unit	Remarks
Durain Secure Veltere	VDS	600	V	
Drain-Source Voltage	VDSX	600	V	V _{GS} = -30V
Continuous Drain Current	lo	±23	А	
Pulsed Drain Current	IDP	±92	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	23	А	Note*1
Non-Repetitive Maximum Avalanche Energy	EAS	1033.1	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	40	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	7.5	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maximum Power Dissipation	PD	2.50	W	Ta=25°C
		400	VV	Tc=25°C
0	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to + 150	°C	

• Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	ID=250µA, VGS=0V		600	-	-	V
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	Tch=25°C	-	-	25	μA
	IDSS	V _{DS} =480V, V _{GS} =0V	Tch=125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	ID=11.5A, VGS=10V		-	0.24	0.28	Ω
Forward Transconductance	g fs	I _D =11.5A, V _{DS} =25V		14	28	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	4400	6600	pF
Output Capacitance	Coss			-	380	570	
Reverse Transfer Capacitance	Crss			-	30	45	
Turn-On Time	td(on)	V _{cc} =300V V _{GS} =10V I _D =11.5A R _{GS} =5.1Ω		-	26	39	ns
	tr			-	12	18	
Turn-Off Time	td(off)			-	144	216	
	tf			-	22	33	
Total Gate Charge	QG	V _{cc} =300V I _D =23A V _{GS} =10V		-	130	195	
Gate-Source Charge	Q _{GS}			-	30	45	nC
Gate-Drain Charge	QGD			-	40	60	
Avalanche Capability	lav	L=1.56mH, Tch=25°C		23	-	-	A
Diode Forward On-Voltage	Vsd	IF=23A, VGS=0V, Tch=25°C		-	0.90	1.35	V
Reverse Recovery Time	trr	IF=23A, VGS=0V		-	0.92	-	μs
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	14	-	μC

Thermal Characteristics

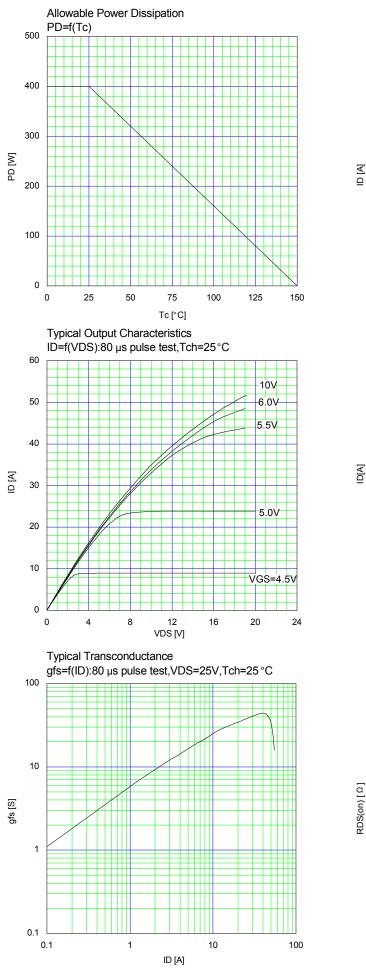
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.313	°C/W
	Rth (ch-a)	Channel to ambient			50.0	°C/W

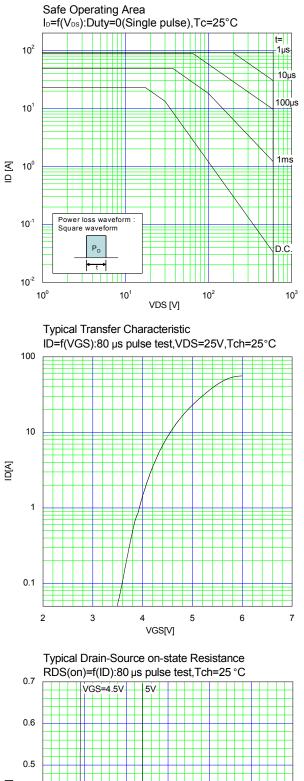
Note *1 : Tch≤150°C

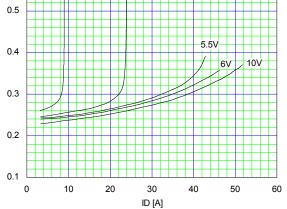
Note 1 : Italia Jo C i As=10A, L=18.9mH, Vcc=60V, Re=50Ω EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

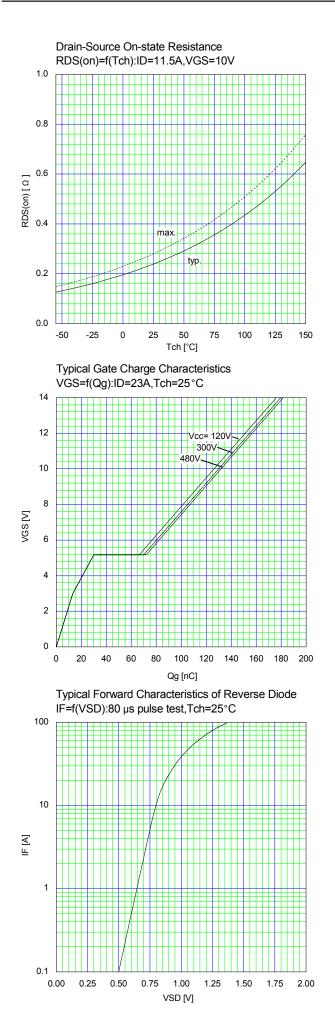
Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

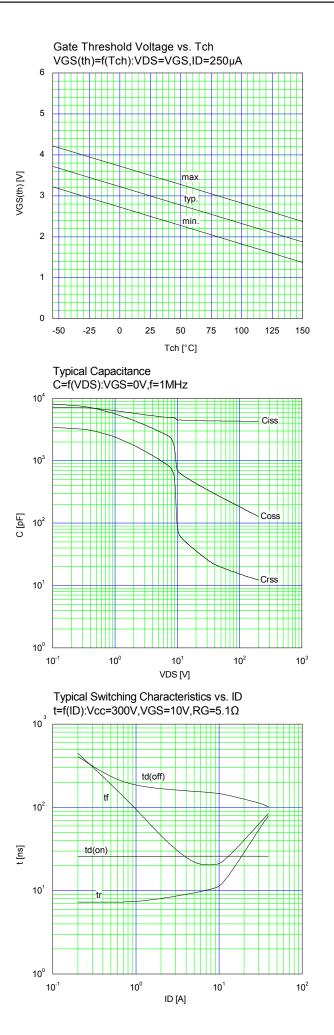
See to the 'Transient Themal impeadance' graph. Note *4 : $I_F \le I_D$, $-di/dt = 100A/\mu_S$, $Vcc \le BV_{DSS}$, $Tch \le 150^\circ C$. Note *5 : $I_F \le I_D$, $dv/dt = 7.5kV/\mu_S$, $Vcc \le BV_{DSS}$, $Tch \le 150^\circ C$.

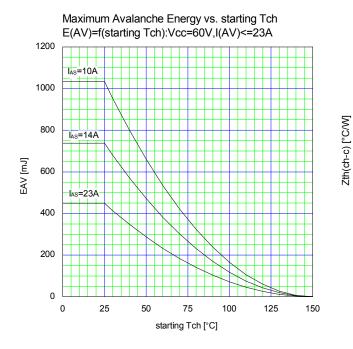




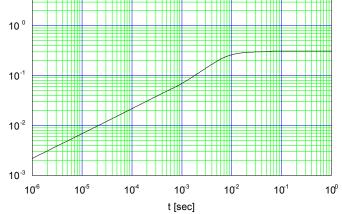








Transient Thermal Impedance Zth(ch-c)=f(t):D=0



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