|  | PAA132 | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 60 | $\mathrm{~V}_{\mathrm{P}}$ |
| Load Current | 600 | mA |
| ${\text { Max } \mathrm{R}_{\text {ON }}}^{2}$ | 1 | $\Omega$ |

## Features

- Small 8-Pin Package
- TTL/CMOS Compatible
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- $3750 \mathrm{~V}_{\text {rms }}$ Input/Output Isolation
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Surface Mount Tape \& Reel Version Available


## Applications

- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls
- Automotive


## Description

The dual 1-Form-A PAA132 Solid State Relay has two independent, optically coupled, normally open, single-pole relays in a single 8-pin package. Clare's patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing a $3750 V_{\text {rms }}$ input-to-output isolation barrier. Control of the isolated output is accomplished by means of a highly effective GaAIAs infrared LED at the input.

By incorporating two independent single-pole relays into a single 8-pin package, the PAA132 saves board space by providing a more compact design solution than two discrete single-pole relays in a variety of applications.

## Approvals

- UL recognized: File Number E76270
- CSA Certified: File Number LR43639-10
- EN/IEC 60950-1 compliant


## Ordering Information

| Part \# | Description |
| :--- | :--- |
| PAA132 | 8 Pin DIP (50/Tube) |
| PAA132S | 8 Pin Surface Mount (50/Tube) |
| PAA132STR | 8 Pin Surface Mount (1,000/Reel) |

## Pin Configuration

## PAA132 Pinout

AC/DC Configuration


Switching Characteristics of Normally Open (Form A) Devices


Absolute Maximum Ratings (@ $25^{\circ} \mathrm{C}$ )

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 60 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current <br> Peak (10ms) | 50 | mA |
|  | 1 | A |
| Input Power Dissipation ${ }^{1}$ | 150 | mW |
| Total Power Dissipation ${ }^{2}$ | 800 | mW |
| Isolation Voltage, Input to Output | 3750 | $\mathrm{~V}_{\text {rms }}$ |
| Operational Temperature | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

${ }^{1}$ Derate Linearly $1.33 \mathrm{mw} /{ }^{\circ} \mathrm{C}$
${ }^{2}$ Derate Linearly $6.67 \mathrm{mw} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

## Electrical Characteristics

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics @ $25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Load Current |  |  |  |  |  |  |
| Continuous ${ }^{1}$ | - | $\mathrm{I}_{\mathrm{L}}$ | - | - | 600 | mA |
| Peak | $t \leq 10 \mathrm{~ms}$ | L LPK | - | - | 2.0 | A |
| On-Resistance | $\mathrm{I}_{\mathrm{L}}=600 \mathrm{~mA}$ | $\mathrm{R}_{\mathrm{ON}}$ | - | 0.85 | 1.0 | $\Omega$ |
| Off-State Leakage Current | $\mathrm{V}_{\mathrm{L}}=60 \mathrm{~V}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Switching Speeds |  |  |  |  |  |  |
| Turn-On | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $\mathrm{T}_{\text {ON }}$ | - | - | 5 | ms |
| Turn-Off |  | $\mathrm{T}_{\text {OFF }}$ | - | - | 2 | ms |
| Output Capacitance | $\mathrm{V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 25 | - | pF |
| Input Characteristics @ $25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Input Control Current | $\mathrm{I}_{\mathrm{L}}=600 \mathrm{~mA}$ | $I_{\text {F }}$ | - | - | 2 | mA |
| Input Dropout Current | - | $\mathrm{I}_{\mathrm{F}}$ | 0.2 | - | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $I_{\text {R }}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Common Characteristics @ $25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Capacitance Input to Output | - | $\mathrm{C}_{1 /}$ | - | 3 | - | pF |

${ }^{1}$ If both poles operate, the load current must be derated so that the package power dissipation value is not exceeded.

PAA132

## PERFORMANCE DATA*


*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## PERFORMANCE DATA*



PAA132


PAA132


PAA132
Typical Turn-On vs. LED Forward Current


PAA132
Typical $I_{F}$ for Switch Operation vs. Temperature $\left(\mathrm{I}_{\mathrm{L}}=300 \mathrm{~mA}\right)$


PAA132
Typical Turn-Off vs. LED Forward Current ( $\mathrm{I}_{\mathrm{L}}=80 \mathrm{~mA}$ )


PAA132
Typical $I_{F}$ for Switch Dropout vs. Temperature ( $\mathrm{I}_{\mathrm{L}}=300 \mathrm{~mA}$ )



PAA132

## Manufacturing Information

## Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

## Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

## MECHANICAL DIMENSIONS

## 8-Pin DIP Through-Hole Package

PC Board Pattern


Dimensions
mm (inches)

Recommended PCB Land Pattern


## MECHANICAL DIMENSIONS

Tape and Reel Packaging for 8-Pin Surface Mount Package


NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

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