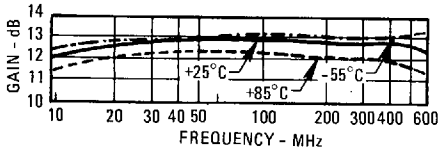


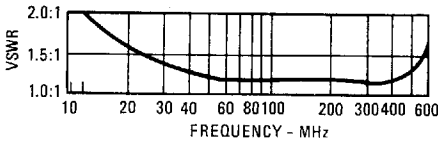


# Typical Performance at 25°C

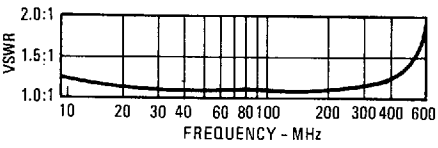
## Gain



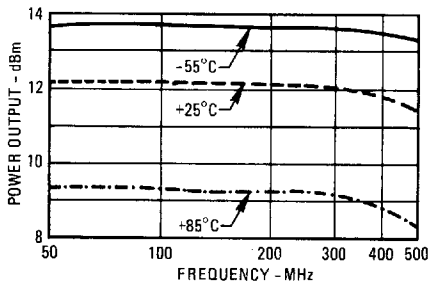
## VSWR Output



## VSWR Input

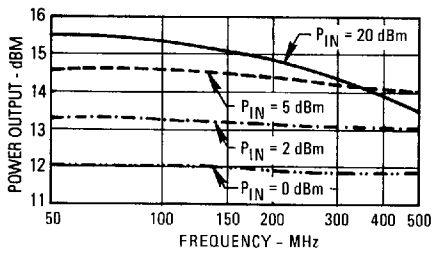


## Power Output Over Temperature\*

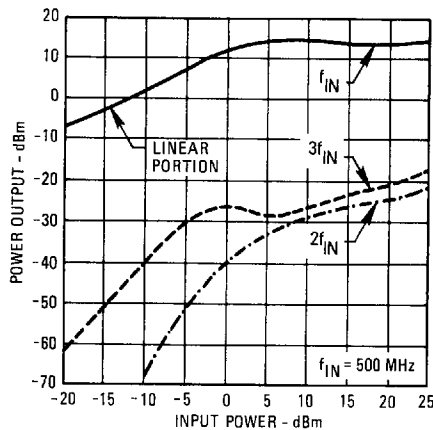
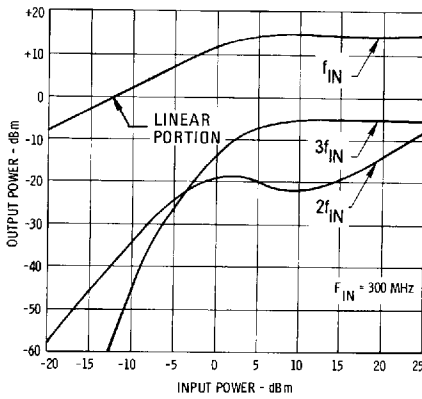
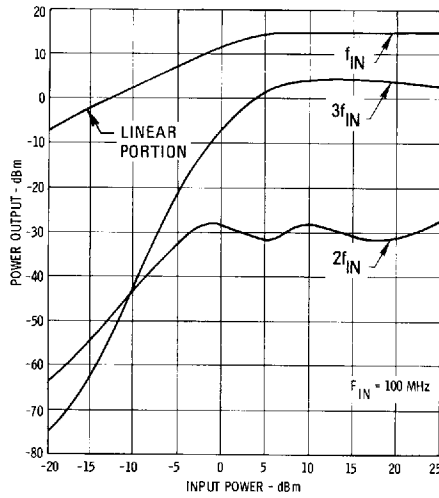


\*at 1 dB Gain Compression

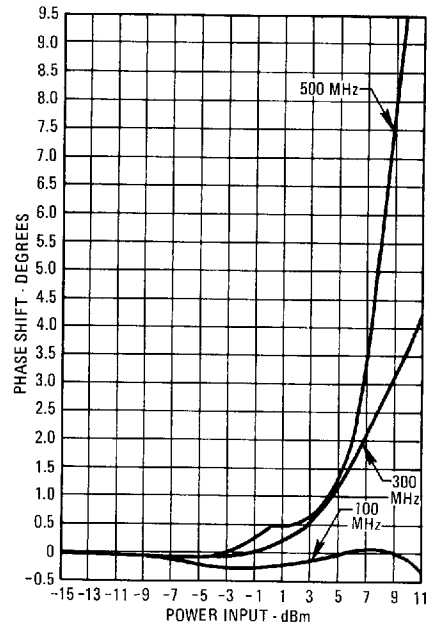
## Power Output vs. Frequency



## Power Output and Distortion Products



## Phase Shift vs. Input Power



4

# Typical Automatic Test Data

V<sub>CC</sub> = 15.0 V

Frequency MHz	VSWR IN	VSWR OUT	GAIN DB
5.0	2.7	1.2	11.5
10.0	1.7	1.1	12.1
50.0	1.1	1.1	12.6
100.0	1.1	1.1	12.6
200.0	1.2	1.2	12.7
300.0	1.3	1.2	12.7
400.0	1.3	1.1	12.9
500.0	1.3	1.1	13.0
600.0	1.1	1.4	13.0
700.0	1.6	2.4	12.1

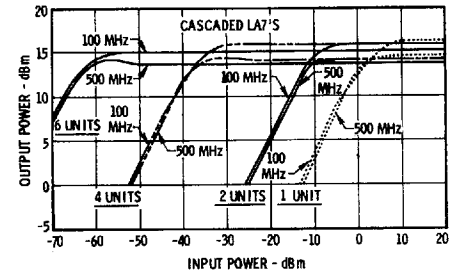
## Linear S-Parameters

Frequency MHz	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
5.0	.455	-56	3.753	-137	.087	9	.102	147
10.0	.256	-67	4.044	-159	.091	5	.059	136
50.0	.058	-60	4.255	172	.094	-1	.042	99
100.0	.047	-32	4.281	156	.096	-5	.051	69
200.0	.075	-39	4.310	126	.102	-13	.072	33
300.0	.113	-67	4.332	98	.109	-23	.081	8
400.0	.138	-95	4.420	68	.118	-35	.063	-12
500.0	.125	-122	4.475	37	.126	-49	.042	30
600.0	.065	-98	4.487	1	.135	-68	.172	49
700.0	.225	-68	4.029	-41	.133	-92	.416	14

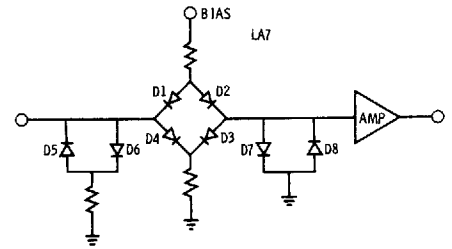
## Thermal Data: V<sub>CC</sub> = 15 Vdc

Thermal Resistance  $\theta_{jC}$  ..... 45°C/W  
 Transistor Power Dissipation P<sub>d</sub> ..... 0.560 W  
 Junction Temperature Rise Above Case T<sub>jC</sub> ... 25.2°C

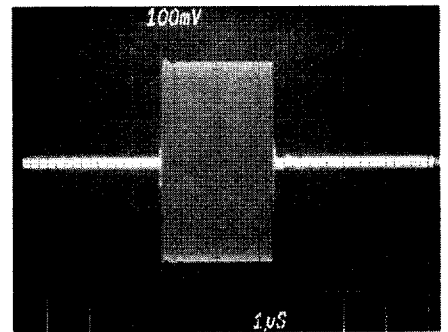
## Limiting Characteristics



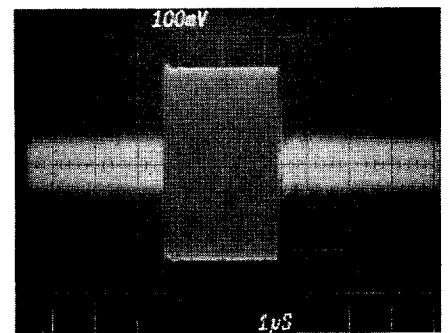
## Schematic Diagram



## Output Response, 6 Cascaded LA7's



-45 dBm Input CW Pulse



+5 dBm Input CW Pulse