

**CR 50 UB**

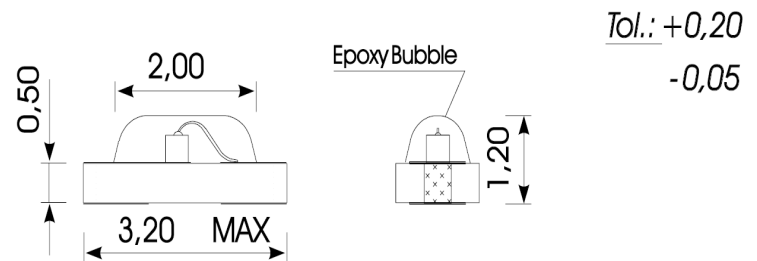
**Features**

Solid State Ceramic Chip  
 High Power Thermal Absorption  
 Superior Light Uniformity Over 180°  
 End-to-End and Side-to-Side Stackable to a pitch of 1.3mm  
 Solderpads conform to Mil-Std 883B  
 Caution: Static electricity and surge damages blue LED chip.  
 GaN/SiC Material  
 Water Clear Lens



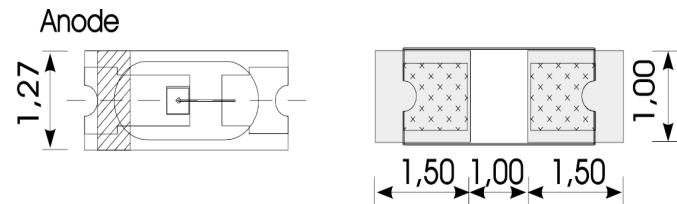
**Applications**

Ideal For Back-Light Applications  
 Custom Configurations



**Maximum Ratings (Ta=25°C)**

Characteristic	Symbol	Max.	Unit
Forward Current	I <sub>F</sub>	50	mA
Reverse Voltage	V <sub>R</sub>	-	V
Power Dissipation	P <sub>D</sub>	130.00	mW
Operating Temperature	T <sub>opr</sub>	-25 ~ +80	°C
Storage Temperature	T <sub>stg</sub>	-25 ~ +120	°C
Soldering Temperature	T <sub>sol</sub>	250	°C
Soldering Time	-	for 10 sec. max	-



**Opto-Electrical Characteristics (Ta=25°C)**

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =20mA	-	4.50	5.50	V
Reverse Current	I <sub>R</sub>	V <sub>R</sub> =5V	-	-	10	μA
Luminous Intensity	I <sub>v</sub>	I <sub>F</sub> =20mA	2.10	5.20	-	mcd
Viewing Angle	2θ <sup>1/2</sup>	-	-	180°	-	deg.
Peak Wavelength	λ <sub>p</sub>	I <sub>F</sub> =20mA	-	460	-	nm
Dominant Wavelength	λ <sub>d</sub>	I <sub>F</sub> =20mA	-	465	-	nm
Spectral Line Half Width	Δλ	I <sub>F</sub> =20mA	-	65	-	nm

Specifications are Subject to Change Without Notice

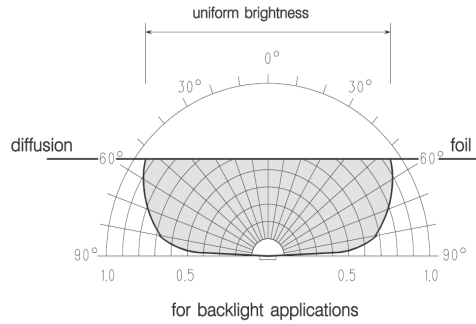
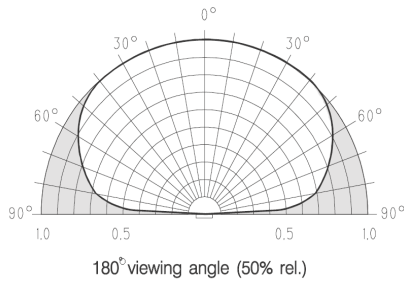
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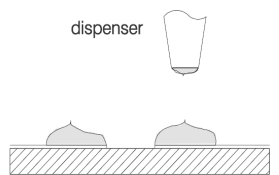
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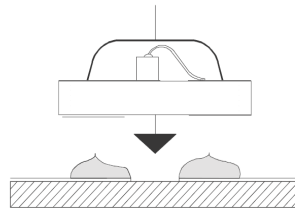
**CR 50 UB Graphs**



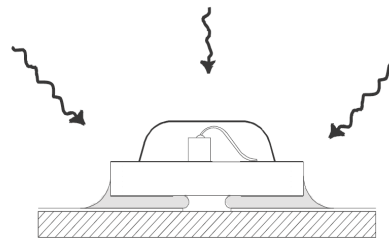
**SUGGESTED MOUNTING METHOD**



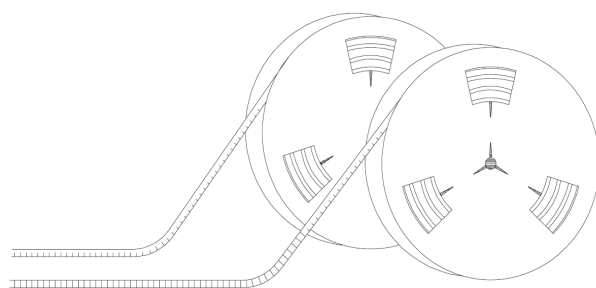
1. Apply conductive epoxy or soldercream onto the tracer



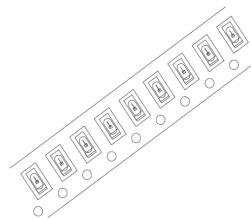
2. Place the CERLED in position



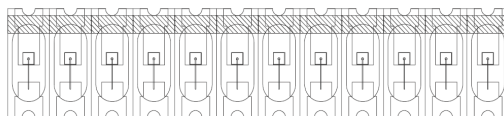
3. Cure epoxy soldercream (temperature and time according to vendors instructions)



Special Packing:  
standard 8mm blister tape



**ARRAYS**



code to order strips:  
CR50 XX - 10\_       
No of LEDs

Available in strips up to 12 CERLEDs with a max. pitch tolerance in spacing and linearity of  $\pm 0,01$  mm between chip centers.