

50-90 AMP/NPN**Pirgo Silicon Planar
Power Transistors**

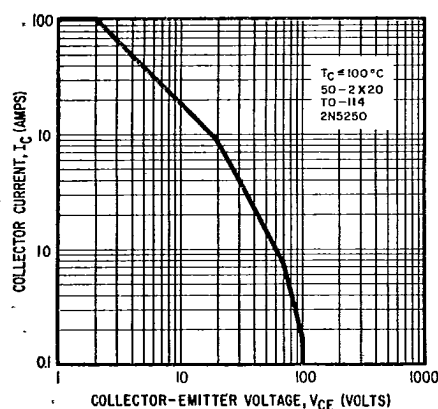
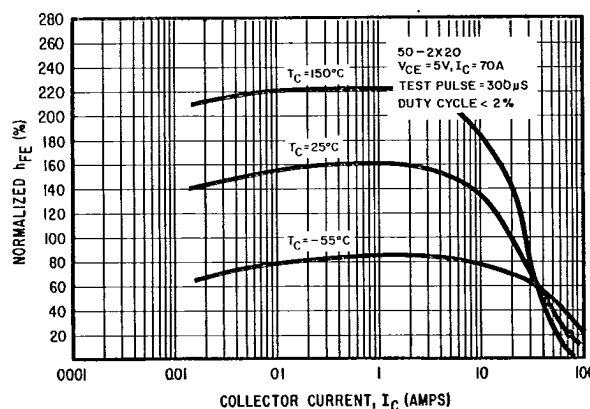
**50-90 AMP
100 VOLT
200 WATT
10 MHz
TO-114**



TO-114

- Linear h_{FE} from 100 mA to 90 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 10$ MHz (minimum)
- High voltage, $BV_{CEO(sus)}$ to 100 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}			h_{FE}		$V_{CE(sat)}$		C_{obMax} pf	P_{DW} @ 100°C
					Min	Max	@ I_{cA}	Min	@ I_{cA}	Max	@ I_{cA}		
2N3149	TO-114	80	80	10				10	50	1.5	50		200
2N3150	TO-114	100	100	10				10	50	1.5	50		200
2N4865	TO-114	100	80	8	10	40	70	5	90	5	90		200
2N5250	TO-114	125	100	10	10	40	70	5	90	5	90		200
PG1530	TO-114	100	80	6				5	50	5	50		200

MAXIMUM SAFE OPERATING AREA**NORMALIZED h_{FE} Vs I_c** **PIRGO QUALITY ASSURANCE ENGINEERING**

Every lot is inspected prior to shipment for electrical and mechanical integrity. The Quality Assurance Engineering department must certify that each lot complies with the customers requirements prior to release to the outgoing shipping department.

The Pirgo Qualifications Lab facility performs regular routine inspections to MIL specs such as MIL-Q-9858,

MIL-S-19500,
MIL-STD-202,
MIL-STD-750,
MIL-STD-883,
MIL-STD-105.

PG--0110-18x