# NPN wideband silicon germanium RF transistor

Rev. 01 — 17 June 2010

**Objective data sheet** 

## 1. Product profile

#### 1.1 General description

NPN silicon germanium microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

### 1.2 Features and benefits

- 40 GHz f<sub>T</sub> silicon germanium technology
- High associated gain 12 dB at 12 GHz

### **1.3 Applications**

- 2nd LNA stage and mixer stage in DBS LNB's
- Analog/digital cordless applications
- Ka band oscillators DRO's

- Low noise high gain microwave transistor
- Noise figure (NF) = 1.4 dB at 5.8 GHz
- Low noise amplifiers for microwave communications systems
- Satellite radio
- WLAN and CDMA applications

#### 1.4 Quick reference data

#### Table 1. Quick reference data

Table 1.	Quick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$C_{CBS}$	collector-base capacitance	$V_{CB}$ = 2 V; f = 1 MHz; $V_{BE}$ = [tbd] V		-	70	-	fF
f <sub>T</sub>	transition frequency	$V_{CE} = 2 V; I_C = 25 mA;$ f = 2 GHz; T <sub>amb</sub> = 25 °C		-	40	-	GHz
G <sub>p(max)</sub>	maximum power gain	$\label{eq:starsessense} \begin{array}{l} f = 5.8 \; GHz; \; I_{C} = 8 \; mA; \\ V_{CE} = 2 \; V; \; T_{amb} = 25 \; ^{\circ}C \end{array}$	<u>[1]</u>	-	21	-	dB
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 2 V; $I_C$ = 10 mA; $T_j$ = 25 °C		70	140	270	
I <sub>C</sub>	collector current			-	-	10	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 90 °C; see <u>Figure 1</u>	[2]	-	-	50	mW
V <sub>CBO</sub>	collector-base voltage	I <sub>E</sub> = 0 A		-	-	10	V
$V_{CEO}$	collector-emitter voltage	I <sub>B</sub> = 0 A		-	-	5	V
V <sub>EBO</sub>	emitter-base voltage	$I_{\rm C} = 0$ A		-	-	0.55	V

[1]  $G_{p(max)}$  is the maximum power gain, if K > 1. If K < 1 then  $G_{p(max)}$  = MSG.

[2]  $T_{sp}$  is the temperature at the solder point of the emitter lead.



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## 2. Pinning information

Table 2.	Pinning	j information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		_
2	В	base	3 4	C I
3	Е	emitter		в
4	С	collector		E sym123
			SOT343F (DFP4)	5,25

## 3. Ordering information

Table 3.	Ordering in	formation		
Type number Packag		Package		
		Name	Description	Version
BFU610F		DFP4	Plastic surface-mounted flat pack package; 4 leads	SOT343F

## 4. Marking

Table 4. Marking codes	
Type number	Marking code
BFU610F	D1

## 5. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

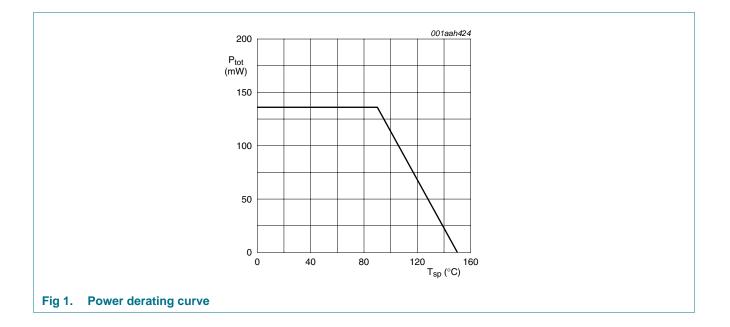
	Тур -	<b>Max</b> 10	Unit
	-	10	
_		10	V
	-	5	V
-	-	0.55	V
-	-	10	mA
-	-	50	mW
5 -	-	150	°C
	-	150	°C
5	5	- 5 - -	5 - 150

[1]  $T_{sp}$  is the temperature at the solder point of the emitter lead.

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## 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	440	-	K/W

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## 7. Characteristics

Table 7.	Characteristics			_		
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
/ <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>E</sub> = 0 mA; I <sub>C</sub> = 2.5 μA; T <sub>j</sub> = 25 °C	10	-	-	V
/ <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_B = 0 \text{ mA}; I_C = 1 \text{ mA}; T_j = 25 \text{ °C}$	5	-	-	V
СВО	collector-base cut-off current	$I_E = 0 \text{ mA}; V_{CB} = 4.5 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
٦ <sub>FE</sub>	DC current gain	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \text{ T}_{j} = 25 \text{ °C}$	70	140	270	
C <sub>CBS</sub>	collector-base capacitance	$V_{CB}$ = 2 V; f = 1 MHz; $V_{BE}$ = [tbd] V	-	70	-	fF
Т	transition frequency	$V_{CE}$ = 2 V; I <sub>C</sub> = 25 mA; f = 2 GHz; T <sub>amb</sub> = 25 °C	-	40	-	GHz
G <sub>p(max)</sub>	maximum power gain	f = 5.8 GHz; I <sub>C</sub> = 8 mA; V <sub>CE</sub> = 2 V; T <sub>amb</sub> = 25 °C	<u>[1]</u> -	21	-	dB
		f = 1.8 GHz; I <sub>C</sub> = 8 mA; V <sub>CE</sub> = 2 V; T <sub>amb</sub> = 25 °C	<u>[1]</u> -	29	-	dB
		$    f = 1.5 \text{ GHz}; I_C = 8 \text{ mA}; V_{CE} = 2 \text{ V};                                   $	<u>[1]</u> _	30.4	-	dB
		$f = 2.4 \text{ GHz}; I_C = 8 \text{ mA}; V_{CE} = 2 \text{ V};$ $T_{amb} = 25 \text{ °C}$	<u>[1]</u> -	28	-	dB
		$f = 12 \text{ GHz}; I_C = 8 \text{ mA}; V_{CE} = 2 \text{ V};$ $T_{amb} = 25 \text{ °C}$	<u>[1]</u> -	14.3	-	dB
s <sub>21</sub>   <sup>2</sup>	insertion power gain	$I_{C}$ = 8 mA; $V_{CE}$ = 2 V; f = 1.5 GHz; $T_{amb}$ = 25 °C	-	19	-	dB
		$I_{C} = 8 \text{ mA}; V_{CE} = 2 \text{ V}; \text{ f} = 1.8 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	-	18	-	dB
		$I_{C}$ = 8 mA; $V_{CE}$ = 2 V; f = 2.4 GHz; $T_{amb}$ = 25 °C	-	17.5	-	dB
		$I_C = 8 \text{ mA}; V_{CE} = 2 \text{ V}; \text{ f} = 5.8 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	-	13.4	-	dB
		$I_C = 8 \text{ mA}; V_{CE} = 2 \text{ V}; f = 12 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	-	7.7	-	dB
C(1dB)	output power at 1 dB gain compression	$\label{eq:Vce} \begin{array}{l} V_{CE} = 2 \; V; \; f = 1.5 \; GHz; \; I_{C} = 25 \; mA; \\ Z_{L} = 50 \; \Omega; \; Z_{S} = 50 \; \Omega \end{array}$	-	[tbd]	-	dBm
		$V_{CE}$ = 2 V; f = 2.4 GHz; I <sub>C</sub> = 25 mA; Z <sub>L</sub> = 50 Ω; Z <sub>S</sub> = 50 Ω	-	[tbd]	-	dBm
		$V_{CE}$ = 2 V; f = 1.8 GHz; I <sub>C</sub> = 25 mA; Z <sub>L</sub> = 50 Ω; Z <sub>S</sub> = 50 Ω	-	[tbd]	-	dBm
		$V_{CE} = 2 \text{ V}; \text{ f} = 5.8 \text{ GHz}; \text{ I}_{C} = 25 \text{ mA};$ $Z_{L} = 50 \Omega; Z_{S} = 50 \Omega$	-	[tbd]	-	dBm

[1]  $G_{p(max)}$  is the maximum power gain, if K > 1. If K < 1 then  $G_{p(max)}$  = MSG.

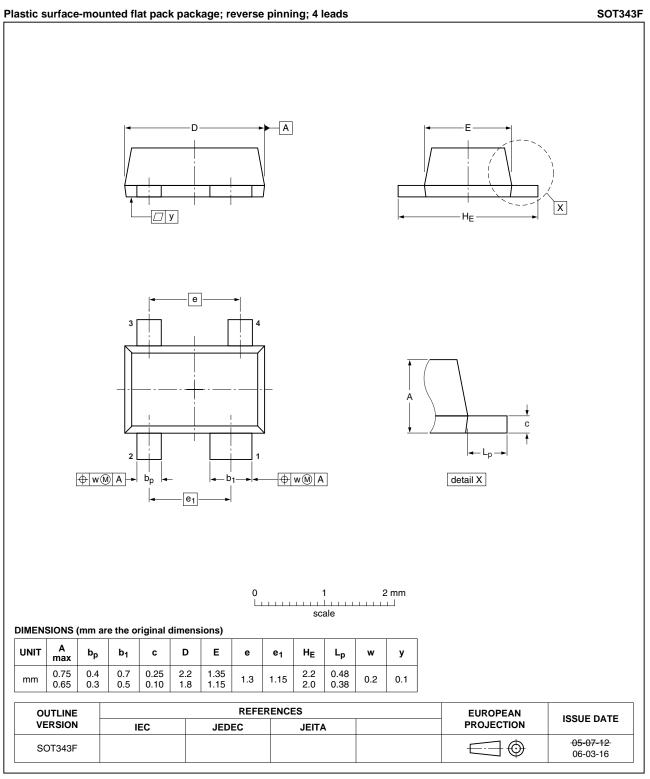
5 of 12

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## 8. Package outline



#### Fig 2. Package outline SOT343F (DFP4)

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## 9. Soldering

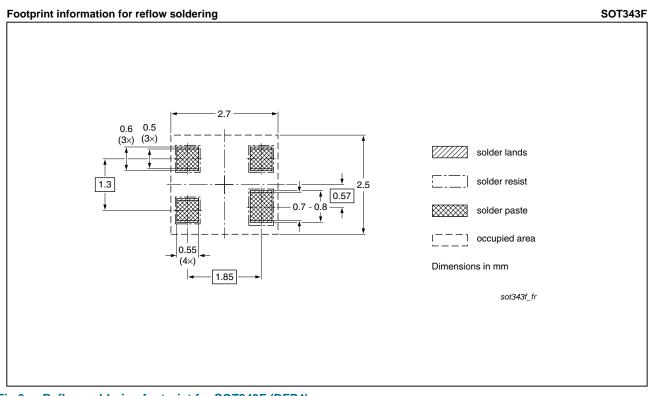


Fig 3. Reflow soldering footprint for SOT343F (DFP4)

### NPN wideband silicon germanium RF transistor

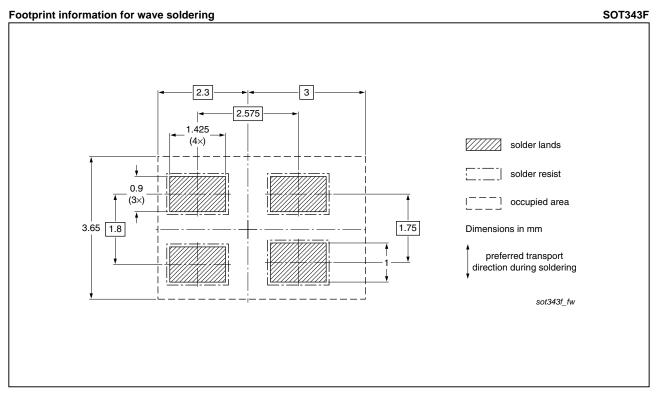


Fig 4. Wave soldering footprint for SOT343F (DFP4)



## **10. Revision history**

Table 8. R	evision his	tory			
Document II	כ	Release date	Data sheet status	Change notice	Supersedes
BFU610F v.1		20100617	Objective data sheet	-	-



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Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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10 of 12

Rev. 01 — 17 June 2010

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## NPN wideband silicon germanium RF transistor

## 13. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Marking2
5	Limiting values2
6	Thermal characteristics4
7	Characteristics5
8	Package outline6
9	Soldering7
10	Revision history9
11	Legal information10
11.1	Data sheet status10
11.2	Definitions10
11.3	Disclaimers
11.4	Trademarks11
12	Contact information11

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