## HITACHI

KAOHSIUNG HITACH! ELECTRONICS CO.,LTD P.O. BOX 26-27 2,13TH EAST ST. K.E.P.Z. KAOHSIUNG TAIWAN R.O.C. TEL:(07) 8215811 (7 LINE) FAX:(07) 821-5815

FOR MESSRS: STD

DATE: May.13,2008

### CUSTOMER'S ACCEPTANCE SPECIFICATIONS

# TX09D70VM1CCA CONTENTS

No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701-TX09D70VM1CCA-5	1-1/1
2	RECORD OF REVISION	7B64PS 2702-TX09D70VM1CCA-5	2-1/2~2/2
3	GENERAL DATA	7B64PS 2703-TX09D70VM1CCA-5	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704-TX09D70VM1CCA-5	4-1/2~2/2
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705-TX09D70VM1CCA-5	5-1/2~2/2
6	OPTICAL CHARACTERISTICS	7B64PS 2706-TX09D70VM1CCA-5	6-1/2~2/2
7	BLOCK DIAGRAM	7B64PS 2707-TX09D70VM1CCA-5	7-1/1
8	INTERFACE TIMING CHART	7B64PS 2708-TX09D70VM1CCA-5	8-1/6~6/6
9	DIMENSIONAL OUTLINE	7B63PS 2709-TX09D70VM1CCA-5	9-1/1
10	APPEARANCE STANDARD	7B64PS 2710-TX09D70VM1CCA-5	10-1/4~4/4
11	PRECAUTION IN DESIGN	7B64PS 2711-TX09D70VM1CCA-5	11-1/3~3/3
12	DESIGNATION OF LOT MARK	7B64PS 2712-TX09D70VM1CCA-5	12-1/1
13	PRECAUTION FOR USE	7B64PS 2713-TX09D70VM1CCA-5	13-1/1

\*When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;

KAOHSIUNG HITACHI	Sh.	7B64PS 2701-TX09D70VM1CGA-5 PAGE	1 1/1 ×
ELECTRONICS CO.,LTD.	No.	7 D041 3 2701-1 X09 D70 V W 1 C CA-3	1-1/1

## RECORD OF REVISION

DATE	SHEET No.	SUMMARY
Oct.28,'05	7B64PS 2704-	4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF LCD
001.20, 00	TX09D70VM1CCA-2	Revised
	PAGE 4-1/2	ITEM SYMBOL MAX.
		LED Forward Current IF 25
		Pulse Forward Current   I <sub>FP</sub>   80
		ITEM SYMBOL MAX.
		Forward Current IF 35
		LED Pulse Forward Current   I <sub>FP</sub> 100
		Note 4:
		30 (V) 25 10 10 10 10 10 10 10 10 10 10
•		Note 5:
		IFP Conditions : pulse width ≤10ms and Duty≤1/10 IFP Conditions : pulse width≤10ms and Duty≤1/10
		Ta=25°C  (E)  100  100  100  100  100  100  100  1
<u>.</u>	7B64PS 2705-	5.2 ELECTRICAL CHARACTERISTICS OF BACK LIGHT
	TX09D70VM1CCA-2	Revised
	PAGE 5-1/2	ITEM SYMBOL CONDITION MAX. TYP. MAX.
		Input Voltage   VF   IF=20mA   -   3.75   4.2
		LED 15 20 20
		Forward Current
		ITEM SYMBOL CONDITION MAX. TYP. MAX.
		I ED
		Input Voltage VF IF=20mA - 3.2 3.5
		LED Forward Current IF - 20 25
	7B64PS 2705- TX09D70VM1CCA-2 PAGE 6-1/6	6.1 OPTICAL CHARACTERISTICS OF LCD Revised the color tone
Service and the service and th	7B64PS 2705- TX09D70VM1CCA-2 PAGE 8-6/6	8.5 INTERNAL PIN CONNECTION Revised the function of PIN35 Added Note1
	G HITACHI IICS CO.,LTD. DATE	May.13,'08 Sh. No. 7B64PS 2702-TX09D70VM1CCA-5 PAGE 2-1/2

### RECORD OF REVISION

DATE	SHEET No.	St	UMMARY					
Jan.27,'06	7B64PS 2705- TX09D70VM1CCA-3 PAGE 8-3/6	8.3 POWER ON/OFF SEQUENCE Added the waveform of PCI signal						
	7B64PS 2705- TX09D70VM1CCA-3 PAGE 8-6/6	8.5 INTERNAL PIN CONNEC Revised the function of I Revised Note1						
Feb.17,'06	7B64PS 2705- TX09D70VM1CCA-4	8.1 INTERFACE TIMING Revised	-					
	PAGE 8-1/6		MIN	······································	MIN			
		Horizontal Total	258		265			
		Horizontal Sync Start	246	$\rightarrow$	244			
		Horizontal Sync End	250		248			
		Horizontal Blank Time	18		25			
May.13,'08	7B64PS 2712-	12.1 LOT MARK						
	TX09D70VM1CCA-5 PAGE 12-1/1	Changed : 5 digits for produc	tion number	•				
		6 digits for production number						
		12.2 Location of lot mark Lot mark change: to Barcode label						
		·						
		·						
	,							
			•					
				-				
	C LUTACUL I	Sh.						
AOHSIUN	J MITAUMI I	May.13,'08 7B64PS 270		1. Table 20, 1. Table 11. Table 11.		2-2/2		

### 3.GENERAL DATA

The specifications are applied to the following TFT-LCD (Transmissive with micro reflectance) module with Back-light unit.

(1)	Part Name	TX09D70VM1CCA
(2)	Module Dimensions	64.0(W)mm x 86.0(H)mm x 8.05(D)mm typ.
(3)	Effective Display Area	53.64(W)mm x 71.52(H)mm (Diagonal:9cm)
(4)	Dot Pitch	0.0745mm x 3(R,G,B)(W) x 0.2235(H)mm
(5)	Resolution	240 x 3(R,G,B)(W) x 320 (H) dots
(6)	Color Pixel Arrangement	R,G,B Vertical Stripe
(7)	LCD Type	Transmissive Color TFT LCD (Normally White)
(8)	Display Type	Active Matrix
(9)	Number of Colors	262 <sup>K</sup> Colors (R,G,B 6 Bit Digital each)
(10)	Backlight	Light Emitting Diode (LED) x 6
(11)	Weight	(48)g
(12)	Interface	40 pin C-MOS
(13)	Power Supply Voltage	3.3V only
		(Including Timing Controller ,LCD and LED Power Unit)
(14)	Viewing Direction	6 O'clock (The direction it's hard to be discolored)
(15)	Touch Panel	Resistance type. The surface is anti-glare.

### 4. ABSOLUTE MAXIMUM RATINGS

### 4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

VSS=0V

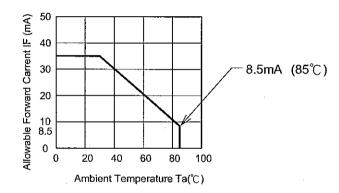
	ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Pow	er Supply for Logic	VDD	-0.3	4.0	V	
Inpu	t Voltage	VI	-0.3	VDD+0.3		(Note 1)
Inpu	t Current	li	0	1	Α	
Stati	Static Electricity		-	±100	V	(Note 2,3)
Otati		VESD1		(8)	. kV	(Note 2,4)
	Forward Current	IF ·	-	35	mA	(Note 5)
LED	Pulse Forward Current	İFP	-	100	mA	(Note 6)
	Reverse Voltage	VR	-	5	V	

Note 1: DTMG, DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

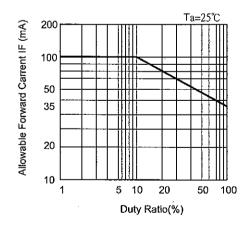
Note 2 : 200pF-0  $\Omega$  25°C -70%RH Note 3 : Interface Pin Connector.

Note 4: The surface of metal bezel and LCD panel.

Note 5:



Note 6 : IFP Conditions : pulse width  $\leq$  10ms and Duty  $\leq$  1/10



KAOHSIUNG HITACHI	DATE	1440.100	Sh.	700 400 0704 TV000 T0 W44 7004 5		
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2704-TX09D70VM1CCA-5 F	PAGE	4-1/2

### 4.2 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	CONDITION	REMARKS
Supply Voltage	7.0	V	DC	
Endurance Voltage	25	V	DC	(Note 1)

Note 1: Waiting 1 minute.

### 4.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPER	ATING	STOR	RAGE	REMARKS	
I I LIVI	Min.	Max.	Min.	Max.	KEIVIARKS	
Ambient Temperature	<b>-20</b> ℃	70℃	-30°C	80℃	(Note 2,3,6,7,9,10)	
Humidity	(Note 1) (Note 1)		te 1)	Without condensation		
Vibration	<u>.</u>	2.45m/s <sup>2</sup> (0.25G)	<del>-</del>	11.76m/s <sup>2</sup> (1.2G)	(Note 4,5)	
Shock	. =	29.4m/s <sup>2</sup> (3G)	<del>-</del>	490m/s <sup>2</sup> (50G)	(Note 5,8)	
Corrosive Gas	Not Ac	ceptable	Not Acc	ceptable		

Note 1 : Ta ≤ 40°C : 85%RH max.

Ta> $40^{\circ}$ C: Absolute humidity must be lower than the humidity of 85%RH at  $40^{\circ}$ C.

For operating condition Ta at -20°C < 100h

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4:5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7 : Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at 25℃.

Note 8: Pulse Width: 10ms

Note 9: This is panel surface temperature, not ambient temperature.

Note 10: If LED is drived by high current, the life time of LED will be reduced, also high temperature and high humidity.

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ELECTRONICS CO.,LTD.	DATE	May. 13, 06	No.	7B64PS 2704-TX09D70VM1CCA-5 PA	HOE	4-212

### 5. ELECTRICAL CHARACTERISTICS

#### 5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C, VSS=0V --

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	_	3.0	3.3	3.6	V
Input voltage for logic	VI	"H" level	1.7	-	VDD	V
(note 1)	V1	"L" level	VSS	-	0.7	V
Power Supply Current (note 2)	IDD	VDD-VSS=3.3V	-	200	· <u>-</u>	mA
Vsync Frequency	fV	-	52	60	68	Hz
Hsync Frequency	fH	-	10.92	19.5	22.12	kHz
DCLK Frequency	fCLK		4.62	5.33	6.04	MHz

Note 1: DTMG, DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

Note 2 : fV=60Hz, Ta=25℃, Pattern used as display pattern : All Black.

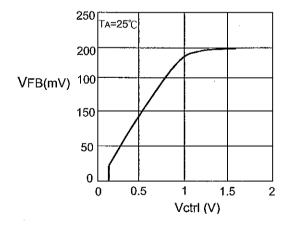
Note 3: Need to made sure of flickering and rippling of display when setting the frame frequency in your set.

### 5.2 ELECTRICAL CHARACTERISTICS OF BACK LIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
LED Input Voltage	VF	IF=20mA	-	3.2	3.5	٧	LED / Part
LED Forward Current	IF	-	-	20	25	mA	LED / Part
LED Reverse Current	IR	VR=5V	-	_	50	$\mu$ A	LED / Part
LED Current Control	Vctrl	VDD-VSS=3.3V	0	1.8	4.0	V	(Note 1)

Note 1: LED current depend on following conditions.

LED current is calculated by Vctrl and VFB when VFB is controlled by Vctrl.



ILED:  $\frac{V_{FB}}{10}$ : When Vctrl > 1.8 V

ILED:  $\frac{Vctrl}{50}$ : When Vctrl < 1 V.

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E	LECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2705-TX09D70VM1CCA-5	RAGE	5-1/2

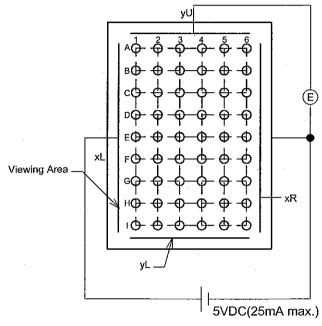
### 5.3 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT		
Decistores between T	·	xR - xL	200 - 650	ohm
Resistance between T	erminai	yU - yL	250 - 500	ohm
Insulance Resistance	(Note 1)	х - у	10M min.	ohm
1 imposite	(Nata 0 2)	X .	1.5 max.	%
Linearity (	(Note 2,3)		1.5 max.	%
Chattering			10 max.	ms

Note 1: Operating Voltage 25V DC.

Note 2: Test Condition.

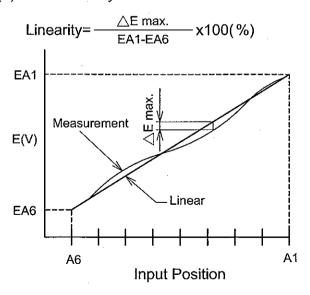
(a) Y axis linearity testing method (with tip radius 0.8, polaycetal pen). VxL-xR=5V, VOUT=VyU.



(b) X axis linearity method VyU-yL=5V, VOUT=VxL.

Note (4) Pen Force Area

Note 3 : Calculation
(a) Y axis linearity



T/P Active Area

\*
EE

1 mm \*1

1 mm \*1

### 5.4 MECHANICAL CHARACTERISTICS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	REMARKS
Pen Input Pressure	0.1 - 1.3	Ν	R0.8mm Polyacetal pen Note(4)
Surface Hardness	3H min.	-	JIS K 5400

		and the state of t	
KAOHSIUNG HITACHI	Sh	The second secon	
III	E   May 13 '08	7B64PS 2705-TX09D70VM1CCA-5   PAGE   5	5-2/2
ELECTRONICS CO.,LTD.	E May.13,'08 No	1 DOT O ZIOO INCODIOVINIOONO TAOL O	<i>'-2,'2</i> [

### 6. OPTICAL CHARACTERISTICS

### 6.1 OPTICAL CHARACTERISTICS OF LCD (BACK LIGHT ON )

Ta=25℃

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE	
Brightness		В	$\phi = 0^{\circ} \theta = 0^{\circ}$	-	320	-	cd/m <sup>2</sup>	(1)	
Uniformity			φ=0° θ=0°	70	-	-	%	(2),(3),(4)	
		θ×	<i>φ</i> =0°,K≥5.0	-	70	-			
Viouing Apalo		θx	<i>φ</i> =180°,K≧5.0	-	70	_	do	(E) (C)	
Viewing Angle		$\theta$ y	φ=90°,K≥5.0	-	80	-	deg	(5),(6)	
		$\theta$ y	<i>φ</i> =270°,K≥5.0	-	60	_			
Contrast Ratio		K	$\phi = 0^{\circ} \theta = 0^{\circ}$	180	300	· <b>-</b>	-	(4)	
Response Time (r	ise-fall)	tr+tf	φ=0° θ=0°		(30)	-	ms	(8)	
Color Tone	Pod	Х		0.55	0.60	0.65	-		
(Primary Color)	Red	у		0.29	0.34	0.39	-		
	Green	х		0.28	0.33	0.38	-		
	Green	. у	$\phi = 0^{\circ} \theta = 0^{\circ}$	0.54	0.59	0.64	-	(4)	
	Plue	х	$\phi = 0^{\circ}  \theta = 0^{\circ}$	0.09	0.14	0.19	-	(4)	
	Blue	у		0.07	0.12	0.17			
	White	Х		0.27	0.32	0.37	_		
	vviile	у		0.29	0.34	0.39	_		

Note 1: Active area center

Note 2: Driving Condition

Display Pattern : White Raster

LED Current: 20mA / Part Measurement of the following 5 places on the display.

X=50 X=120 X=190

Y=70

7 8 9

Y=160

1 2 3

(Measurement condition: HITACHI standard)

Note  $(4)\sim(7)$ : See page 6-2/2

Note 3: Definition of the brightness uniformity

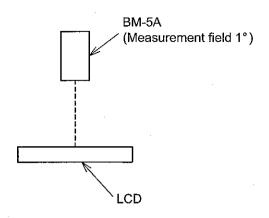
(Min. brightness x 100 Max. brightness

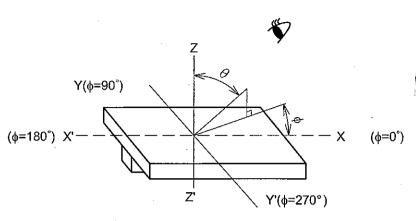
KAOHSIUNG HITACHI	DATE	May 42 200	Sh.	7D04D0 ~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	000701/042/064	DACE	0.4/0
ELECTRONICS CO.,LTD.	DATE	May. 13, 08	No.	7B64PS 2706-1X	09D70VM1CCA-5	FAGE	6-1/2

Note 4: Measurement Condition

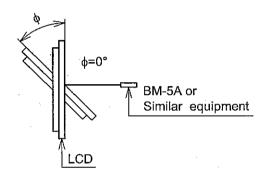
Note 5 : Definition of  $\theta$  and  $\phi$  (Normal)

Viewing direction





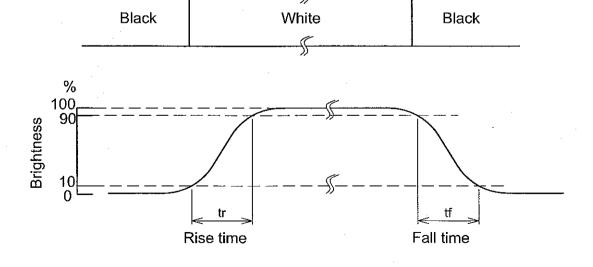
Note 6: Definition of Viewing angle



Note 7: Definition of contrast "K"

K= White Brightness
Black Brightness

Note 8: Definition optical response time



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## 7.BLOCK DIAGRAM I/F(CN1) Data / Clock Timing Controller Timing Signals Gate Power Supply TFT-LCD .ED Control Signal [ Power G320 Touch Panel Circuit Signals D2 D720 Source Driver LED Driving LED B/L Circuit Touch Panel KAOHSIUNG HITACHI 7B64PS 2707-TX09D70VM1CCA-5 PAGE 7-1/1 DATE May.13,'08 No. ELECTRONICS CO.,LTD.

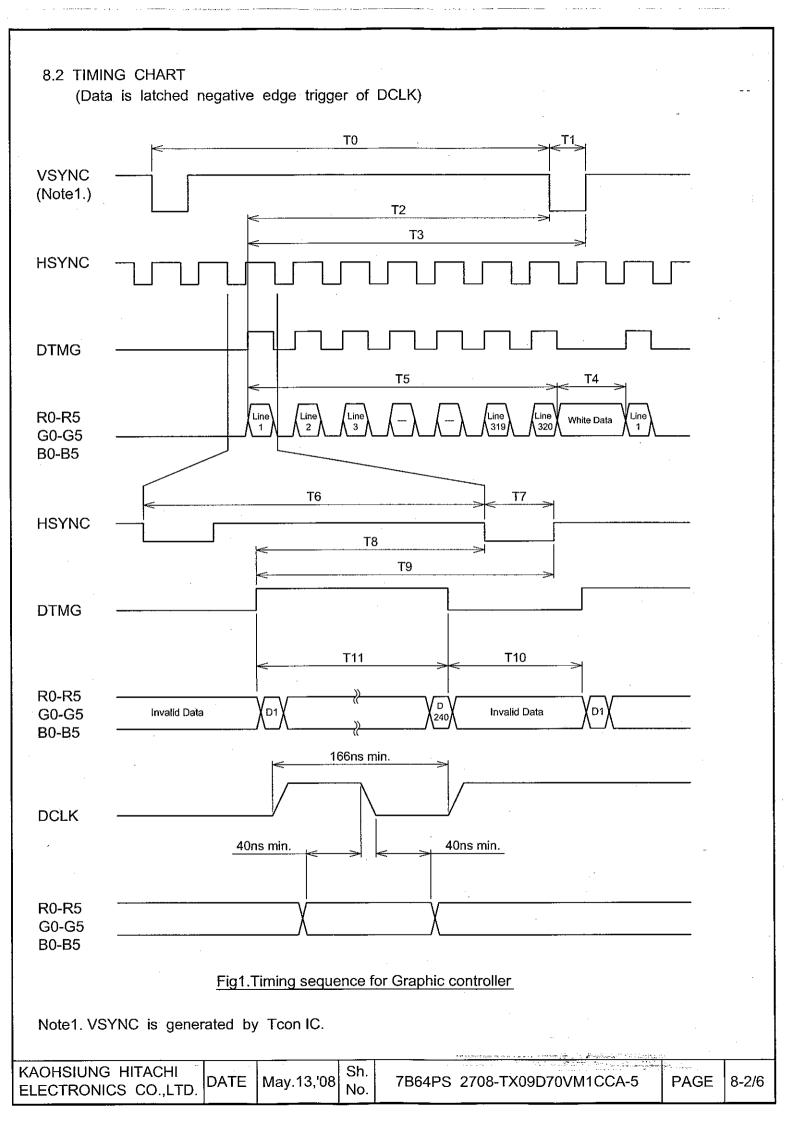
### 8. INTERFACE TIMING

### 8.1 INTERFACE TIMING

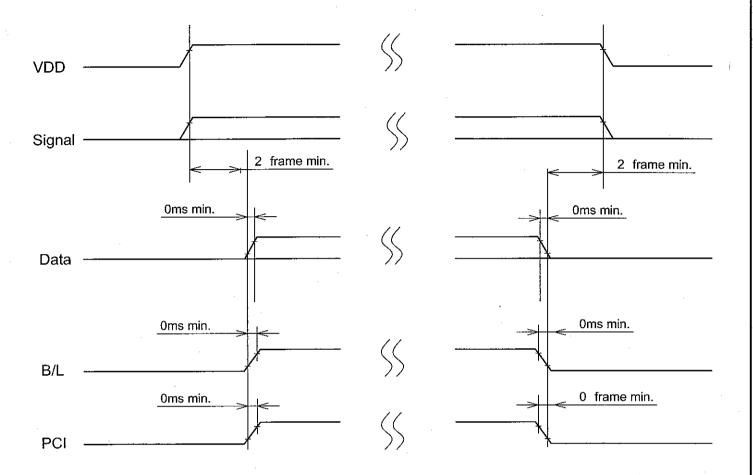
	MIN.	TYP.	MAX.	UNIT	SYMBOL
Vertical Total		327	-	Line	TO ,
Vertical Sync Width	1	1	-	Line	T1
Vertical Sync Start	<u>.</u>	322	-	Line	T2
Vertical Sync End	-	323	-	Line	T3
Vertical Blank Time	5	7	_	Line	T4
Vertical Display End	_	320	<b>-</b> .	Line	T5
Horizontal Total	265	273	509	Pixel Clock	T6
Horizontal Sync Width	4	5	10	Pixel Clock	T7
Horizontal Sync Start	244	251	307	Pixel Clock	T8
Horizontal Sync End	248	256	317	Pixel Clock	T9
Horizontal Blank Time	25	33 .	269	Pixel Clock	T10
Horizontal Display End	_	240	-	Pixel Clock	T11

Note: Vertical Total should be set to odd.

KAOHSIUNG HITACHI		May 12 '09	Sh.	7B64PS 2708-TX09D70VM1CCA-5 PAGE 8-1/	٦
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B84PS 2708-1X09D70VWITCCA-3FAGE 8-17	



### 8.3 POWER ON/OFF SEQUENCE







## 8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA 8.4.1 Display Colors

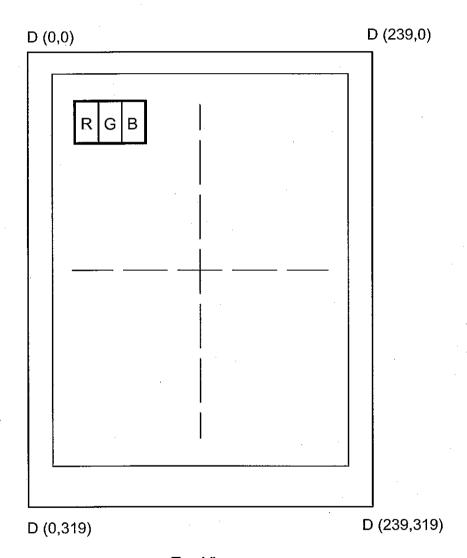
02	ispiay Coi		F	Red	Data	<u>—</u>			Gı	reen	Da	ta			Е	lue	Dat	<u>—</u>	
	Input	R5		R3			R0	G5	_	G3			G0	B5	В4	В3		В1	B0
color .	•	MSI					SB	MS					SB	MS					.SB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(0)	Ō	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	.1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	• •	••	• •	• •	• •	••	• •	• •	•••	•	:		••	:	:
Reu	:	• •	:	:	:	:	:	••		••	:	:	:	:	:	:	:	:	:
	Red(2)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	. 0	0_	0	0	0	0	0	0	0 -	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	<u> </u> :_	<u>:</u>	:	:	:	:	:	:	:	:	:_	:	:	:	:	
010011	:	:	:	:	:		:	:	:	:	:	:	• •	:	:_		:		:
	Green(2)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(1)	0	0	0	0_	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0.	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	. 0	0	1
	Blue(61)	0	0	0	0	0.	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:		:	:	<u>:</u>	:	:	:	;	:	_ : _
	:	:	:	:	:	:	:	:_	:	<u>:</u>	:	:_	:	:	:	:	:	:	:
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0 -	0	0	0	0	0	0	0	0	0	0	0_	1	1	1	1	1	1

KAOHSIUNG HITACHI	DATE	May.13,'08	7B64PS 2708-TX09D70VM1CCA-5 PAGE	8-4/6
ELECTRONICS CO.,LTD.	-,	No.		

### 8.4.2 Data address

D (0,0) D (1,0)

R G B R G B



### 8.5 INTERNAL PIN CONNECTION

CN1 AMP: 1770046-3(Suitable FPC: t0.3±0.03mm , 0.5±0.03mm pitch)

PIN No.	SIGNAL	FUNCTION
1	VDD	Power Supply for Logic
2	VDD	Power Supply for Logic
3	VDD	Power Supply for Logic
4	DCLK	Dot Clock
5	VSS	GND
6	HSYNC	Horizontal Sync Pulse
7	VSS	GND
8	DTMG	Timing Signal for Data
9	VSS	GND
10	NC	No Connection
11	VSS	GND
12	R5	
13	R4	Red Data
14	R3	
15	VSS	GND
16	R2	
17	R1	Red Data
18	. R0	
19	VSS	GND
20	G5	
21	G4	Green Data
22	G3	
23	VSS	GND
24	G2	
25	G1	Green Data
26	G0	
27	VSS	GND
28	B5	
29	B4	Blue Data
30	B3	
31	VSS	GND
. 32	B2	
33	B1	_Blue_Data
34	B0	
35	PCI	Power Control In (Note1)
36	Vctrl	LED Current Control
37	XR	Touch Panel Right Side
38	YL	Touch Panel Lower Side
39	XL	Touch Panel Left Side
40	YU	Touch Panel Upper Side

Note 1. Please follow the page 8-3/6 to set the PCI.

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KAOHSIUNG HITACHI		NA 1	3,00 Sh.	ZDC4DC ÖZOCTV	(09D70VM1CCA-5		9 6/6	
ELECTRONICS CO.,LTD.	DATE	iviay. i	3,'08 No.		(09D/0VMHCCA-5	PAGE	0-0/0	l

### 9. DIMENSIONAL OUTLINE 64±0.3 63.0±0.3(T/P Outline) $0.5\pm0.3$ 57.64±0.2(Bezel opening Area) 3.18±0.3 56.14±0.5(T/P Viewing Area) 3.43±0.3 54.14min.(T/P Active Area) 4.43max. 53.64±0.1(LCD Active Area) 5:18±0.3 52±0.2 (Bezel Opening Area) 4.0±0.3(Except FPC Area) 6±0.15 32±0.3 (1.0)86.0±0.3 83.5±0.3(T/P Outline) 75.52±0.2(Bezel Opening Area) 74.02±0.5(T/P Viewing Area) 72.02min.(T/P Active Area) 71.52±0.1(LCD Active Area) 40.3 H2 H2 2-(2.4) Connector-0 (5.25)(12.08)45±0.3(PCB Outline) CN1 (26.95) (23)0 4-φ3.0 8.05±0.3 Components Area (0.5)(Including Connector Area) 63±0.3(PCB Outline) (0.5)3.12 (2.13) (1.0) Detail B Detail A

Section H2-H2



Scale: NTS Unit: mm

	KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.	DATE	May.13,'08	Sh. No.	7B64PS 2709-T	X09D70VM1CCA-5	PAGE	9-1/1
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(0.55)

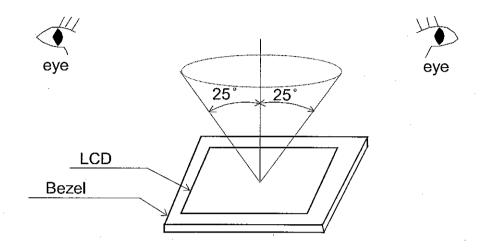
(0.55)

Detail B

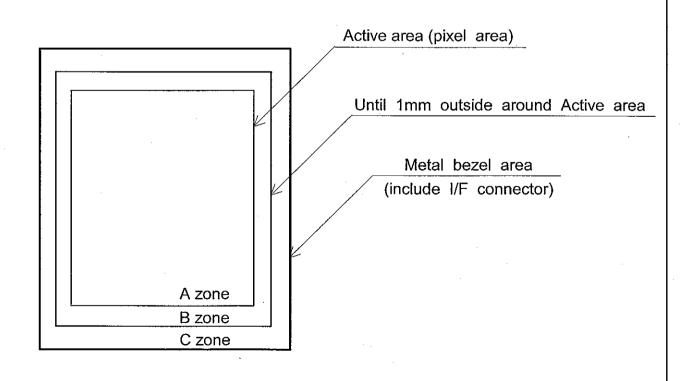
### 10. APPEARANCE STANDARD

## 10.1 APPEARANCE INSPECTION CONDITION Visual inspection should be done under the following condition.

- (1) The inspection should be done in a dark room. (More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure. Viewing angle ≤ 25°



### 10.2 DEFINITION OF ZONE



KAOHSIUNG HITACHI		Sh.	
ELECTRONICS CO.,LTD.	DATE	May.13,'08 No.	7B64PS 2710-TX09D70VM1CCA-5 PAGE 10-1/4

### 10.3 APPEARANCE SPECIFICATION

### (1)LCD Appearance

\*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

No.	ITEM			CRITE	APPLIED ZONE			
	Scratches	Length L(mm)	,	Width W(mm)		Maximum number acceptable		
		L≦2.0 L≦2.0	0.03	$\frac{W \leq 0}{$		ignored 4	A,B	
		L>2.0		05 <w< td=""><td></td><td>none</td><td><u>-</u></td></w<>		none	<u>-</u>	
	Dent		one is acceptable by HITACHI stand				А	
	Wrinkles in Polarizer	Same as abov	е				Α	
	Bubbles	Average D(m		r .	M			
		D≦	0.3	,	·	2	A	
		. 0.3	< D			none	Ī . [	
	Stains	Filam		entous (	Line sh	ape)		
	Foreign	Length		Width		Maximum number		
	Materials	L(mm)		N(mm)		acceptable	A,B	
		L<2.0	-	<b>√</b> ≤0.05		4	_ A,b	
	Dark spot	L≦1.0	0.05	5 <w≦0< td=""><td>).1</td><td>2</td><td></td></w≦0<>	).1	2		
L		,		ound(Do	t shape	)		
_	•	· Average dian	neter D(ı	mm)	M	aximum number		
C						acceptable	_	
			<u>≤0.15</u>		6 4			
D		0.15 <d< td=""><td><u>≦0.2</u></td><td></td></d<>	<u>≦0.2</u>					
		0.2 <d< td=""><td><u></u></td><td colspan="3">none</td><td>-l i</td></d<>	<u></u>	none			-l i	
		The total r			Filamentous + Round=9			
	O-1 T	Those wiped out		· · · · · · · · · · · · · · · · · · ·				
	Color Tone	To be judged b		CHI ST	A			
	Color Uniformity  Dot Defect	Same as abov	e I			NA	A	
	Dot Defect					Maximum number		
						acceptable		
	.*	Sparkle mode	_	1 /	dot	4	-	
		Oparitio mode	<b>,</b>		lots	2(sets)	1	
	•		L		otal	4	1 .	
		Black mode		1 d		4	A,B	
			F		lots	2(sets)	1	
			L		tal	4	1	
		Sparkle mode & Black mode		2 dots		2(sets)	1	
			L	Total		6	<b>-</b>	

KAOHSIUNG HITACHI		Sh	700400 0740 74200 744 600 6 074 05 40 044
ELECTRONICS CO.,LTD.	JA⊺E	May. 13, 08 No	7B64PS 2710-TX09D70VM1CCA-5 PAGE 10-2/4

### (2)Touch panel appearance

Visual inspection should be done under the following condition.

- \*) The inspection should be done in a dark room. (more than 500 (lx) and non-directive)
- \*) The distance between eyes of an inspector and the LCD module is 30 cm.
- \*) The viewing angle ≤ 60°.

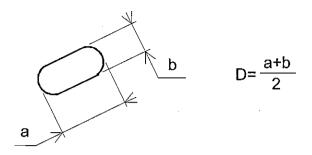
No.	ITEM		CRITE	ERIA		APPLIED ZONE	
	Scratches	Length Width Maximum number L(mm) W(mm) acceptable					
		-		0.05	ignored	A,B	
		10 <l< td=""><td>0.05≦W&lt;</td><td>&lt;0.1</td><td>none</td><td></td></l<>	0.05≦W<	<0.1	none		
		-	0.1≦V	J	none		
_	Foreign		Filamentous	(Line sh	nape)		
T 0	Materials	Length L(mm)	Width W(mm		Maximum number acceptable		
U	Dark Spot	-	W<0.0	5	Ignored	A,B	
C		L>3	0.05≦W≦	<b>≦</b> 0.1	none	_	
'		- '	W≧0.	1	Round		
P			Round(Do	Round(Dot shape)			
A N		Average diame	ge diameter D(mm) Maximum numbe acceptable			A,B	
E	•	D≦0.2	25		ignored	1 .	
L		0.25 <d≦< td=""><td><b>≦0.35</b></td><td></td><td>6</td><td>В</td></d≦<>	<b>≦0.35</b>		6	В	
		0.35<	D		none	A,B	
	Newton Ring (Touch Panel)	To be judged by H		A,B			
	Touch Panel Uncleanness	No conspicuous d		А			
	Rubbing Scratch	To be judged by H	-				

### (3) Glass indentation

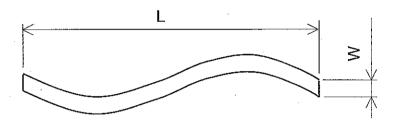
ITEM	SPECIFI	CATIONS
Common Indentation	X Z	X         Y         Z           ≤5.0         ≤3.0         ≤t
Corner Broken	Z	X     Y     Z       ≤3.0     ≤3.0     ≤t
Proceeding Crack		None

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KAOHSIUNG HITACHI		May.13,'08	<b>اان</b>	7B64PS 2710-TX09D70VM1CCA-5 PAGE 10-3/4
ELECTRONICS CO.,LTD.	DAIE	Iviay, 13, 00	No.	TD04F3 21 10-1 X09DT0VIVITECA-9 FAGE 10-3/4

Note 1: Definition of average diameter (D)



Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

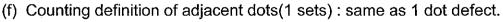
- (a) Dot Defect: Defect Area > 1/2 dot
- (b) Sparkle mode: Brightness of dot is more than 30% at Black raster.
- (c) Black mode: Brightness of dot is less than 70% at R.G.B raster.
- (d) 1 dot: Defect dot is isolated, not attached to other defect dot.
- (e) N dot: N defect dots are consecutive.

(N means the number of defect dots.)

R	G	В	R	G	В	R	G	В
				Х				

2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



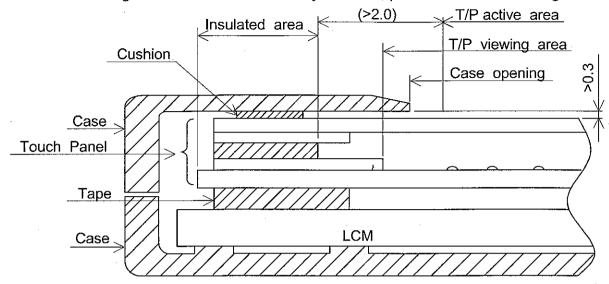
(g) Those wiped out easily are acceptable

KAOHSIUNG HITACHI	DATE	May 12 '00	Sh.	7B64PS 2710-TX09D70VM1CCA-5 PA		10.4/4
ELECTRONICS CO.,LTD.	DATE	May. 13, 08	No.	7864PS 2710-1X09D70VW1CCA-51PA	(GE	10-4/4

#### 11. PRECAUTION IN DESIGN

#### 11.1 MOUNTING PRECAUTION

(1) When assembling the Touch Panel and you case, please refer to the figure below.



- (2) The clearance between the Touch Panel and case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (3) The case shall be designed not to touch the tail portion (FPC for Touch Panel).
- (4) The boundary space between the effective area and the insulated area is unstable. Touching this area may effect the operation of the Touch Panel.

  The case must be designed so that it does not touch the boundary space.

#### 11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

#### 11.3 HANDLING PRECAUTIONS

(1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched, pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 3H.

KAOHSIUNG HITACHI	DATE	May 12 '09	Sh.	7B64PS 2711-TX09D70VM1CCA-5 PAGE 11	1 1/2
ELECTRONICS CO.,LTD.	DATE	May. 13, 00	Vo.	7B04P3 2711-1X09D70VWITCCA-5 FAGE 11	1-1/3

(2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use:

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly.
  - Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.
  - When you need to take out the LCD module from some place at low temperature for test, etc.
  - It is required to be warmed them up to temperature higher than room temperature before taking them out.
- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands. (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than 1.96×10<sup>4</sup> Pa.

  And if the pressure area is less than 1cm<sup>2</sup>, maximum pressure must be less than 1.96N.
- (9) Since the metal width is narrow on these locations (see page 9-1/1), please careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.
  Hard wiping accumulated dust will leave scars on the surface even using a cloth.

#### 11.4 OPERATION PRECAUTION

(1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.

LCM module's should usually be used under recommended operating conditions shown in chapter 5. Exceeding any of these conditions may adversely affect its reliability.

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ELECTRONICS CO.,LTD.	DATE	May.13,′08   Ne	lo.	7B64PS 2711-TX09D70VM1CCA-5 PA	\GE	.11-2/3

- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
  However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40℃ 85%RH.
- (5) Resistance range: Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (polyacetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

#### 11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between -20°C and 70°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

#### 11.6 SAFETY

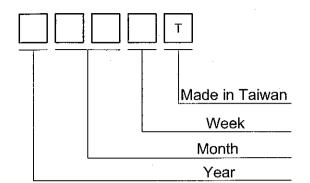
Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

KAOHSIUNG HITACHI	D A TE	10.00	Sh.	7B64PS 2711-TX09D70VM1CCA-5-PA	ACE	44.0/0
ELECTRONICS CO.,LTD.	DATE	May.13, 08	No.	7864P\$ 2711-1X09D7UVM1CGA-5-FRA	AGE	11-3/3

### 12.DESIGNATION OF LOT MARK

### 12.1 LOT MARK

Lot mark is consisted of 4 digits for production lot and 6 digits for production control.



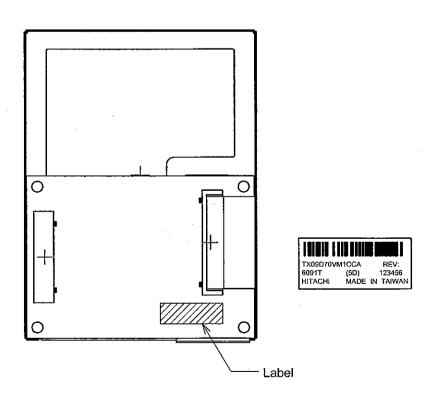
		Se	rial l	No.

Year	Mark
2008	8
2009	9
2010	0
2011	1
2012	2

Month	Jan.	Feb.	Mar.	Apr.	Мау	Jun.
Mark	01	02	03	04	05	06
Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	07	08	09	10	11	12

Week (Day In Calendar)	Figure In Lot Mark
01~07	1
08~14	2
15~21	3
22~28	4
29~31	5

12.2 Location of Label: On the PCB



KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.	DATE	May.13,'08	Sh. No.	7B64PS 2712-TX09D70VM1CCA-5 PAGE	12-1/1	
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#### 13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity. Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
  - 1) When a question is arisen in the specifications.
  - 2) When a new problem is arisen which is not specified in this specifications.
  - 3) When an inspection specifications change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
  - 4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with HITACHI.