







### **DUAL 40V PNP LOW SATURATION SWITCHING TRANSISTOR**

### **Features**

- V<sub>CEO</sub> = -40V
- R<sub>SAT</sub> = 104 mΩ
- I<sub>C</sub> = -3A Continuous Collector Current
- Low Equivalent On Resistance
- Low Saturation Voltage (-220mV @ -1A)
- h<sub>FE</sub> specified up to -3A
- Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)
- "Green" Devices (Note 2)

## **Applications**

DC-DC Converters

**CCFL** Backlighting

- Charging circuits
- Power switches
- Motor control

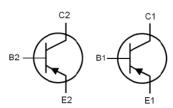
## **Mechanical Data**

- Case: DFN3020B-8
- Case material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Rating 94V-0
- Terminals: Pre-Plated NiPdAu leadframe.
- Nominal package height: 0.8mm
- Moisture Sensitivity: Level 1 per J-STD-020
- Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (approximate)

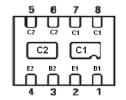
### DFN3020B-8



DFN3020B-8



Device symbol



Pin Configuration

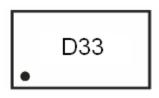
### **Ordering Information**

Product	Status	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTD720MCTA	Active	DFN3020B-8	D33	7	8	3000

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

# **Marking Information**



D33 = Product type Marking Code Dot denotes Pin 1





### **Maximum Ratings**

Parameter	Symbol	Limit	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-40	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7.5	V
Peak Pulse Current	Ісм	-4	Α
Continuous Collector Current (Notes a and b)	Ic	-3	Α
Base Current	I <sub>B</sub>	-1	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation at $T_A = 25^{\circ}C$ (Notes a and f)	Б.	1.5	W
Linear Derating Factor	P <sub>D</sub>	12	mW/°C
Power Dissipation at T <sub>A</sub> = 25°C (Notes b and f)	В	2.45	W
Linear Derating Factor	$P_{D}$	19.6	mW/°C
Power Dissipation at T <sub>A</sub> = 25°C (Notes c and f)	P <sub>D</sub>	1	W
Linear Derating Factor	FD	8	mW/°C
Power Dissipation at T <sub>A</sub> = 25°C (Notes d and f)	P <sub>D</sub>	1.13	W
Linear Derating Factor	FD	9	mW/°C
Power Dissipation at T <sub>A</sub> = 25°C (Notes d and g)	$P_{D}$	1.7	W
Linear Derating Factor	PD	13.6	mW/°C
Power Dissipation at T <sub>A</sub> = 25°C (Notes e and g)	P <sub>D</sub>	3	W
Linear Derating Factor		24	mW/°C
Junction to Ambient (Notes a and f)	$R_{ hetaJA}$	83.3	°C/W
Junction to Ambient (Notes b and f)	$R_{ hetaJA}$	51	°C/W
Junction to Ambient (Notes c and f)	$R_{ hetaJA}$	125	°C/W
Junction to Ambient (Notes d and f)	$R_{ hetaJA}$	111	°C/W
Junction to Ambient (Notes d and g)	$R_{ hetaJA}$	73.5	°C/W
Junction to Ambient (Notes e and g)	$R_{ hetaJA}$	41.7	°C/W
Junction Temperature	TJ	150	°C
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

Notes:

- a. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- b. Measured at t <5 secs for a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.

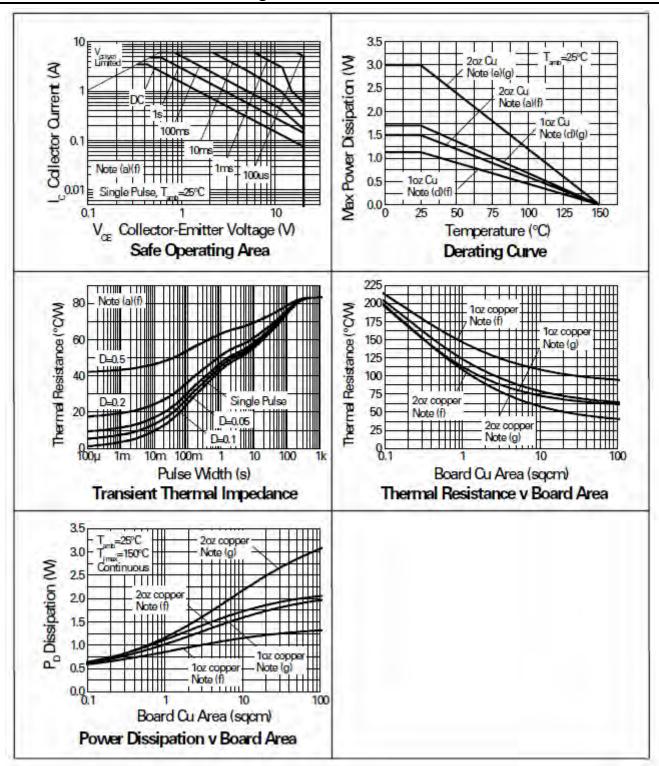
  c. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.

  d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The
- copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- e. For a dual device surface mounted on 85 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- f. For a dual device with one active die.
- g. For dual device with 2 active die running at equal power.





## **Thermal Characteristics and Derating information**







# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

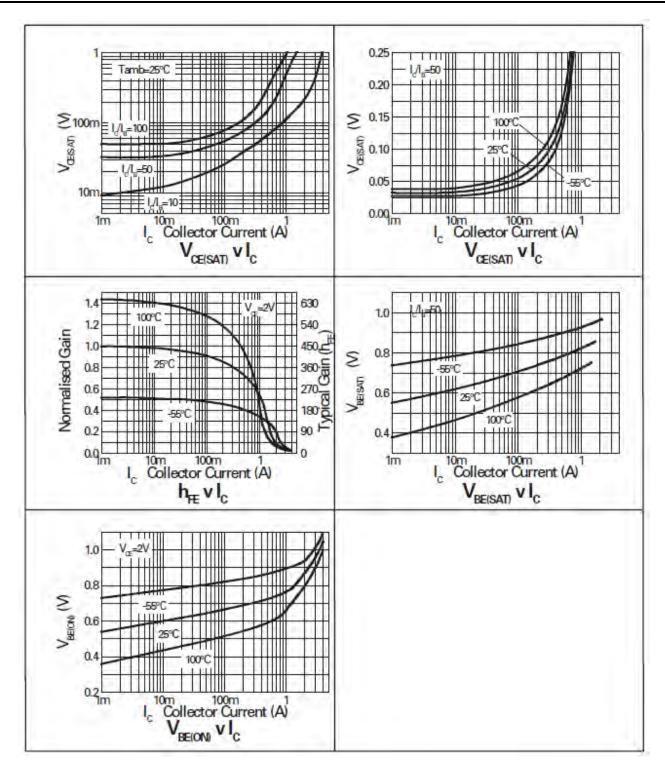
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-50	-80	-	V	$I_{C} = -100 \mu A$
Collector-Emitter Breakdown Voltage (Note 3)	V <sub>(BR)CEO</sub>	-40	-70	-	V	$I_C = -10 \text{mA}$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-7.5	-8.5	-	V	$I_E = -100 \mu A$
Collector Cutoff Current	I <sub>CBO</sub>	-	-	-25	nA	V <sub>CB</sub> = -16V
Emitter Cutoff Current	I <sub>EBO</sub>	-	-	-25	. nA	$V_{EB} = -6V$
Collector Emitter Cutoff Current	I <sub>CES</sub>	-	-	-25	nA	V <sub>CES</sub> = -10V
		300	480	-	-	$I_C = -10 \text{mA}, V_{CE} = -2 \text{V}$
	h <sub>FE</sub>	300	450	-	-	$I_C = -100 \text{mA}, V_{CE} = -2 \text{V}$
Static Forward Current Transfer Ratio (Note 3)		180	290	-	-	$I_{C} = -1A, V_{CE} = -2V$
		60	130	-	-	$I_C = -1.5A$ , $V_{CE} = -2V$
		12	22	-	-	$I_C = -3A$ , $V_{CE} = -2V$
	V <sub>CE(sat)</sub>	-	-25	-40	mV	$I_C = -0.1A$ , $I_B = -10mA$
		-	-150	-220		$I_C = -1A$ , $I_B = -50mA$
Collector-Emitter Saturation Voltage (Note 3)		-	-195	-300		$I_C = -1.5A$ , $I_B = -100mA$
		-	-210	-300		$I_C = -2A$ , $I_B = -200mA$
		-	-260	-370		$I_C = -2.5A$ , $I_B = -250mA$
Base-Emitter Turn-On Voltage (Note 3)	V <sub>BE(on)</sub>	-	-0.89	-0.95	V	I <sub>C</sub> = -2.5A, V <sub>CE</sub> = -2V
Base-Emitter Saturation Voltage (Note 3)	V <sub>BE(sat)</sub>	-	-0.97	-1.05	V	$I_C = -2.5A$ , $I_B = -250mA$
Output Capacitance	C <sub>obo</sub>	-	19	25	pF	V <sub>CB</sub> = -10V. f = 1MHz
Transition Frequency	f <sub>T</sub>	150	190	-	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -50mA, f = 100MHz
Turn-on Time	ton	-	40	-	ns	$V_{CC} = -15V, I_{C} = -0.75A$
Turn-off Time	t <sub>off</sub>	-	435	-	ns	$I_{B1} = I_{B2} = -15\text{mA}$

Notes: 3. Measured under pulsed conditions. Pulse width = 300  $\mu$ s. Duty cycle  $\leq$  2%

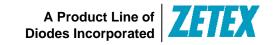




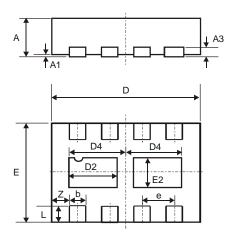
# **Typical Characteristics**





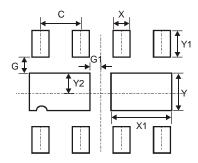


# **Package Outline Dimensions**



DFN3020B-8					
Dim	Min	Max	Тур		
Α	0.77	0.83	0.80		
A1	0	0.05	0.02		
A3	-	-	0.15		
b	0.25	0.35	0.30		
D	2.95	3.075	3.00		
D2	0.82	1.02	0.92		
D4	1.01	1.21	1.11		
е	-	-	0.65		
Е	1.95	2.075	2.00		
E2	0.43	0.63	0.53		
L	0.25	0.35	0.30		
Z	-	-	0.375		
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)
С	0.650
G	0.285
G1	0.090
Х	0.400
X1	1.120
Y	0.730
Y1	0.500
Y2	0.365





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