SPECIFICATION FOR APPROVAL

() Preliminary Specification

(♦) Final Specification

Title	19.0" SXGA TFT LCD
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BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD CO., Ltd.
*MODEL	LM190E01
SUFFIX	C4

*When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
1	

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	DATE			
G T. Kim / G.Manager				
REVIEWED BY				
K.J. Kwon / Manager				
PREPARED BY				
K N. Kim / Engineer				
Product Engineering Dept. LG. Philips LCD Co., Ltd				

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RECORD OF REVISIONS

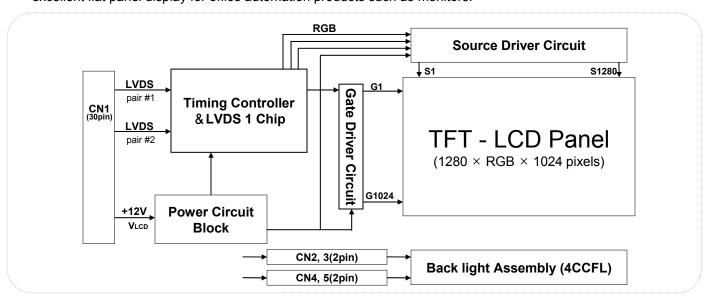
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1. General Description

LM190E01-C4 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 19.0 inch diagonally measured active display area with SXGA resolution (1024 vertical by 1280 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, a palette of more than 16,777,216 colors. It has been designed to apply the 8Bit parallel CMOS interface. It is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LM190E01-C4 characteristics provide an excellent flat panel display for office automation products such as monitors.



General Features

19.0 inches (481.84mm) diagonal	
404.2(H) x 330.0(V) x 22.3(D) mm(Typ.)	
0.294 mm x 0.294 mm	
1280 horizontal By 1024 vertical Pixels. RGB stripe arrangement	
LVDS 2Port	
8-bit, 16,777,216 color	
250 cd/m ² (Center 1Point, typ)	
Viewing Angle Free [R/L 176(Typ.), U/D 176(Typ.)]	
Total 22.94 Watt(Typ.), (3.29 Watt @V _{LCD} , 19.65 @[Lamp=7.5mA])	
2,800g (Typ.)	
Transmissive mode, normally Black	
Hard coating (3H), Anti-glare treatment of the front polarizer	

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2. Absolute Maximum Ratings

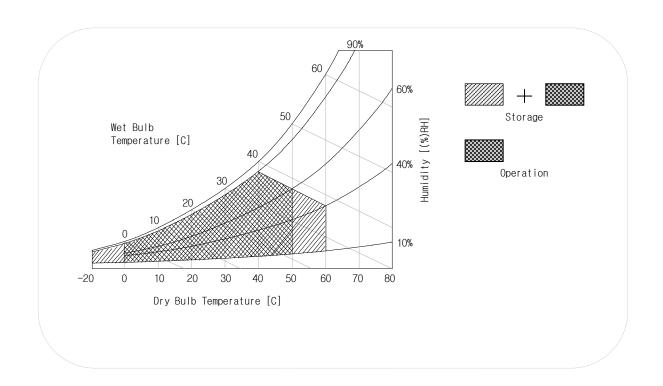
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. Absolute Maximum Ratings

Parameter	Cymbol	Val	ues	Units	Notes
Parameter	Symbol	Min	Max	Units	Notes
Power Supply Input Voltage	V_{LCD}	-0.3	+14.0	Vdc	At 25℃
Operating Temperature	T _{OP}	0	50	°C	
Storage Temperature	T _{ST}	-20	60	°C	1
Operating Ambient Humidity	H _{OP}	10	90	%RH	1
Storage Humidity	H _{ST}	10	90	%RH	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

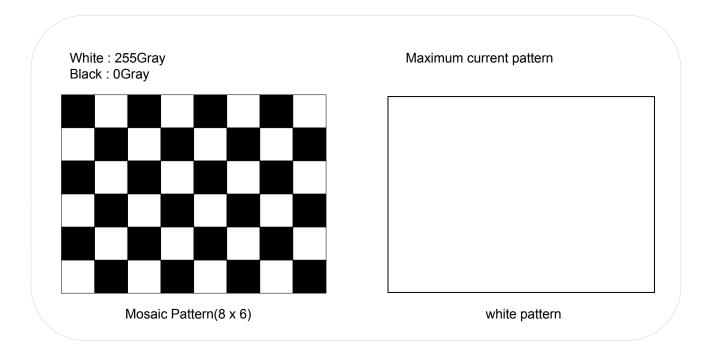
It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL/Backlight, is typically generated by an inverter. The inverter is an external unit to the LCDs.

Table 2_1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
T drameter	Cymbol	Min	Тур	Max	Onit	110100
MODULE :	-					
Power Supply Input Voltage	V_{LCD}	11.4	12.0	12.6	Vdc	
Davier Cumply Input Cumput		-	274	315	mA	1
Power Supply Input Current	LCD	-	360	468	mA	2
Power Consumption	P _{LCD}	-	3.29	3.78	Watt	1
Inrush current	I _{RUSH}	-	-	3.0	Α	3

Note:

- 1. The specified current and power consumption are under the VLCD=12.0V, $25 \pm 2^{\circ}$ C, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern below
- 3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).



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Table 2_2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Values			Unit	Notes
i aram	Cici	Gyrribor	Min	Тур	Max	Offic	140163
LAMP :							
Operating Volta	age	V_{BL}	645(8.0mA)	655(7.5mA)	790(3mA)	V_{RMS}	1, 2
Operating Curr	ent	I _{BL}	3.0	7.5	8.0	mA _{RMS}	1
Established Starting Voltage		Vs					1, 3
	at 25 °C				1,100	V_{RMS}	
	at 0 °C				1,400	V_{RMS}	
Operating Freq	Operating Frequency		40	60	70	kHz	4
Discharge Stabilization Time		T _s			3	Min	1, 5
Power Consumption		P_{BL}		19.65	21.62	Watt	6
Life Time			40,000			Hrs	1, 7

Note: The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.

- Do not attach a conducting tape to lamp connecting wire.
 If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.
- 1. Specified values are for a single lamp.
- 2. Operating voltage is measured at 25 \pm 2°C. The variance of the voltage is \pm 10%.
- 3. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. (Inverter open voltage must be more than lamp starting voltage.)
 Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 4. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 5. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The lamp power consumption shown above does not include loss of external inverter.

 The used lamp current is the lamp typical current (P_x = V_x x I_x x N_y)
- The used lamp current is the lamp typical current. ($P_{BL} = V_{BL} \times I_{BL} \times N_{Lamp}$)

 7. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C.

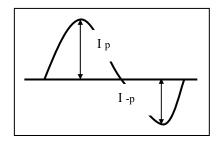
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- 8. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
 - Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.

It shall help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$|I_{p} - I_{-p}| / I_{rms} \times 100\%$$

* Distortion rate

$$I_p (or I_{-p}) / I_{rms}$$

- 9. The inverter which is combined with this LCM, is highly recommended to connect coupling(ballast) condenser at the high voltage output side. When you use the inverter which has not coupling(ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.
- 10.In case of edgy type back light with over 4 parallel lamps, input current and voltage wave form should be synchronized

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3-2. Interface Connections

- —LCD Connector(CN1): GT101-30S-H16 (Manufactured by LGC) or Equivalent
- -Mating Connector : FI-X30H (Manufactured by JAE) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No	Symbol	Description	
1	FR0M	Minus signal of odd channel 0 (LVDS)	
2	FR0P	Plus signal of odd channel 0 (LVDS)	
3	FR1M	Minus signal of odd channel 1 (LVDS)	
4	FR1P	Plus signal of odd channel 1 (LVDS)	
5	FR2M	Minus signal of odd channel 2 (LVDS)	
6	FR2P	Plus signal of odd channel 2 (LVDS)	First data
7	GND	Ground	i iist data
8	FCLKINM	Minus signal of odd clock channel (LVDS)	
9	FCLKINP	Plus signal of odd clock channel (LVDS)	
10	FR3M	Minus signal of odd channel 3 (LVDS)	
11	FR3P	Plus signal of odd channel 3 (LVDS) ———	
12	SR0M	Minus signal of even channel 0 (LVDS)	
13	SR0P	Plus signal of even channel 0 (LVDS)	
14	GND	Ground	
15	SR1M	Minus signal of even channel 1 (LVDS)	
16	SR1P	Plus signal of even channel 1 (LVDS)	
17	GND	Ground	Second data
18	SR2M	Minus signal of even channel 2 (LVDS)	
19	SR2P	Plus signal of even channel 2 (LVDS)	
20	SCLKINM	Minus signal of even clock channel (LVDS)	
21	SCLKINP	Plus signal of even clock channel (LVDS)	
22	SR3M	Minus signal of even channel 3 (LVDS)	
23	SR3P	Plus signal of even channel 3 (LVDS) ——	'
24	GND	Ground	
25	NC	No Connection	
26	NC	No Connection	
27	NC	No Connection	
28	VLCD	Power Supply +12.0V	
29	VLCD	Power Supply +12.0V	
30	VLCD	Power Supply +12.0V	



Note: 1. NC: No Connection.

- 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 3. All VLCD (power input) pins should be connected together.
- 4. Input Level of LVDS signal is based on the IEA 664 Standard.

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Table 4. REQUIRED SIGNAL ASSIGNMENT FOR Flat Link(TI:SN75LVDS83) Transmitter

Pin#	Pin Name	Require Signal	Pin#	Pin Name	Require Signal
1	Vcc	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T _X CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL Vcc	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	Vcc	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3-	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	T _X CLKOUT +	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T _X CLKOUT -	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T _X OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T _X OUT2 –	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS Vcc	Power Supply for LVDS
17	Vcc	Power Supply for TTL Input	45	T _X OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	T _X OUT1 –	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T _X OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T _X OUT0 -	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	Vcc	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

Notes: Refer to LVDS Transmitter Data Sheet for detail descriptions.

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Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3,CN4,CN5)

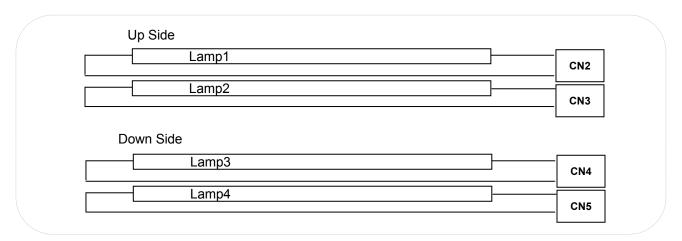
The backlight interface connector is a model 1674817-1(CN2/CN3/CN4/CN5) manufactured by AMP. The mating connector part number are SM02B-BHSS-1-TB(2pin) or equivalent. The pin configuration for the connector is shown in the table below.

No	Pin	Symbol	Description	Notes
CN2	1	HV	Power supply for lamp 1(High voltage side) - Gray	1
CINZ	2	LV	Power supply for lamp 1(Low voltage side) - Black	2
CN3	1	HV	Power supply for lamp 2(High voltage side) - Sky Blue	1
CINS	2	LV	Power supply for lamp 2(Low voltage side) - Blue	2
CN4	1	HV	Power supply for lamp 3(High voltage side) - Gray	1
CIN4	2	LV	Power supply for lamp 3(Low voltage side) - Black	2
CN5	1	HV	Power supply for lamp 4(High voltage side) - Sky Blue	1
CIND	2	LV	Power supply for lamp 4(Low voltage side) - Blue	2

Notes:

- 1. The high voltage power terminal is colored gray, sky blue.
- 2. The low voltage pin color is black, blue.

<BACKLIGHT CONNECTOR DIAGRAM>



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3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 6. Timing Table

ITEM	Symbol		Min	Тур	Max	Unit	Note
DOLK	Period	t _{CLK}	23.65	18.52	14.82	ns	
DCLK	Frequency	-	42.3	54.0	67.5	MHz	
	Period	t _{HP}	664	844	1024	t _{CLK}	
Hsync	Frequency	f _H	50.10	63.98	79.95	KHz	
	Width	t _{WH}	8	56	-	t _{CLK}	
	Period	t _{VP}	1034	1066	1098	t _{HP}	
Vsync	Frequency	f _V	47	60	75	Hz	
	Width	t _{wv}	2	3	-	t _{HP}	
	Horizontal Valid	t _{HV}	640	640	640		
	Horizontal Back Porch	t _{HBP}	8	124	-		
	Horizontal Front Porch	t _{HFP}	8	24	-	t _{CLK}	
DE (Date	Horizontal Blank	-	24	-	t _{HP} - t _{HV}		
(Data Enable)	Vertical Valid	t _{VV}	1024	1024	1024		
	Vertical Back Porch	t_{VBP}	1	38	-	4	
	Vertical Front Porch	t _{VFP}	1	1	-	t _{HP}	
	Vertical Blank	-	10	-	t _{VP} - t _{VV}		

Note:

Hsync period and Hsync width-active should be even number times of tclk.

If the value is odd number times of tolk, display control signal can be asynchronous.

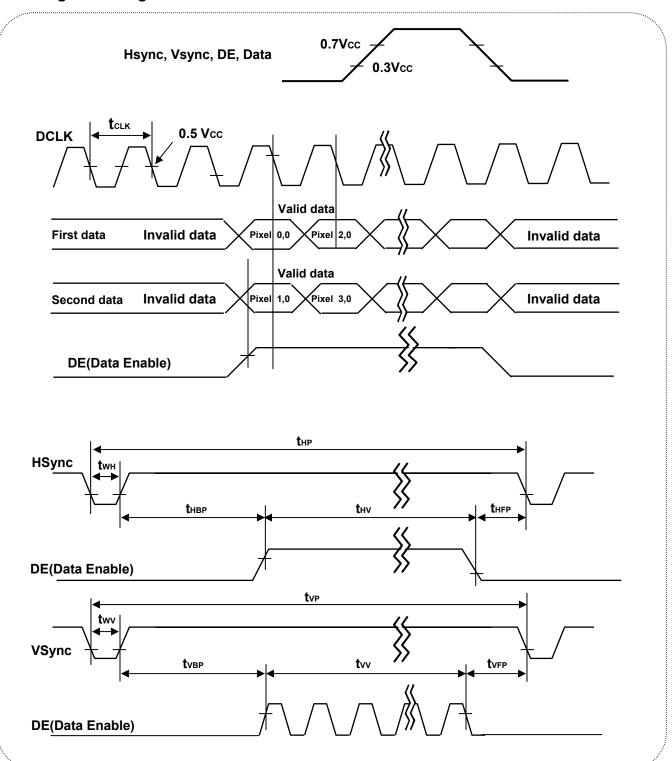
In order to operate this LCM a Hsync, Vsync and DE (Data Enable) signals should be used.

- 1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 2. Vsync, Hsync should be keep the above specification.
- 3. Hsync Period, Hsync Width and Horizontal Back Porch should be any times of a character number (8).

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3-4. Signal Timing Waveforms



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3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 9. COLOR DATA REFERENCE

											Inj	out	Co	lor	Da	ata									
	Color				R	ed							Gre	en							BI	ue			
		MS				<u></u>	<u></u>		SB		SB	0-		00			SB	M:				<u></u>		г -	SB
																								B1	BO
Basic Color	Black Red (255) Green (255) Blue (255) Cyan Magenta Yellow White	0 1 0 0 1 1	01000111	0 1 0 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 1 0 1 1	0 1 0 1 0 1	0 0 1 0 1 0 1	0 1 0 1 0 1	0 0 1 0 1 1	0 0 1 0 1 0 1	0 1 0 1 0 1	0 1 0 1 0 1	0 0 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0	0 0 1 1 1 0	0 0 1 1 0 1	0 0 1 1 1 0
Red	Red(000) Dark Red(001) Red(002) Red(253) Red(254) Red(255) Bright	0 0 - 1 1	000111	000-111	0 0 - 1 1	0 0 0 - 1 1	0 0 0 - 1 1	0 1 - 0 1 1	0 1 0 - 1 0 1	00011000	0 0 0 - 0 0 0	00011000	0 0 0 - 0 0 0	00011000	00011000	0 0 0 - 0 0 0	0 0 0 - 0 0 0	000 000	00011000	00011000	00011000	0 0 0 - 0 0 0	0 0 0 - 0 0 0	00011000	0 0 0 0 0 0
Green	Green(000) Dark Green(001) Green(002) Green(253) Green(254) Green(255) Bright	00011000	00011000	00011000	00011000	0 0 0 0 0 0	0 0 0 - 0 0 0	00011000	00011000	000111	0 0 0 - 1 1	000111	0 0 0 - 1 1	00011111	000111	0 0 1 - 0 1 1	0 1 0 - 1 0 1	00011000	00011000	00011000	00011000	0 0 0 0 0 0	0 0 0 0 0 0	00011000	00011000
Blue	Blue(000) Dark Blue(001) Blue(002) Blue(253) Blue(254) Blue(255) Bright	00011000	00011000	00011000	00011000	0 0 0 - 0 0 0	0 0 0 - 0 0	00011000	00011000	00011000	0 0 0 - 0 0 0	00011000	0 0 0 - 0 0 0	00011000	00011000	0 0 0 - 0 0 0	0 0 0 - 0 0 0	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 0 - 1 1	0 0 0 - 1 1	0 1 - 0 1	0 1 0 - 1 0 1

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3-6. Power Sequence

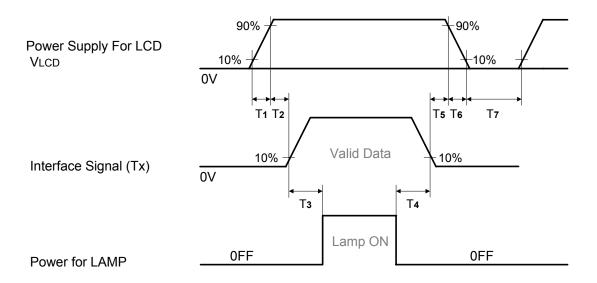


Table 8. POWER SEQUENCE

Parameter		Values		Units
Parameter	Min	Тур	Max	Offics
T1	1	-	10	ms
T2	0.5	-	50	ms
Т3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	-	-	100	ms
T7	1	-	-	ms

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
- 3. Lamp power must be turn on after power supply for LCD an interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' for 30 minutes in a dark environment at 25° C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 presents additional information concerning the measurement equipment and method.

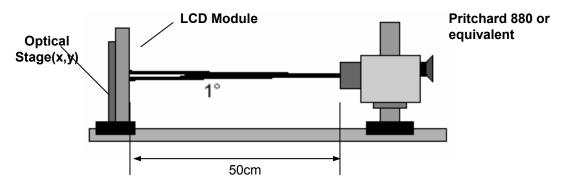


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 9. OPTICAL CHARACTERISTICS

Ta= 25°C, V_{LCD}=12.0V, fv=60HzDclk=54MHz, I_{BL}=7.5mA

				, -LCD	,	,	
Deven	4	Cymala al		Values		Lluito	Notes
Paramo	eter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio		CR	250	400	-		1
Surface Luminanc	e, white	L _{WH}	200	250	-	cd/m ²	2
Luminance Variation	on	δ _{WHITE} 5P			1.25		3
Dannana Tima	Rise Time	Tr _R	-	12	20	ms	4
Response Time	Decay Time	Tr_D	-	13	20	ms	4
	DED	Rx		0.640			
	RED	Ry		0.344			
	GREEN	Gx		0.299			
Color Coordinates	GREEN	Gy	Тур	0.602	Тур		
[CIE1931]	BLUE	Bx	-0.03	0.145	+0.03		
	BLUE	Ву		0.074			
	WHITE	Wx		0.313			
	VVIIIE	Wy		0.329			
Viewing Angle (CR	>10)						
x axis	s, right(φ=0°)	θr	85	88		degree	5
x axis	s, left (φ=180°)	θΙ	85	88			
y axis	s, up (φ=90°)	θu	85	88			
y axis	s, down (φ=270°)	θd	85	88			
Gray Scale				-			6

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Notes:

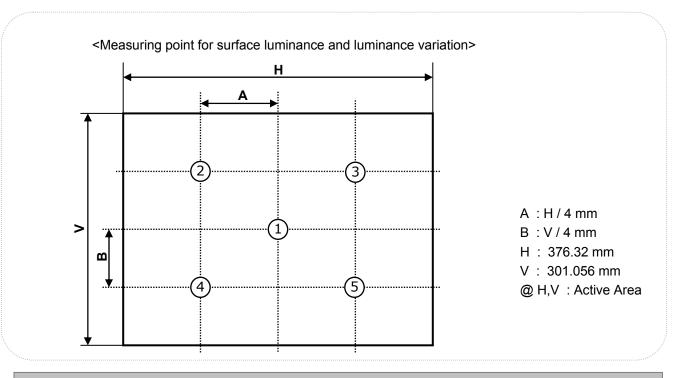
1. Contrast ratio(CR) is defined mathematically as :

It is measured at center point(1)

- 2. Surface luminance is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 6.
- 3. The variation in surface luminance , $\delta_{\text{ WHITE}}$ is defined as

For more information see [Figure 6].

Figure 6. Luminance measuring point



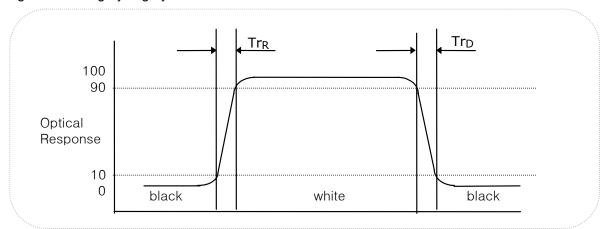
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4. Response time is the time required for the display to transition from black to white (Rise Time, Tr_R) and from white to black (Decay Time, Tr_D). For additional information see [Figure 7]. The sampling rate is 2,500 sample/sec.

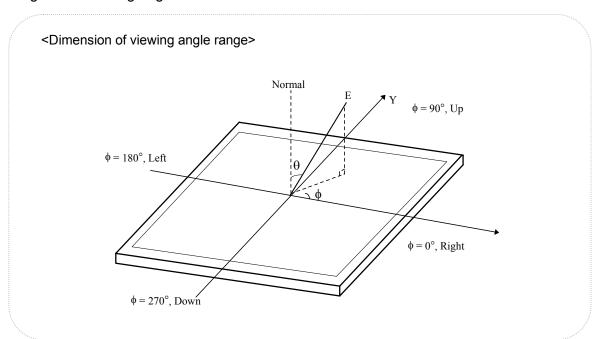
Figure 7. Response time

The response time is defined as the following figure and shall be measured by switching the input signal for each gray to gray.



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 8.

Figure 8. Viewing angle



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6. Gray scale specification

Table 12. Gray scale

Gray Level	Luminance [%] (Typ)	Gray Level	Luminance [%] (Typ)
L0	0.30	L143	33.2
L15	0.46	L159	42.4
L31	1.37	L175	52.4
L47	3.05	L191	62.4
L63	5.69	L207	73.0
L79	9.59	L223	83.6
L95	14.4	L239	93.0
L111	19.8	L255	100
L127	25.3		

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5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

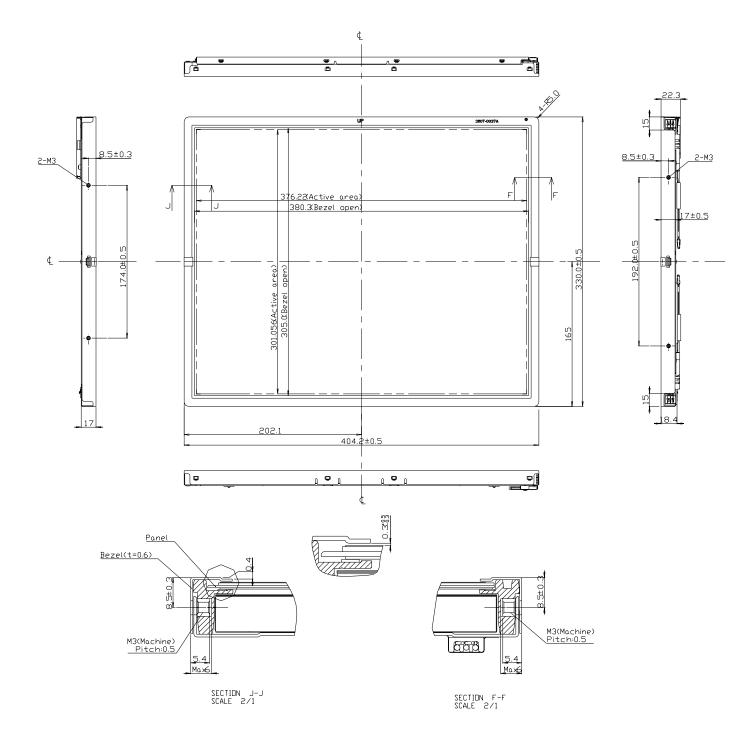
	Horizontal	404.20mm				
Outline Dimension	Vertical	330.0mm				
	Depth	22.3 mm				
Bezel Area	Horizontal	380.3 mm				
Dezel Alea	Vertical	305mm				
Activo Diaplay Area	Horizontal	376.32mm				
Active Display Area	Vertical	301.056mm				
Weight	2,800g (Typ.) 2,940(Max)					
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer					

Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.

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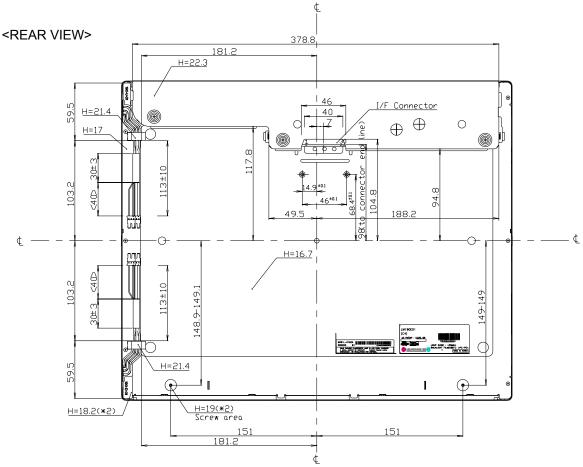


<FRONT VIEW>



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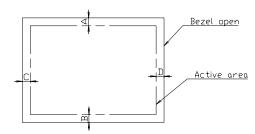




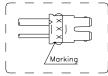
Notes

- Backlight: 4 Cold Cathode Fluorescent Lamps
 I/F Connector Specification: GT101-30S-H16(LGC)/IN-30-BA 10(UJU)
 Lamp Connector Specification

- 1674817-1(AMP) or Compatible
 4. Torque of user hole: 3.0~4.0kgf-cm
 5. Tilt and partial disposition tolerance of display area as following
 (1) Y-Direction: IA-BI <= 1.0
 (2) X-Direction: IC-DI <= 1.0



6. Lamp(CCFL) olt No. is marked at backlight connector



- 7. Do not wind conductive tape around the backlight wires 8. Unspecified tolerances to be $\pm 0.5 \text{mm}$



6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration : X,Y,Z, 20 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Altitude operating storage / shipment	0 - 10,000 feet(3048m) 0 - 40,000 feet(12,192m)

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

c) EN 60950 : 2000, Third Edition

IEC 60950: 1999, Third Edition

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L M

A,B,C: Inch
D: Year
E: Month
F: Panel Code
G: Factory Code
H: Assembly Code
I,J,K,L,M: Serial No

Note

1. Year

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. Month

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

3. Panel Code

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	Н

4. Factory Code

Factory Code	LPL Gumi	LPL Nanjing	
Mark	K	С	

5. Serial No

Serial No.	1 ~ 99,999	100,000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

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b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 5 pcs

b) Box Size : 533mm \times 310mm \times 459mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metal foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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