

# HIGH POWER DUAL OPERATIONAL AMPLIFIER

154

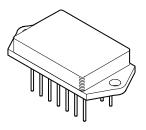
M.S.KENNEDY CORP.

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(315) 701-6751

#### **FEATURES:**

- · Space Efficient Dual Power Amplifier
- Low Cost
- · High Voltage Operation: 150V
- Low Quiescent Current: 50mA Typ. TotalHigh Output Current: 5A Min. Per Amp
- No Second Breakdown
  High Speed: 27V/µS Typ.
  Monolithic MOS Technology
- Internal Compensation For Gains Greater Than 10 V/V



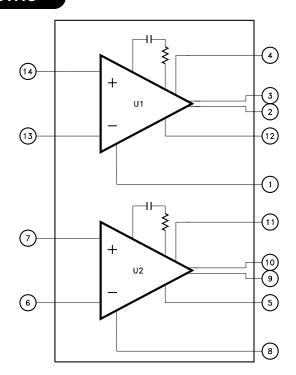
MIL-PRF-38534 QUALIFIED

**MSK154** 

#### **DESCRIPTION:**

The MSK 154 is a high power dual monolithic MOSFET operational amplifier ideally suited for high power amplification and magnetic deflection applications. With a total supply voltage rating of 150 volts and 5A of available output current per amplifier, the MSK 154 is also an excellent low cost choice for motor drive circuits. With both amplifiers in the same package, thermally induced output offset voltages are eliminated. The MOSFET output frees the MSK 154 from secondary breakdown limitations and power dissipation is kept to a minimum with a total quiescent current rating of only 50mA. The MSK 154 is packaged in a hermetically sealed 14 pin power dip with heat sink bolt down tabs.

# **EQUIVALENT SCHEMATIC**



# TYPICAL APPLICATIONS

- . PA Audio
- . Magnetic Deflection
- Motor Drive
- · Noise Cancellation
- · High Power Bridge Amplifier

# **PIN-OUT INFORMATION**

- 1 -Vcc1
- 2 Output Drive 1B
- 3 Output Drive 1A
- 4 + Vcc1

1

- 5 Current Sense 2
- 6 Inverting Input 2
- 7 Non Inverting Input 2
- 14 Non Inverting Input 1
- 13 Inverting Input 1
- 12 Current Sense1
- 11 + Vcc 2
- 10 Output Drive 2A
  - 9 Output Drive 2B
- 8 -Vcc 2

# **ABSOLUTE MAXIMUM RATINGS**

Vcc ②	Total Supply Voltage	Тsт	Storage Temperature65°C to +150°C
± <b>І</b> оит	Output Current (within S.O.A.) ±5A	$T_LD$	Lead Temperature 300°C
$V_{IND}$	Input Voltage (Differential) ± 16V	Tc	Case Operating Temperature
$V_{IN}$	Input Voltage (Common Mode) ± Vcc		(MSK154B)55°C to +125°C
<b>T</b> J	Junction Temperature		(MSK154)40°C to +85°C
		RTH	Thermal Resistance (DC)
			Junction to Case 2.0°C/W

# **ELECTRICAL SPECIFICATIONS**

Parameter	Test Conditions ①	Group A	MSK154B		MSK154 ⑤				
Parameter		Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
STATIC									
Supply Voltage Range ② ④			±15	±50	± 75	±15	±50	±75	V
	Each Amp VIN = 0V	1	-	± 25	±50	-	± 25	±50	mA
Quiescent Current		2	-	± 20	±45	-	-	-	mA
	V IIV — U V		-	± 30	±65	-	-	-	mA
INPUT									
Offset Voltage	VIN = OV	1	-	±5	±10	-	± 5	±15	mV
Offset Voltage Drift ④	VIN = 0V	2,3	-	±10	±50	-	±10	-	μV/°C
Offset Voltage vs ±Vcc 4	VIN = 0V	1	-	±8	± 15	-	±8	±15	$\mu$ V/V
Input Bias Current ④	Vcm=0V	1,3	-	± 20	± 100	-	± 20	± 100	pА
		2	-	-	±50	-	-	-	nA
Input Impedance ④	(DC)	-	-	1011	-	-	10 11	-	Ω
Input Capacitance ④		-	-	5	-	-	5	-	pF
Common Mode Rejection ④	Vcm = ±30VDC	-	90	106	-	90	106	-	dB
Noise ④ 10KHz BW		-	-	10	-	1	10	-	μ <b>V</b> RMS
оитрит									
Output Voltage Swing	RL = 10K	4	±45	±47	-	±45	±47	-	V
Output Voltage Swing IouT = 5A Pk		4	±40	±42	-	±40	±42	-	٧
Power Bandwidth 4 Vout = 80VPP		-	-	66	-	-	66	-	KHz
Settling Time to 0.1% 3 4	10V Step	-	-	2	-	-	2	-	μS
Capacitive Load ④ Av = +10V/V		-	10	-	-	10	-	-	nF
TRANSFER CHARACTERISTICS									
Slew Rate		4	20	27	-	20	27	-	V/μS
Open Loop Voltage Gain ④ F = 15Hz		4	94	106	-	94	106	-	dB

## **NOTES:**

① Unless otherwise noted  $\pm Vcc = \pm 50VDC$  and specifications apply to each amplifier. ② Derate maximum supply voltage  $0.5V/^{\circ}C$  below  $Tc = +25^{\circ}C$ . No derating is needed above  $Tc = 25^{\circ}C$ . ③ Av = -10V/V measured in false summing junction circuit.

<sup>(</sup>in Devices shall be capable of meeting the parameter, but need not be tested. Typical parameters are for reference only.

⑤ Industrial grade devices shall be tested to subgroups 1 and 4 unless otherwise requested.

Military grade devices ('B' suffix) shall be 100% tested to subgroups 1,2,3 and 4.
 Subgroup 5 and 6 testing available upon request.

<sup>(8)</sup> Subgroup 1,4 Tc = +25°C Subgroup 2,5 Tc = +125°C

Subgroup 3,6  $T_A = -55$  °C

# **APPLICATION NOTES**

#### CURRENT LIMIT (SEE TYPICAL CONNECTION DIAGRAM)

A value of current limit resistance can be calculated as follows:

$$RcL = (0.83 - (0.05 * IcL)) / IcL$$

#### Where:

RCL is the current limit resistor value ICL is the current limit

0.05 \* IcL is the voltage dropped in the current limit path across internal impedances other than the actual current limit resistor

0.83 volts is the voltage drop that must be developed across the current limit connections to activate the current limit circuit

The maximum practical value of current limit resistance is 16 ohms. The current limit resistor will decrease available output voltage swing in the following manner:

VR is the reduction in output voltage swing due to the current limit resistor. It is recommended the user limit output current to a value as close to the required output current as possible, without clipping output voltage swing. Current limit will vary with case temperature. Refer to the typical performance curves to predict current limit drift. If current limit is not required replace the resistor with a short.

#### STABILITY

It is recommended that the parallel sum of the input and feed-back resistor be 1000 ohms or less to minimize phase shift caused by the R-C network formed by the input resistor, feed-back resistor and input capacitance. An effective method of checking amplifier stability is to apply the worst case capacitive load to the output of the amplifier and drive a small signal square wave across it. If overshoot is less than 25%, the system will typically be stable.

#### INPUT PROTECTION

Input protection circuitry within the MSK 154 will clip differential input voltages greater than 16 volts. The inputs are also protected against common mode voltages up to the supply rails as well as static discharge. There are 300 ohm current limiting resistors in series with each input. These resistors may become damaged in the event the input overload is capable of driving currents above 1mA. If severe overload conditions are expected, external input current limiting resistors are recommended.

### POWER SUPPLY DECOUPLING

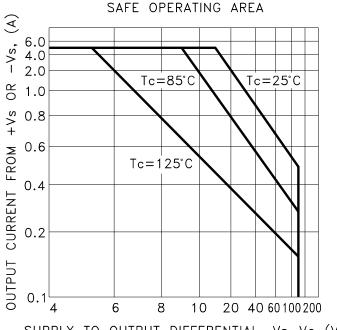
A 0.1 microfarad ceramic disc and low ESR capacitor with a value of 10 microfarads per amp of output current should be placed in parallel from each power supply pin to ground. These capacitors must be rated for the full power supply voltage. Since the MSK 154 is commonly used in circuits where the loop gain is greater than 10 V/V, high frequency noise that enters the op-amp through the power supply lines will be amplified and could cause the amplifier to break into oscillation. In addition, without supply bypassing, the inductance of the power supply lines interacts with capacitive loads to form an oscillatory LC tank circuit. The power supply decoupling capacitors will minimize this effect and keep the circuit stable.

#### SAFE OPERATING AREA (SOA)

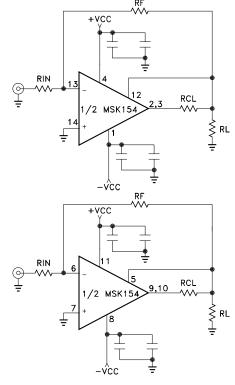
The MOSFET output stage of this power operational amplifier has two distinct limitations:

- 1. The current handling capability of the die metallization.
- 2. The junction temperature of the output MOSFET's.

NOTE: The output stage is protected against transient flyback. However, for protection against sustained, high energy flyback, external fast-recovery reverse biased diodes should be connected from the output to ground.

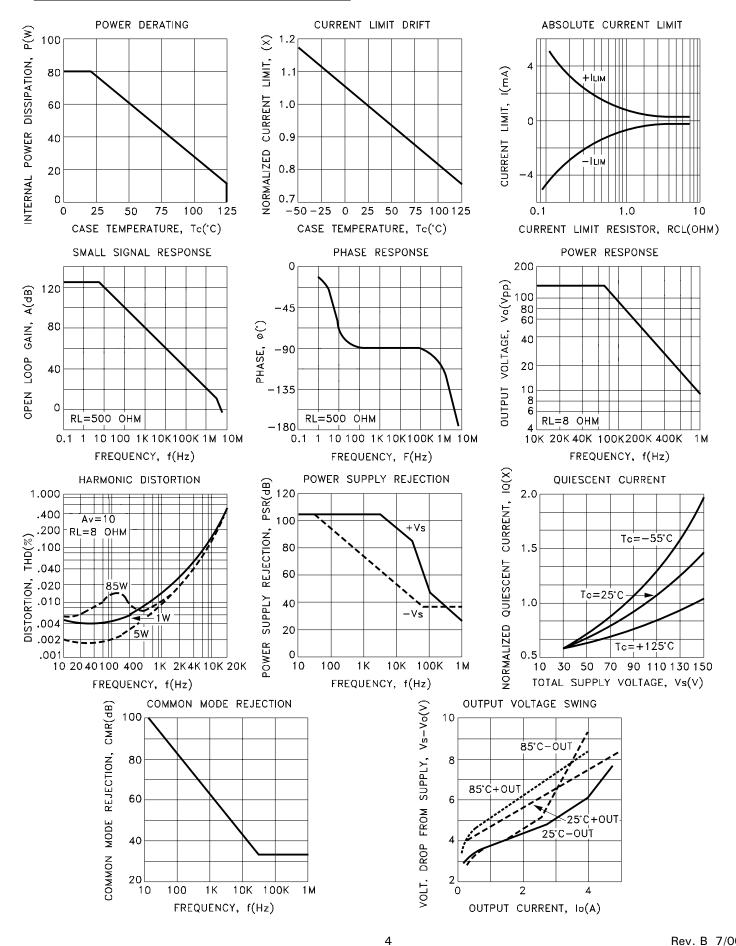


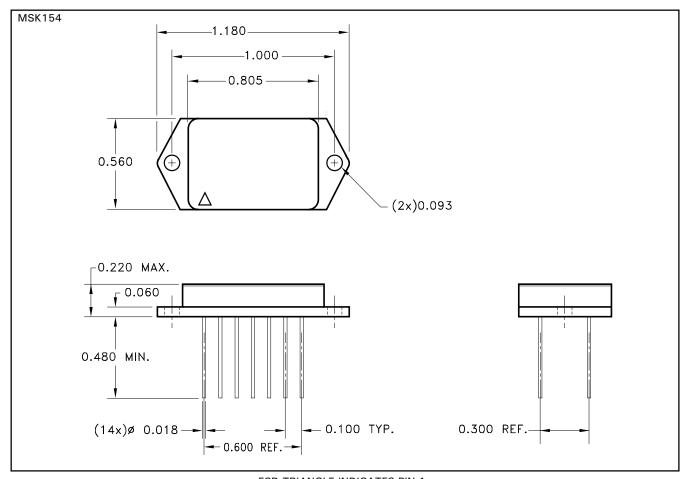
SUPPLY TO OUTPUT DIFFERENTIAL, Vs-Vo (V)



**TYPICAL CONNECTION DIAGRAM** 

# **TYPICAL PERFORMANCE CURVES**





ESD TRIANGLE INDICATES PIN 1. ALL DIMENSIONS ARE  $\pm\,0.010$  INCHES UNLESS OTHERWISE LABELED.

# ORDERING INFORMATION

Part Number	Screening Level			
MSK154	Industrial			
MSK154B	Military-Mil-PRF-38534			

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