

## FEATURES

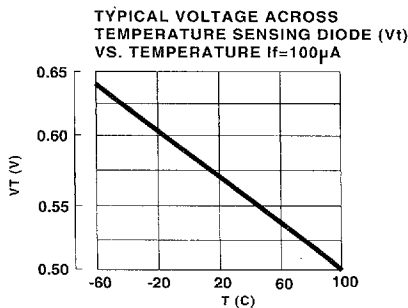
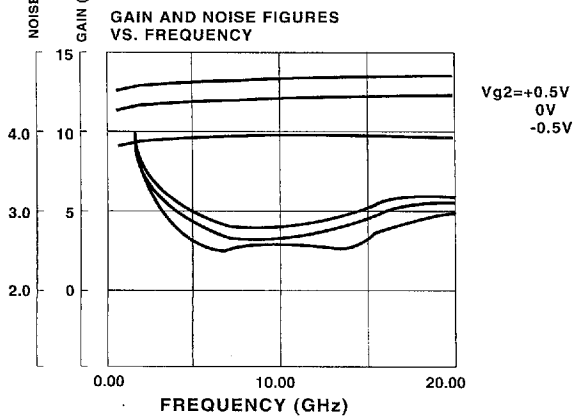
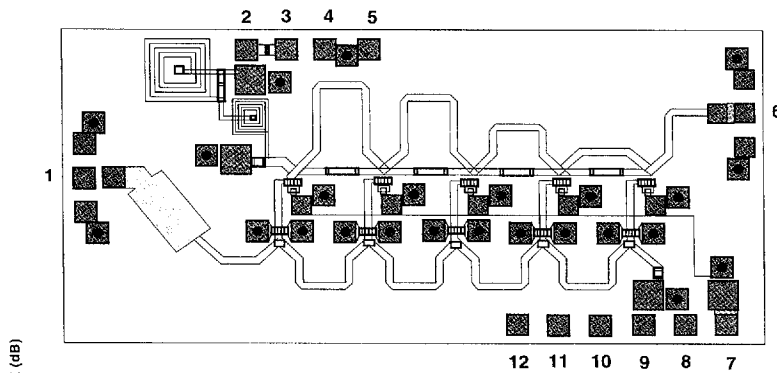
- Cascode Configuration
- 0.25  $\mu\text{m}$  HEMT Technology
- 10 dB Gain
- <4dB Noise Figure 2-18GHz
- <4.5dB Noise Figure 18-20GHz
- AGC Control With Gate Bias
- -55°C to +85°C Operation
- Temperature Sensing Diode Included On Chip
- Ideal for EW and Test Equipment Applications



**MODEL NO.  
P35-5100-0**

**GaAs MMIC LNA**

**MMIC**



## GUARANTEED PERFORMANCE

@ 25°C

PARAMETER	MIN	TYP	MAX	UNITS	CONDITIONS
SMALL SIGNAL GAIN	8	10		dB	
GAIN RIPPLE			$\pm 0.6$	dB	
NOISE FIGURE			4.0 4.5	dB	f = 2-18 GHz f = 18-20 GHz
GAIN CONTROL		-7/+2		dB	
HARMONICS		-40		dBc	f = 2GHz, f = 9GHz, P <sub>in</sub> = -18 dBm
OUTPUT POWER AT 1 dB GAIN COMP.	6			dBm	f = 2-18 GHz
INPUT RETURN LOSS	10			dB	
OUTPUT RETURN LOSS	10			dB	
MAXIMUM INPUT POWER			+14	dBm	
DC POWER SUPPLY	0 -1	60	95	mA V V	V <sub>DD</sub> = 4.0V Vg1, V <sub>DD</sub> = 4.0V Vg2, V <sub>GG</sub> = 4.0V
OPERATING TEMPERATURE	-55		+85	°C	

CONDITION IS V<sub>DD</sub> = 3.5V, Vg1 SET TO I<sub>DD</sub> = 0.5I<sub>DSS</sub>, Vg2 = 0V

### NOTES:

1. Bias-up arrangement. Apply V<sub>DD</sub> with Vg1 and Vg2 set to 0V. Apply Vg1 to set I<sub>DS</sub> = 50% I<sub>DSS</sub>. Apply Vg2 to set required gain. To bias down reverse procedure.
2. The auxiliary common-source bias, Vg1a, is connected to Vg1 via a 1K  $\Omega$  on-chip resistor. For normal operation Vg1 is used.
3. The input bond wire should ideally be 0.3mm, and the output 0.4mm long.

DAICO INDUSTRIES

■ 2604920 0002419 525 ■

257

This product is manufactured by GEC Marconi Materials, UK, and is distributed by Daico Industries.