#### **Operational Amplifiers**

High-Gain Single and Dual Operational Amplifiers For Military, Industrial and Commercial Applications

#### Features:

- Input bias current (all types): 500 nA max.
- Input offset current (all types):
   200 nA max.

The RCA-CA1458, CA1558 (dual types); CA741C, CA741 (single-types); CA747C, CA747 (dual types); and CA748C, CA748 (single types) are general-purpose, high-gain operational amplifiers for use in military, industrial, and commercial applications.

These monolithic silicon integrated-circuit devices provide output short-circuit protection and latch-free operation. These types also feature wide common-mode and differential-mode signal ranges and have low-offset voltage nulling capability when used with an appropriately valued potentiometer. A 5-megohm potentiometer is used for offset nulling types CA748C, CA748 (See Fig. 10); a 10-kilohm potentiometer is used for offset nulling types CA741C, CA741, CA747CE, CA747E (See Fig. 9); and types CA1458, CA1558, CA747CT, have no specific terminals for offset nulling. Each type consists of a differential-input amplifier that effectively drives a gain and level-shifting stage having a complementary emitter-follower output.

RCA's manufacturing process make it possible to produce IC operational amplifiers with low-burst ("popcorn") noise characteristics. Type CA6741, a low-noise version of the CA741, gives limit specifications for burst noise in the data

#### **Applications:**

- Comparator
- DC amplifier
- Integrator or differentiator
- Multivibrator
- Narrow-band or band-pass filter
- Summing amplifier

bulletin, File No. 530. Contact your RCA Sales Representative for information pertinent to other operational amplifier types that meet low-burst noise specifications.

This operational amplifier line also offers the circuit designer the option of operation with internal or external phase compensation.

Types CA748C and CA748, which are externally phase compensated (terminals 1 and 8) permit a choice of operation for improved bandwidth and slew-rate capabilities. Unity gain with external phase compensation can be obtained with a single 30-pF capacitor. All the other types are internally phase-compensated.

RCA No. of Type No. Ampl.		Phase Comp.	Offset Voltage Null	Min. A <sub>OL</sub>	Max. V <sub>IO</sub> (mV)	Operating-Temperature Range (°C)		
CA1458	dual	int.	no	20k	6	0 to +70▲		
CA1558	dual	int.	no	50k	5	-55 to +125		
CA741C	single	int.	yes	20k	6	0 to +70 <sup>▲</sup>		
CA741	single	int.	yes	50k	5	-55 to +125		
CA747C	dual	int.	yes*	20k	6	0 to +70 <sup>♠</sup>		
CA747	dual	int.	yes*	50k	5	-55 to +125		
CA748C	single	ext.	yes	20k	6	0 to +70 <sup>▲</sup>		
CA748	single	ext.	ves	50k	5	-55 to +125		

<sup>\*</sup>In the 14-lead dual-in-line plastic package only.

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<sup>^</sup>All types in any package style can be operated over the temperature range of −55 to +125°C, although the published limits for certain electrical specifications apply only over the temperature range of 0 to +70°C.

<sup>\*</sup>Technical Data on LM Branded types is identical to the corresponding CA Branded types.

Operational Amplifiers

# CA741, CA747, CA748, CA1458, CA1558, LM741, LM748, LM1458, LM1558

#### ORDERING INFORMATION

When ordering any of these types, it is important that the appropriate suffix letter for the package required be affixed to the type number. For example: If a CA1458 in a straightlead TO-5 style package is desired, order CA1458T.

TYPE NO.	TO-5 STYLE			PLASTIC		СНІР	BEAM- LEAD	FIG. NO.	
	8L	8L 10L DIL- 8L 14L CAN							
CA1458	Т		s	E		Н		1d, 1h	
CA1558	Т		s	E				1d, 1h	
CA741C	Т		s	E		Н		1a, 1e	
CA741	Т		s	E			L	1a, 1e	
CA747C		Т			E	Н		1b, 1f	
CA747		Т			Е			1b, 1f	
CA748C	Т		S	E		Н		1c, 1g	
CA748	Т		s	E				1c, 1g	

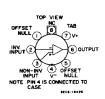
#### MAXIMUM RATINGS, Absolute-Maximum Values at $T_A = 25^{\circ}C$ :

DC Supply Voltage (between V <sup>+</sup> and V <sup>-</sup> terminals):				
CA741C, CA747C <sup>4</sup> , CA748C, CA1458 <sup>4</sup>				36 V
CA741, CA747 <sup>4</sup> , CA748, CA1558 <sup>4</sup>				44 V
Differential Input Voltage				±30 V
DC Input Voltage*				±15 V
Output Short-Circuit Duration				
Device Dissipation:				
Up to 70 C (CA741C, CA748C)				500 mW
Up to 75°C (CA741, CA748)				500 mW
Up to 30°C (CA747)				800 mW
Up to 25°C (CA747C)				800 mW
Up to 30°C (CA1558)				680 mW
Up to 25°C (CA1458)				680 mW
For Temperatures Indicated Above			Dera	te linearly 6.67 mW/°C
Voltage between Offset Null and V- (CA741C, CA7				
Ambient Temperature Range:				
Operating - CA741, CA747E, CA748, CA1558.				55 to +125 °C
CA741C, CA747C, CA748C, CA145				
Storage				
Lead Temperature (During Soldering):				
At distance $1/16 \pm 1/32$ inch $(1.59 \pm 0.79 \text{ mm})$ fr	om case fo	or 10 second	s max	<b>265</b> °C
* If Supply Voltage is less than ± 15 volts, the Absolu	ute Maxim	um Input V	oltage is equ	al to the Supply Volt-

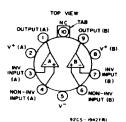
If Supply Voltage is less than ± 15 volts, the Absolute Maximum Input Voltage is equal to the Supply Voltage.

<sup>▲</sup> Voltage values apply for each of the dual operational amplifiers.

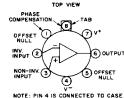
<sup>†</sup> All types in any package style can be operated over the temperature range of -55 to +125°C, although the published limits for certain electrical specifications apply only over the temperature range of 0 to +70°C.



1a.—CA741CS,CA741CT,CA741S, & CA741T with internal phase compensation.

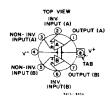


1b.—CA747CT and CA747T with internal phase compensation.

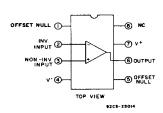


NOTE: PIN 4 IS CONNECTED TO CASE

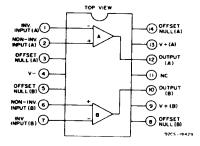
1c.—CA748CS, CA748CT,CA748S, and CA748T with external phase compensation.



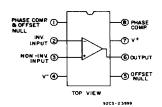
1d.—CA1458S,CA1458T,CA1558S, and CA1558T and internal phase compensation.



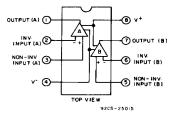
1e.-CA741C and CA741E with internal phase compensation.



1f.-CA747CE and CA747E with internal phase compensation.



1g.-CA748CE and CA748E with external phase compensation.



1h.-CA1458E and CA1558E with internal phase compensation.

Fig. 1 - Functional diagrams.

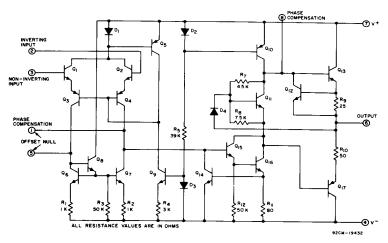


Fig.2—Schematic diagram of operational amplifier with external phase compensation for CA748C and CA748.

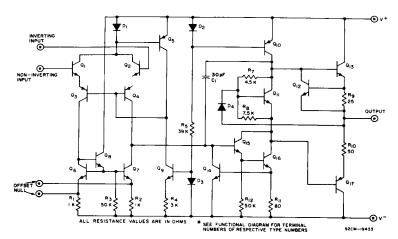


Fig.3—Schematic diagram of operational amplifiers with internal phase compensation for CA741C, CA741, and for each amplifier of the CA747C, CA747, CA1458, and CA1558.

#### ELECTRICAL CHARACTERISTICS

Typical Values Intended Only for Design Guidance

CHARACTERISTIC	TEST CONDITIONS V± = ±15 V •	TYP. VALUES ALL TYPES	UNITS
Input Capacitance, C <sub>1</sub>		1.4	pF
Offset Voltage Adjustment Range		±15	m∨
Output Resistance, RO		75	Ω
Output Short-Circuit Current		25	mA
Transient Response: Rise Time, t <sub>r</sub>	Unity gain V <sub>I</sub> = 20 mV	0.3	μs
Overshoot	R <sub>L</sub> = 2 kΩ C <sub>L</sub> ≤ 100 pF	5	%
Slew Rate, SR: Closed-loop	R <sub>1</sub> ≥ 2 kΩ	0.5	V/μs
Open-loop <sup>▲</sup>	N = 2 K32	40	] •/μ3

<sup>▲</sup> Open-loop slew rate applies only for types CA748C and CA748.

# ELECTRICAL CHARACTERISTICS For Equipment Design

	TEST CONDIT	LIMITS CA741C CA747C*					
CHARACTERISTIC	V <sup>+</sup> = 15 V, V <sup>-</sup> = -15 V	Ambient Temperature, T <sub>∆</sub>	CA748C CA1458*			UNITS	
			Min.	Тур.	Max.		
Input Offset Voltage,	R <sub>S</sub> =≤10kΩ	25 °C	_	2	6	mV	
V <sub>IO</sub>		0 to 70 °C	-	-	7.5	] ""	
Input Offset Current,		25 °C	-	20	200	nA	
10		0 to 70 °C	_	-	300		
Input Bias Current,		25 °C	-	80	500	nA	
IB		0 to 70 °C	_	-	800		
Input Resistance, R			0.3	2	_	МΩ	
Open-Loop Differential	R <sub>L</sub> ≥2kΩ	25 °C	20,000	200,000	1		
Voltage Gain, A <sub>OL</sub>	V <sub>O</sub> = ±10 V	0 to 70 °C	15,000				
Common-Mode Input Voltage Range, V <sub>ICR</sub>		25 °C	±12	±13	-	٧	
Common-Mode Rejection Ratio, CMRR	R <sub>S</sub> ≤10kΩ	25 °C	70	90	_	dB	
Supply-Voltage Rejection Ratio, PSRR	R <sub>S</sub> ≤10kΩ	25 °C	_	30	150	μV/V	
C Valana Carina	$R_L \ge 10 \text{ k}\Omega$	25 °C	±12	±14			
Output Voltage Swing, VOPP	D > 21.0	25 °C	±10	±13	_	V	
OFF	R <sub>L</sub> ≥2 kΩ	0 to 70 °C	±10	±13			
Supply Current, I <sup>±</sup>		25 °C	_	1.7	2.8	mA	
Device Dissipation, PD		25 °C	_	50	85	mW	

<sup>\*</sup> Values apply for each section of the dual amplifiers.

#### **ELECTRICAL CHARACTERISTICS**

For Equipment Design

	TEST CONDI		UNITS				
CHARACTERISTIC	V <sup>+</sup> = 15 V, V <sup>-</sup> = -15 V						
		Ambient Temperature, T <sub>A</sub>		CA1558*			
			Min.	Тур.	Max.		
Input Offset Voltage, VIO	R <sub>S</sub> = ≤ 10 kΩ			1	5	mV .	
		−55 to +125 °C		1	6		
		25 °C		20	200	nA	
Input Offset Current, I <sub>10</sub>		−55 °C		85	500		
		+125 °C		7	200		
		25 °C		80	500		
Input Bias Current, I <sub>IB</sub>		−55 °C		300	1500	nΑ	
		+125 °C		30	500		
Input Resistance, R <sub>1</sub>			0.3	2	-	МΩ	
Open Loop Differential	R <sub>L</sub> ≥2kΩ	25 °C	50,000	200,000	_	-	
Voltage Gain, A <sub>OL</sub>	V <sub>O</sub> = ± 10 V	–55 to +125 °C	25,000		_		
Common-Mode Input Voltage Range, V <sub>ICR</sub>		-55 to +125 °C	±12	±13	_	٧	
Common-Mode Rejection Ratio , CMRR	R <sub>S</sub> ≤ 10 kΩ	−55 to +125 °C	70	90	_	dB	
Supply Voltage Rejection Ratio, PSRR	R <sub>S</sub> ≤10kΩ	−55 to +125 °C	_	30	150	μV/V	
Output Voltage	$R_L \ge 10  k\Omega$	-55 to +125 °C	±12	±14	-		
Swing, V <sub>OPP</sub>	R <sub>L</sub> ≥2kΩ	–55 to +125 °C	±10	±13	_		
		25 °C		1.7	2.8		
Supply Current, I <sup>±</sup>		–55 °C		2	3.3	mA	
		+125 °C		1.5	2.5		
		25 °C		50	85	-	
Device Dissipation, PD		–55 °C		60	100	mW	
		+125 °C	_	45	75		

<sup>\*</sup> Values apply for each section of the dual amplifiers.

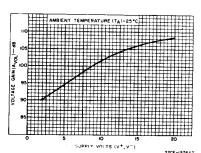


Fig.4—Open-loop voltage gain vs. supply voltage for all types except CA748 and CA748C.

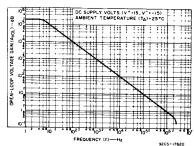


Fig.5—Open-loop voltage gain vs. frequency for all types except CA748 and CA748C.

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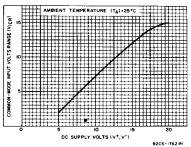


Fig.6—Common-mode input voltage range vs. supply voltage for all types.

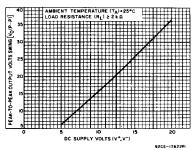


Fig.7—Peak-to-peak output voltage vs. supply voltage for all types except CA748 and CA748C.

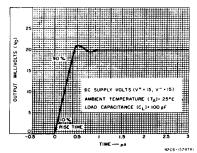


Fig.8—Output voltage vs. transient response time for CA741C and CA741.

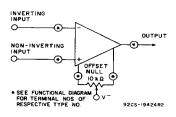


Fig.9-Voltage offset null circuit for CA741C, CA741, CA747CE, and CA747E.

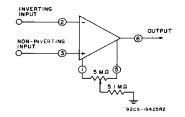


Fig. 10-Voltage-offset null circuit for CA748C and CA748.

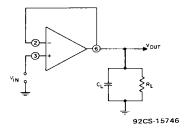
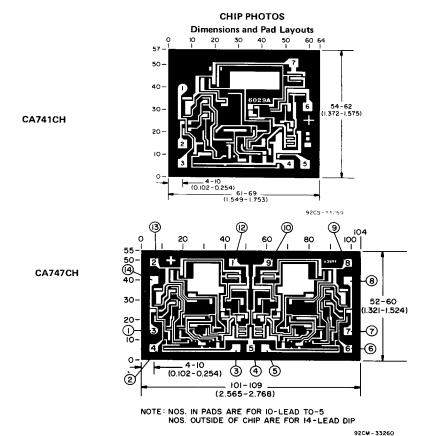
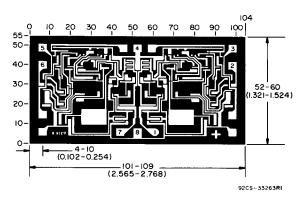


Fig. 11-Transient response test circuit for all types.





Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils ( $10^{-3}$  inch).

**CA1458H** 

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