

SERIES DL6780 DIGITAL DELAY MODULES

- 30 Pin Configuration • TTL and DTL Compatible • 10 Equally Spaced Taps

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50µA Max
- Logic 0 Input Current : -4mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100PPM/°C



Input Test Conditions

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3NS
- Input Current : 120mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay NSEC ±5%	Tap to Tap Delay NS ⁽¹⁾	Rise Time NS Max ⁽²⁾
DL6786	50	5	4
DL6787	100	10	4
DL6788	150	15	4
DL6789	200	20	4
DL6790	250	25	4
DL6791	300	30	4
DL6792	400	40	5
DL6793	500	50	6
DL6794	1000	100	6

Note: (1) Measured at 1.5v level leading edge, Tol. ±5% or ±2NS, whichever is greater.
 (2) Measured from 0.75v to 2.4v.

