

T-47-13

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DIGITAL DELAY LINE SERIES 0447 AND A447 CONTROLLED LEADING & TRAILING EDGE DELAY MODULE 5 TAP

TECHNICAL INFORMATION

TEST CONDITIONS

Pulse Voltage 3.2 Volts
 Rise Time 3.0 Nsec (10%-90%)
 Pulse Width 1.2 X Total Delay
 Pulse Period 4 X Pulse Width
 Supply Current, I_{CC} 60.0 Milliamps max.
 Supply Voltage, V_{CC} 5.0 Volts
 Ambient Temperature 25°C

PERFORMANCE CHARACTERISTICS

Logic 1 Input Current 50 Microamp max.
 Logic 0 Input Current -2 Milliamp max.
 Logic 1 Output Voltage 2.7 Volts min.
 Logic 0 Output Voltage 0.5 Volts max.
 Delay Tolerance From Input To Tap ±2 Nsec or 5% whichever is greater
 Delay Tolerance From Tap To Tap ±2 Nsec or 7% whichever is greater

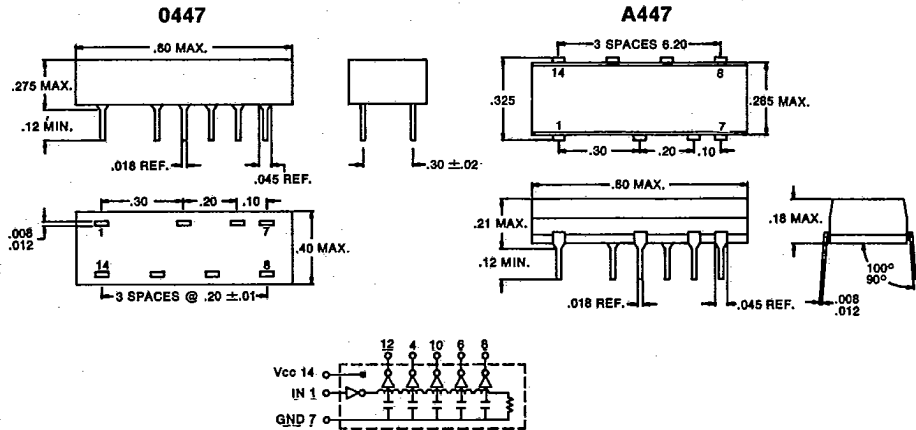
All specified Performance Characteristics apply at above listed Test Conditions.

ELECTRICAL CHARACTERISTICS

Supply Voltage, V_{CC} 4.75 to 5.25 Volts
 Operating Temperature Range 0°C To 70°C
 Temperature Coefficient Of Total Delay 500PPM/°C Typical
 Minimum Input Pulse Width 20% Of Total Delay
 Maximum Duty Cycle 50%

DRIVE CAPABILITIES

10 TTL Loads/Tap max.
 20 TTL Loads/Unit max.
 — Compatible with TTL and DTL circuits
 — Other delays and tolerances upon request



Part * Number	Total Delay 1, 3	Delay / Tap 1, 3	Rise, Time 2, 3
0447-0025-A1	25NS	5NS	4NS
0447-0050-A1	50NS	10NS	4NS
0447-0100-A1	100NS	20NS	4NS
0447-0150-A1	150NS	30NS	4NS
0447-0200-A1	200NS	40NS	4NS
0447-0250-A1	250NS	50NS	4NS
0447-0300-A1	300NS	60NS	4NS
0447-0350-A1	350NS	70NS	4NS
0447-0400-A1	400NS	80NS	4NS
0447-0450-A1	450NS	90NS	4NS
0447-0500-A1	500NS	100NS	5NS

1 Delays measured at 1.5 Volts level on Leading and Trailing Edge.
 2 Rise and Fall Times measured from .75 Volts to 2.4 Volts.
 3 Measured with 1 STTL (15PF) load per tap.
 * Auto Insertable package part number starts with A447.

Specifications Subject To Change Without Notice