

BAT750

0.75A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

Very Low Forward Voltage Drop

High Conductance

For Use in DC-DC Converter, PCMCIA, and Mobile Telecommunications Applications

Lead Free/RoHS Compliant (Note 3)

Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

Case: SOT-23

Case Material: Molded Plastic. UL Flammability

Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

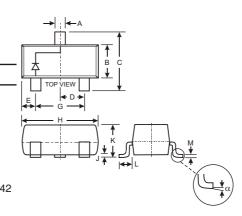
Terminals: Solderable per MIL-STD-202, Method 208 Lead Free Plating (Matte Tin Finish annealed over Alloy 42

leadframe).

Polarity: See Diagram

Marking: K77 or K79 and Date Code, See Page 3

Weight: 0.008 grams (approximate)



	SOT-23	
Dim	Min	Max
Α	0.37	0.51
В	1.20	1.40
С	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
Н	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
	0	8
All Din	nensions	in mm

Maximum Ratings @ TA = 25 C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	٧	
RMS Reverse Voltage	$V_{R(RMS)}$	28	V	
Average Rectified Current	I _O	0.75	A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load	I _{FSM}	5.5	Α	
Power Dissipation (Note 1)	P_d	350	mW	
Typical Thermal Resistance, Junction to Ambient Air (Note 1)	R _{JA}	286	C/W	
Operating and Storage Temperature Range	T_{j},T_{STG}	-55 to +125	С	

Electrical Characteristics @ TA = 25 C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	40	45		V	I _R = 300uA
Forward Voltage			225 235 290 340 390 420 475	280 310 350 420 490 540 650	mV	I _F = 50mA I _F = 100mA I _F = 250mA I _F = 500mA I _F = 750mA I _F = 1500mA
Reverse Current (Note 2)	I _R		50	100	Α	V _R = 30V
Total Capacitance Reverse Recovery Time			175 25		pF pF	$V_R = 0V$, $f = 1.0MHz$ $V_R = 25V$, $f = 1.0MHz$
				10	ns	$I_F = I_R = 100$ mA, $I_{rr} = 10$ mA. See figure 6.

Notes: 1. Part mounted on FR-4 PC board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

- 2. Short duration test pulse used to minimize self-heating effect.
- 3. No purposefully added lead.



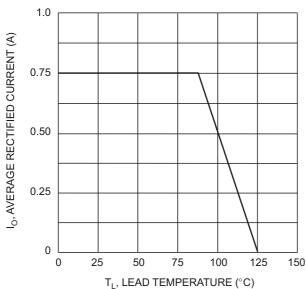


Fig. 1 Forward Current Derating Curve

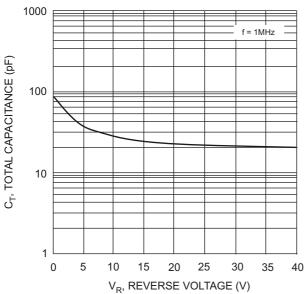


Fig. 3 Total Capacitance vs Reverse Voltage

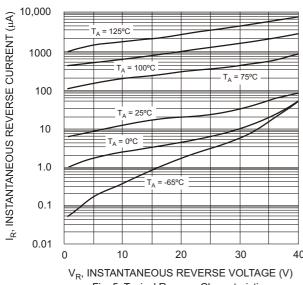
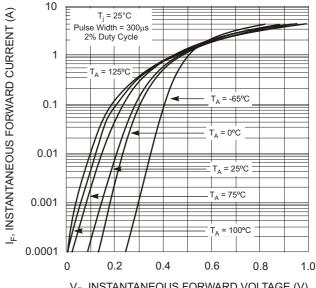
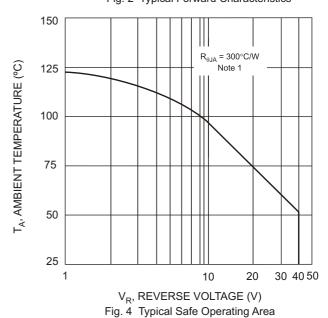


Fig. 5 Typical Reverse Characteristics



V_F, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics



Note: 1. Assumed application thermal conditions. R JA varies depending on application.

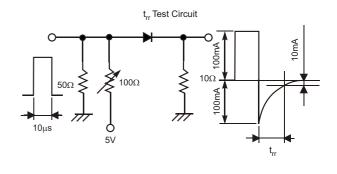


Fig. 6 Reverse Recovery Time Test Circuit and Waveform

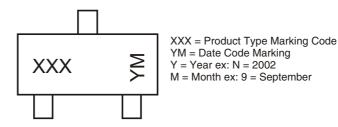


Ordering Information (Note 4)

Device	Packaging	Shipping		
BAT750-7-F	SOT-23	3000/Tape & Reel		

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



Date Code Key

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	М	N	Р	R	S	Т	J	V	W	Х	Υ	Z

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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