

Fast CMOS 16-Bit Octal Registered Transceivers

Product Features:

Common Features:

- PI74FCT16651T and PI74FCT162651T have high current drive and four speed grades.
Standard speeds at 9.0 ns max.
"A" speeds at 6.3 ns max.
"C" speeds at 5.4 ns max.
"E" speeds at 3.8 ns max.
- $V_{CC} = 5\text{ V} \pm 10\%$
- Hysteresis on all inputs
- Packaged in 56-pin plastic TSSOP and SSOP

PI74FCT16651T Features:

- High output drive: $I_{OH} = -32\text{ mA}$; $I_{OL} = 64\text{ mA}$
- Power off disable outputs permit "live insertion"
- Typical VOLP (Output Ground Bounce) $< 1.0\text{ V}$
at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$

PI74FCT162651T Features:

- Balanced output drivers: $\pm 24\text{ mA}$
- Reduced system switching noise
- Typical VOLP (Output Ground Bounce) $< 0.6\text{ V}$
at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$

Product Description:

Pericom Semiconductor's PI74FCT series of logic circuits are produced in the Company's advanced 0.8 micron CMOS technology, achieving industry leading speed grades.

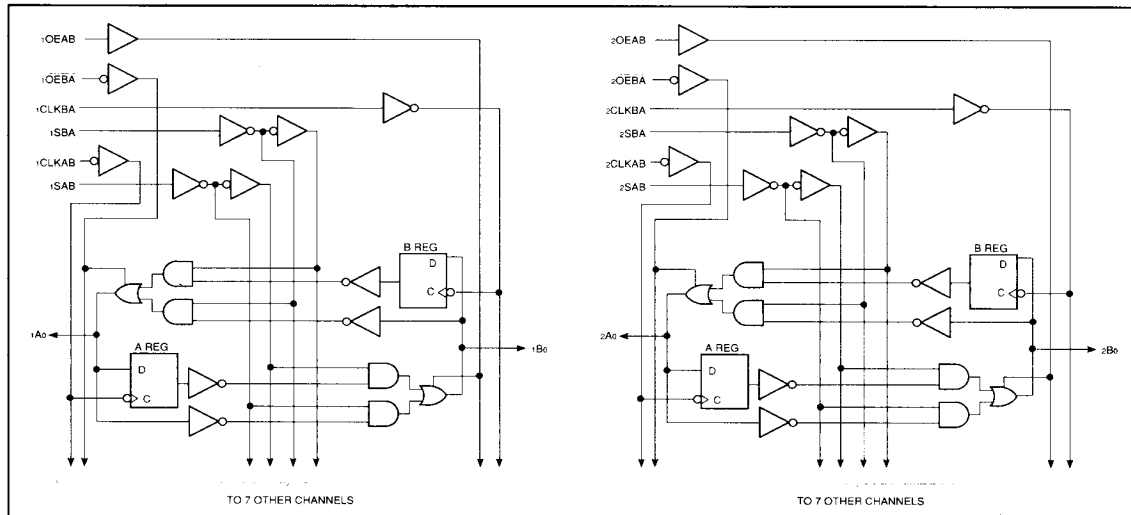
The PI74FCT16651T and PI74FCT162651T are inverting 16-bit registered transceivers organized as two independent 8-bit bus transceivers designed with 3-state D-type flip-flops and control circuitry arranged for multiplexed transmission of data directly from the data bus or from the internal storage registers. Each 8-bit transceiver utilizes the enable controls ($\overline{xOEAB}$ and $\overline{xOEBA}$) to control the transceiver functions. The Select ($\overline{xSAB}$ and $\overline{xSBA}$) control pins are used to select either real-time or stored data transfer. The circuitry used for select control will eliminate the typical decoding glitch that occurs in a multiplexer during the transition between real-time and stored data. A low input level selects real-time data and a high selects stored data.

The PI74FCT16651T output buffers are designed with a Power-Off disable allowing "live insertion" of boards when used as backplane drivers.

The PI74FCT162651T has $\pm 24\text{ mA}$ balanced output drivers. It is designed with current limiting resistors at its outputs to control the output edge rate resulting in lower ground bounce and undershoot. This eliminates the need for external terminating resistors for most interface applications.

All products are available in 56-pin 240 mil wide plastic TSSOP and 300 mil wide plastic SSOP packages.

Logic Block Diagram

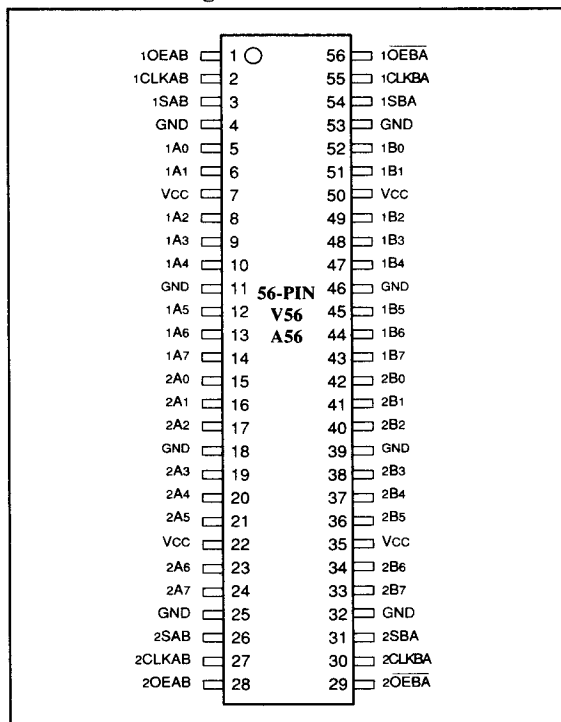


Truth Table

Function/Operation	Inputs						DATA I/O ⁽²⁾	
	xOEAB	xOEBA	xCLKAB	xCLKBA	xSAB	xSBA	xAx	xBx
Isolation	L	H	H or L	H or L	X	X	Input	Input
Store A and B Data	L	H	↑	↑	X	X		
Store A, Hold B	X	H	↑	H or L	X	X	Input	Unspecified ⁽¹⁾
Store A in Both Registers	H	H	↑	↑	X ⁽²⁾	X	Input	Output
Hold A, Store B	L	X	H or L	↑	X	X	Unspecified ⁽¹⁾	Input
Store B in Both Registers	L	L	↑	↑	X	X ⁽²⁾	Output	Input
Real Time B Data to A Bus	L	L	X	X	X	L	Output	Input
Stored B Data to A Bus	L	L	X	H or L	X	H		
Real Time A Data to B Bus	H	H	X	X	L	X	Input	Output
Stored A Data to B Bus	H	H	H or L	X	H	X		
Stored A Data to B Bus and Stored B Data to A Bus	H	L	H or L	H or L	H	H	Output	Output

- The data output functions may be enabled or disabled by various signals at the xOEAB or xOEBA inputs. Data input functions are always enabled, i.e., data at the bus pins will be stored on every low-to-high transition on the clock inputs.
- Select control = L: clocks can occur simultaneously.
Select control = H: clocks must be staggered in order to load both registers.
H = High Voltage Level; L = Low Voltage Level; X = Don't Care; ↑ = LOW-to-HIGH transition

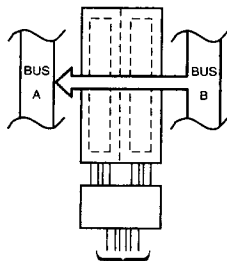
Product Pin Configuration



Product Pin Description

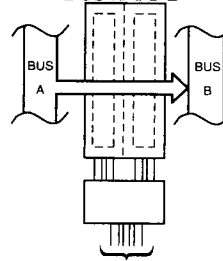
Pin Name	Description
xAx	Data Register A Inputs Data Register B Outputs
xBx	Data Register B Inputs Data Register A Outputs
xCLKAB, xCLKBA	Clock Pulse Inputs
SAB, SBA	Output Data Source Select Inputs
xOEAB, xOEBA	Output Enable Inputs
GND	Ground
Vcc	Power

**REAL-TIME TRANSFER
 BUS B TO A**



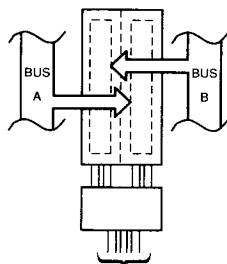
xOEAB	xOEBA	xCLKAB	xCLKBA	xSAB	xSBA
L	L	X	X	X	L

**REAL-TIME TRANSFER
 BUS A TO B**



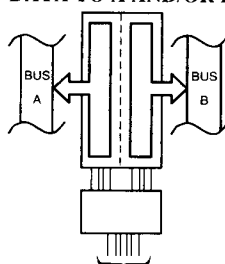
xOEAB	xOEBA	xCLKAB	xCLKBA	xSAB	xSBA
H	H	X	X	L	X

**STORAGE FROM
 A AND/OR B**



xOEAB	xOEBA	xCLKAB	xCLKBA	xSAB	xSBA
X	H	↑	X	X	X
L	X	X	↑	X	X
L	H	↑	↑	X	X

**TRANSFER STORES
 DATA TO A AND/OR B**



xOEAB	xOEBA	xCLKAB	xCLKBA	xSAB	xSBA
H	L	H or L	H or L	H	H

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–55°C to +125°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to Vcc
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	1.0W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, TA = –40°C to +85°C, VCC = 5.0V ± 10%)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level			0.8	V
I _{IH}	Input HIGH Current	VCC = Max., VIN = VCC			±5	μA
I _{IL}	Input LOW Current	VCC = Max., VIN = GND			±5	μA
IOZH	High Impedance	VCC = Max., VOUT = 2.7 V			±10	μA
IOZL	Output Current	VCC = Max., VOUT = 0.5 V			±10	μA
V _{IK}	Clamp Diode Voltage	VCC = Min., IIN = –18 mA		–0.7	–1.2	V
I _{OS}	Short Circuit Current	VCC = Max. ⁽³⁾ , VOUT = GND	–80	–140	–200	mA
I _O	Output Drive Current	VCC = Max. ⁽³⁾ , VOUT = 2.5 V	–50		–180	mA
V _H	Input Hysteresis			100		mV

PI74FCT16651T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
VOH	Output HIGH Voltage	VCC = Min., VIN = VIH or VIL	IOH = –3.0 mA	2.5	3.5	V
			IOH = –15.0 mA	2.4	3.5	
			IOH = –32.0 mA	2.0	3.0	
VOL	Output LOW Voltage	VCC = Min., VIN = VIH or VIL	IOH = 64 mA	0.2	0.55	V
IOFF	Power Down Disable	VCC = 0 V, VIN or VOUT ≤ 4.5 V	—	—	±100	μA

PI74FCT162651T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
VOH	Output HIGH Voltage	VCC = Min., VIN = VIH or VIL	IOH = –24.0 mA	2.4	3.3	V
VOL	Output LOW Voltage	VCC = Min., VIN = VIH or VIL	IOH = 24 mA	0.3	0.55	V
IODL	Output LOW Current	VCC = 5 V, VIN = VIH OR VIL, VOUT = 1.5 V ⁽³⁾	60	115	150	mA
IODH	Output HIGH Current	VCC = 5 V, VIN = VIH OR VIL, VOUT = 1.5 V ⁽³⁾	–60	–115	–150	mA

Capacitance (TA = 25°C, f = 1 MHz)

Parameters ⁽⁴⁾	Description	Test Conditions	Typ	Max.	Units
CIN	Input Capacitance	VIN = 0 V	4.5	6	pF
COUT	Output Capacitance	VOUT = 0 V	5.5	8	pF

Notes:

- For conditions show as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at VCC = 5.0, +25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		2	500	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.4 V ⁽³⁾		0.5	1.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., Outputs Open xOEAB = xOEBA = GND One Bit Toggling 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND		75	120	μA/ MHz
I _C	Total Power Supply Current ⁽⁶⁾	V _{CC} = Max., Outputs Open f _{CP} = 10 MHz (xCLKBA) 50% Duty Cycle xOEAB = xOEBA = GND One Bit Toggling fi = 5 MHz 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND		0.8	2.7 ⁽⁵⁾	mA
			V _{IN} = 3.4 V V _{IN} = GND		1.3	4.2 ⁽⁵⁾	
		V _{CC} = Max., Outputs Open f _{CP} = 10 MHz (xCLKBA) 50% Duty Cycle xOEAB = xOEBA = GND 16 Bits Toggling fi = 2.5 MHz 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND		3.8	7.5 ⁽⁵⁾	
			V _{IN} = 3.4 V V _{IN} = GND		8.3	21.0 ⁽⁵⁾	

- Notes:**
- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
 - Typical values are at V_{CC} = 5.0 V, +25°C ambient.
 - Per TTL driven input (V_{IN} = 3.4 V); all other inputs at V_{CC} or GND.
 - This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
 - Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed but not tested.
 - I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}

$$I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP}/2 + f_i N_i)$$

I_{CC} = Quiescent Current
ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = 3.4 V)
D_H = Duty Cycle for TTL Inputs High
N_T = Number of TTL Inputs at D_H
I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
f_{CP} = Clock Frequency for Register Devices (Zero for Non-Register Devices)
f_i = Input Frequency
N_i = Number of Inputs at f_i
All currents are in milliamps and all frequencies are in megahertz.

PI74FCT16651 Switching Characteristics over Operating Range

Preliminary

		Summary									
Parameters	Description	Conditions ⁽¹⁾	FCT16651T		FCT16651AT		FCT16651CT		FCT16651ET		Unit
			Com.		Com.		Com.		Com.		
			Min	Max	Min	Max	Min	Max	Min	Max	
TP _{LH}	Propagation Delay	C _L = 50 pF R _L = 500Ω	2.0	9.0	2.0	6.3	1.5	5.4	1.5	3.8	ns
TP _{HL}	Bus to Bus										
TP _{ZH}	Output Enable Time		2.0	14.0	2.0	9.8	1.5	7.8	1.5	4.8	ns
TP _{ZL}	xOEAB or x OEBA to Bus										
TP _{HZ}	Output Disable Time		2.0	9.0	2.0	6.3	1.5	6.3	1.5	4.0	ns
TP _{LZ}	xOEAB or x OEBA to Bus										
TP _{LH}	Propagation Delay		2.0	9.0	2.0	6.3	1.5	5.7	1.5	3.8	ns
TP _{HL}	Clock to Bus										
TP _{LH}	Propagation Delay		2.0	11.0	2.0	7.7	1.5	6.2	1.5	4.2	ns
TP _{HL}	xSBA or xSAB to Bus										
TS _U	Set-up Time HIGH or LOW, Bus to Clock		4.0	—	2.0	—	2.0	—	2.0	—	ns
TH	Hold Time HIGH or LOW, Bus to Clock		2.0	—	1.5	—	1.5	—	0.0	—	ns
tw	Clock Pulse Width HIGH or LOW		6.0	—	5.0	—	5.0	—	3.0 ⁽⁴⁾	—	ns
TSK(O)	Output Skew ⁽³⁾		—	0.5	—	0.5	—	0.5	—	0.5	ns

PI74FCT162651 Switching Characteristics over Operating Range

Preliminary

Parameters	Description	Conditions ⁽¹⁾	FCT162651T		FCT162651AT		FCT162651CT		FCT162651ET		Unit
			Com.		Com.		Com.		Com.		
			Min	Max	Min	Max	Min	Max	Min	Max	
TP _{LH}	Propagation Delay	C _L = 50 pF R _L = 500Ω	2.0	9.0	2.0	6.3	1.5	5.4	1.5	3.8	ns
TP _{HL}	Bus to Bus										
TP _{ZH}	Output Enable Time		2.0	14.0	2.0	9.8	1.5	7.8	1.5	4.8	ns
TP _{ZL}	xOEAB or x OEBA to Bus										
TP _{HZ}	Output Disable Time		2.0	9.0	2.0	6.3	1.5	6.3	1.5	4.0	ns
TP _{LZ}	xOEAB or x OEBA to Bus										
TP _{LH}	Propagation Delay		2.0	9.0	2.0	6.3	1.5	5.7	1.5	3.8	ns
TP _{HL}	Clock to Bus										
TP _{LH}	Propagation Delay		2.0	11.0	2.0	7.7	1.5	6.2	1.5	4.2	ns
TP _{HL}	xSBA or xSAB to Bus										
TS _U	Set-up Time HIGH or LOW, Bus to Clock		4.0	—	2.0	—	2.0	—	2.0	—	ns
TH	Hold Time HIGH or LOW, Bus to Clock		2.0	—	1.5	—	1.5	—	0.0	—	ns
tw	Clock Pulse Width HIGH or LOW		6.0	—	5.0	—	5.0	—	3.0 ⁽⁴⁾	—	ns
TSK(O)	Output Skew ⁽⁵⁾		—	0.5	—	0.5	—	0.5	—	0.5	ns

Notes:

- See test circuit and wave forms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.
- This is limit is guaranteed but not tested.