TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIV)

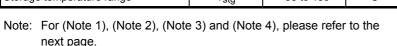
TPC8114

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

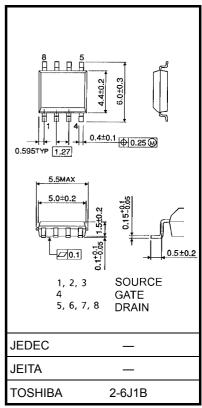
- Small footprint due to small and thin package
- Low drain-source ON resistance: R_{DS} (ON) = 3.1 m Ω (typ.)
- High forward transfer admittance: $|\,Y_{\rm fs}\,|$ = 47 S (typ.)
- Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- Enhancement-mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_D = -1 mA)

Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-30	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	-30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	-18	A	
	Pulse (Note 1)	I _{DP}	-72		
Drain power dissipatio	n (t = 10 s) (Note 2a)	PD	1.9	W	
Drain power dissipatio	n (t = 10 s) (Note 2b)	PD	1.0	W	
Single pulse avalanch	e energy (Note 3)	E _{AS}	211	mJ	
Avalanche current		I _{AR}	-18	А	
Repetitive avalanche e (N	energy lote 2a) (Note 4)	E _{AR}	0.19	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature r	ange	T _{stg}	–55 to 150	°C	

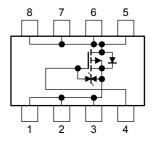


This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.080 g (typ.)

Circuit Configuration



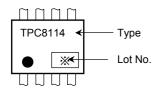
Unit: mm

TOSHIBA

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

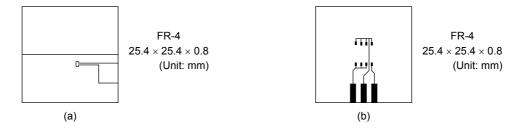
Marking (Note 5)



Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



Note 3: $V_{DD} = -24 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 500 μ H, R_G = 25 Ω , I_{AR} = -18 A

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1. ※ Weekly code: (Three digits)



Week of manufacture (01 for first week of year, continues up to 52 or 53) Year of manufacture

(One low-order digits of calendar year)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curre	Gate leakage current		$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA	
Drain cut-OFF current		I _{DSS}	$V_{DS} = -30$ V, $V_{GS} = 0$ V	_		-10	μA	
Drain-source breal	kdown voltage	V (BR) DSS	$I_D = -10$ mA, $V_{GS} = 0$ V	-30	_		v	
Dialit-source breat	down voltage	V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 20$ V	-15	_		v	
Gate threshold voltage		V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V	
Drain-source ON resistance		Rec (ON)	$V_{GS} = -4 V, I_D = -9 A$		5.2	6.8	mΩ	
		R _{DS (ON)}	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -9 \text{ A}$		3.1	4.5		
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -9 \text{ A}$	23.5	47		S	
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz		7480	_	pF	
Reverse transfer capacitance		C _{rss}			1320			
Output capacitance		C _{oss}			1460			
· · · · · · · · · · · · · · · · · · ·	Rise time	tr	$V_{GS} = -9 A$ $V_{GS} = -9 A$ OV = -9 A OV = -9	—	25	—	- ns	
	Turn-ON time	t _{on}		_	36	_		
	Fall time	t _f		_	235			
	Turn-OFF time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty \leq 1%, t_W = 10 μs	_	625			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -24 \text{ V}, \text{ V}_{GS} = 10 \text{ V},$		180		nC	
Gate-source charge 1		Q _{gs1}	$I_{\rm D} = -18 \rm{A}$	_	10			
Gate-drain ("miller") charge		Q _{gd}			60			

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	—	—	-72	А
Forward voltage (diode)		V _{DSF}	$I_{DR} = -18 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$			1.2	V	

RESTRICTIONS ON PRODUCT USE

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.