Z86 Prototype board -28 Pin DIL package

This prototype board provides a low cost means of getting "up and running" with designs using the Zilog Z86E30 and Z86E31 microcontroller. Ideal for use in evaluating lab ideas, one off projects and demonstations and gaining experience with the workings of the Z86E30/31 device.

The board has been laid out to accept the 28 pin DIL package. The PCB is laid out as two functional areas. The first area contains the Z86E30/31 microcontroller (not supplied) along with a removable 4MHz resonator (allowing user selectable crystal frequency) and associated oscillator components. A buffer is connected to port 2 and port 3 to ensure the full drive capability of the Z8 is available on the prototype area. The buffers are connected to indicating LED's such that the true logic state of the ports can be observed. These LED's indicate the state for the port both in input and output mode. A user selectable jumper allows the buffers to be tristated to reduce power consumption. An on board 5 volt regulator allows the board to operate from 8 to 12 volts input and to provide up to 500ma (depending on Vin) to the board. Reverse polarity supply protection is included.

The second area of the board is a protype area into which all of the port lines are routed. A grid of 40 thou holes on a standard 0.1 inch pitch make up an area of 30 rows by 25 columns. Power and ground are routed into the array to allow easy hook up. This allows easy construction for switches, seven segment LED's, logic chips, op amps etc that are on a 0.1 grid and provides a stand alone means of construction without the complications of routing off board.

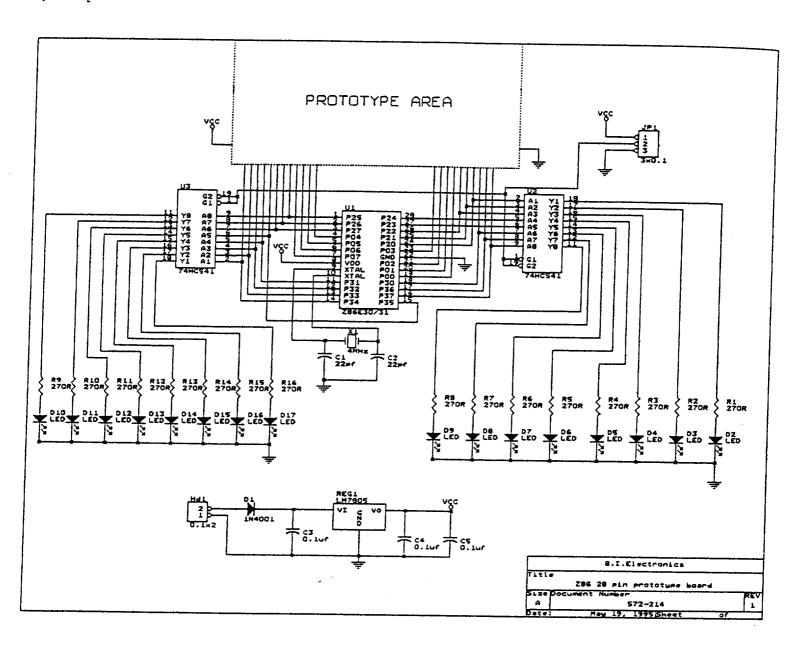
The Z8E30/31 prototype board provides savings in time, cost of prototype PCB's and ease of use. Fault finding is quicker than on hand wired or veroboard type of construction allowing quicker time to development.

FARNELL COMPONENTS

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Specification

Supply Voltage	8 to 15 volts DC	
Supply Current max	200 ma	Note 1
Supply Current min	10 ma	Note 2
Prototype area current	500 ma	Note 3
Maximum clock Freq	8MHz	Note 4
Prototype hole size	40 thou	1.000
Prototype area	25 X 30 x 0.1	

Note 1:All LED's illuminated

Note 2: All LED's off

Note 3: Current available depends on input to output differential. For reliable operation the regulator dissipation should be below 1.5 watts

Note 4: Loading capacitors are 22pf these may need altering for different oscillators