

HIGH SPEED SWITCHING APPLICATIONS

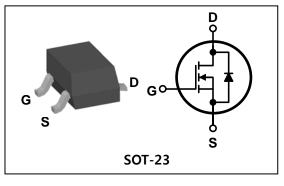
Features

- Low Gate Threshold Voltage
- Low C_{rss} : C_{rss}=2.0pF(Typ.)
- Voltage controlled small signal switch
- Low $R_{DS(on)}$: $R_{DS(on)}=5\Omega(Max.)$

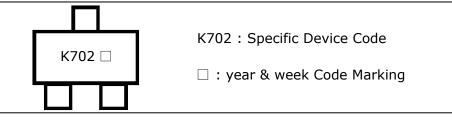
Ordering Information

Type No.	Marking	Package Code
STK7002	<u>K702</u> <u>□</u> ① ②	SOT-23





Marking Diagram



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V _{DSS}	60	V
Gate-source voltage	V _{GSS}	±20	V
Drain current (DC) *	I _D	115	mA
Drain current (Pulsed) *	\mathbf{I}_DM	800	mA
Junction temperature	Tյ	150	°C
Storage temperature range	T _{stg}	-55~150	

* Limited by maximum junction temperature

Thermal Characteristics

Characteristic	Symbol	Rating	Unit
Power dissipation	P _D	350	mW
Thermal resistance, Junction-Ambient *	R _{th(J-A)}	357	°C/W

* Device mounted on FR-4 PCB, 99.5% Alumina 10 x 8 x 0.6mm. Minimum land pad size

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Off Characteristics (Note1)							
Drain-source breakdown voltage	BV _{DSS}	$I_D=10uA$, $V_{GS}=0$	60	-	-	V	
During country off country of	Ŧ	V _{DS} =60V, V _{GS} =0	-	-	1.0	uA	
Drain-source cut-off current	I _{DSS}	V _{DS} =60V, V _{GS} =0, @T _C =125°C	-	-	200		
Gate leakage current	I _{GSS}	$V_{DS}=0V$, $V_{GS}=\pm 20V$	-	-	±100	nA	
On Characteristics (Note1)							
Gate threshold voltage	V _{GS(th)}	$I_D=250uA, V_{DS}=V_{GS}$	1.0	2.0	2.5	V	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} =10V, I _D =500mA	-	2.4	5.0		
		V_{GS} =5V, I_{D} =50mA	-	3.2	5.0	Ω	
Forward transfer conductance	g _{fs}	V _{DS} =10V, I _D =100mA	80	-	-	mS	
Dynamic Characteristics							
Input capacitance	Ciss		-	22	-		
Output capacitance	Coss		11	-	pF		
Reverse transfer capacitance	Crss		-	2	-		
Switching Characteristics							
Turn-on delay time	t _{D(ON)}	(ON) V _{DD} =30V, I _D =100mA - 7	7	-			
Turn-off delay time	t _{D(OFF)}	$V_{GS} = 10V, R_{G} = 25\Omega$	-	11	-	ns	

Electrical Characteristics (T_A=25°C unless otherwise noted)

Note1 : Short duration test pulse used to minimize self-heating effect.

Electrical Characteristic Curves

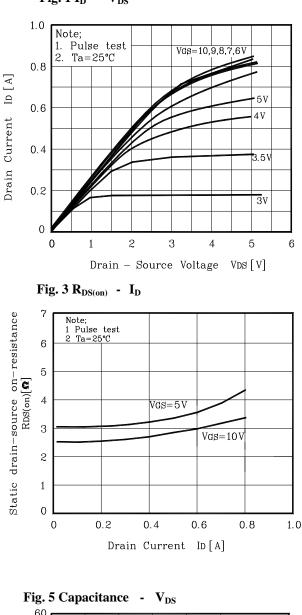
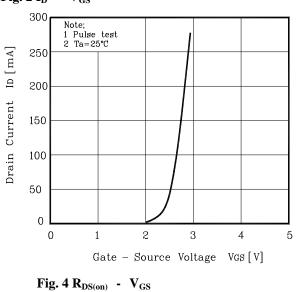
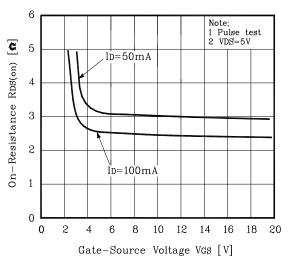
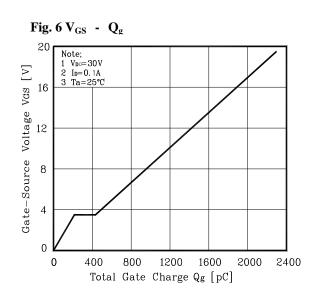




Fig. 2 I_D - V_{GS}







Note; 1 Vcs=0V 2 f=1MHz 3 Ta=25°C Capacitance C [pF] Ciss Coss Crss Drain-Source Voltage VDS [V]

Electrical Characteristic Curves

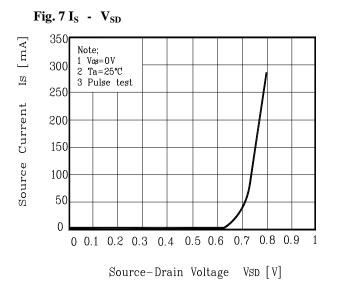
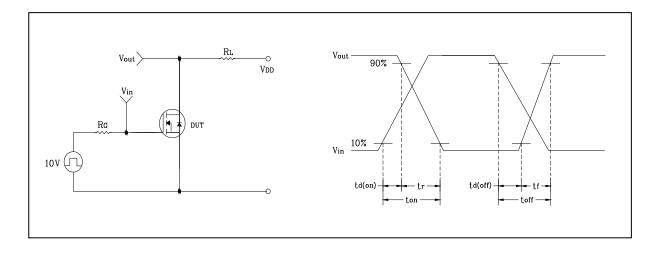
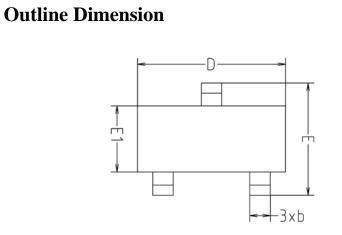
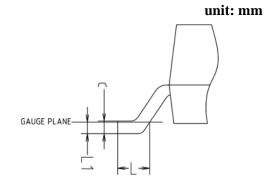


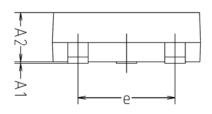
Fig. 8 Resistive Switching Test Circuit & Waveform

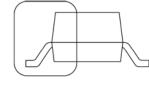








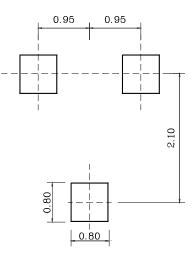




SEE DETAIL 'A'

SYMBOL	MILLIMETERS			NOTE
511000	MINIMUM	NOMINAL	MAXIMUM	
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
e	1.90BSC			
L	0.20	-	-	
L1		0.12BSC		

*** Recommended Land Pattern** [unit: mm]



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