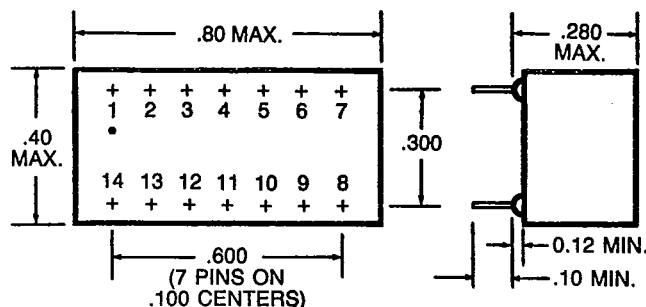


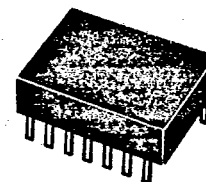
YCL

LOW POWER SCHOTTKY DIGITAL DELAY LINES TTL COMPATIBLE • 14 PIN PACKAGE 5 TAPS • SINGLE • DUAL • TRIPLE

SERIES JL05, JL01, JL02 AND JL03



White Dot locates Pin 1



ONLY ACTIVE PINS ARE SUPPLIED

Intermediate delay values
available upon request.

SERIES JL05 (5 TAP)		
MODEL NO. (Fig. 1)	Delay ns	Delay/Tap ns
JL05025	25	5
JL05030	30	6
JL05035	35	7
JL05040	40	8
JL05045	45	9
JL05050	50	10
JL05060	60	12
JL05075	75	15
JL05100	100	20
JL05125	125	25
JL05200	200	40
JL05250	250	50
JL05300	300	60
JL05400	400	80
JL05500	500	100

Delay/ line (ns)	MODEL NUMBERS		
	Series JL01 ⁴	Series JL02	Series JL03
	One output (Fig. 2)	Dual output (Fig. 3)	Triple output (Fig. 4)
10	JL01010	JL02010	JL03010
15	JL01015	JL02015	JL03015
20	JL01020	JL02020	JL03020
25	JL01025	JL02025	JL03025
30	JL01030	JL02030	JL03030
35	JL01035	JL02035	JL03035
40	JL01040	JL02040	JL03040
50	JL01050	JL02050	JL03050
60	JL01060	JL02060	JL03060
70	JL01070	JL02070	JL03070
75	JL01075	JL02075	JL03075
100	JL01100	JL02100	JL03100
200	JL01200	JL02200	JL03200
250	JL01250	JL02250	JL03250
300	JL01300		
400	JL01400		
500	JL01500		

DC PARAMETERS		LIMITS	
		Min.	Max.
V _{oh}	V _{cc} = min I _{oh} = -0.40mA	2.5V	—
V _{ol}	V _{cc} = min I _{ol} = 8.0mA	—	0.5V
I _{ih}	V _{cc} = max V _{ih} = 2.7V	—	20μA
I _{il}	V _{cc} = max V _{il} = 0.40	-40.0mA	—
I _i	V _{cc} = max V _i = 5.5V	—	0.10mA
V _i	V _{cc} = min I _{in} = -18 mdc	-1.5vdc	—
I _{cc}	V _{cc} = max outputs low	Series JL05 25mA Series JL01 12mA Series JL02 22mA Series JL03 32mA	

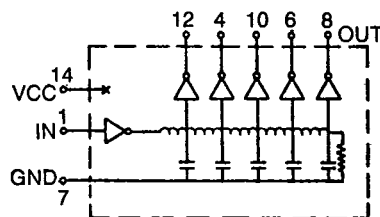


FIG.1

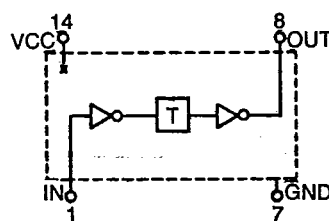


FIG.2

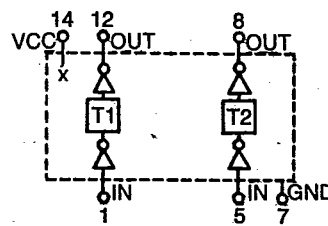


FIG.3

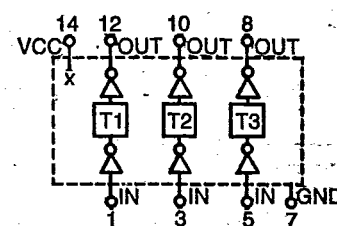


FIG.4

SPECIFICATIONS:

- Supply voltage: 5.0VDC $\pm 5\%$
- Delay tolerances: ± 2 ns or $\pm 5\%$ whichever is greater
- Rise time: 8.0ns max
- Minimum Pulse Width: 40% of Total Delay
- Maximum Duty Cycle: 50%
- Operating temp. range: 0°C to 70°C
- Temp. coeff. of delays: 1.0ns + 1000ppm/°C
- Terminals: Electro tin plated alloy 42 .020w x .010th

TEST CONDITIONS:

- V_{cc} = 5.0VDC; Temp. 25°C ± 5 °C
- Time delay measured at the 1.5V level
- Rise time measured from .75V to 2.4V
- All outputs loaded with 15pf
- Input Test Pulse:

Pulse Voltage: 3.0V
Pulse rise time: 2ns
Pulse width: 1.2 x max T_d
Pulse spacing: 5 x max T_d