



7A Ultra Low Dropout Voltage Regulator Fast Response, Adjustable & Fixed

FEATURES

- Low Dropout Voltage 540mV at 7A Full Load Current
- Adjustable Output Down to 1.2V from ATX Power Supply
- Fixed Output Voltages of 3.3V, 2.8V & 2.5V
- Extremely Tight Voltage and Line Regulation
- Standard 5-Terminal Low Cost TO-220 & TO-263
- Replacement to Industry Standard LT1580

APPLICATIONS

- 3.3V to 2.8V ATX Power Supplies
- 3.3V to 2.9V for Portable PENTIUM™ Processor
- 5V to 3.5V VRE Supply
- High Efficiency "Green" Computer Systems

PRODUCT DESCRIPTION

The Alpha Semiconductor AS1580 is a 7A Low Dropout Regulator with extremely low dropout voltage. The adjustable version requires only two external resistors to set the output voltage. The fixed version has a preset output of 3.3V, 2.8V or 2.5V and does not require any external resistors. The AS1580 features a low dropout of 540mV(Typ.) and offers fast transient response. This device is suitable for Pentium applications requiring 2.8V or 2.5V from 3.3V ATX power supplies, where a low current input voltage 1V greater than the output voltage is needed. With an external sense pin the load regulation is less than 1mV. This device is an excellent choice for the use in powering low voltage microprocessors that require a lower dropout, fast transient response to regulate from 3.3V and 5V supplies. The AS1580 is also an excellent choice as a post regulator for switching supplies applications.

The AS1580 offers protection against over-current faults, reversed input polarity, over temperature operation and positive and negative transient voltage.

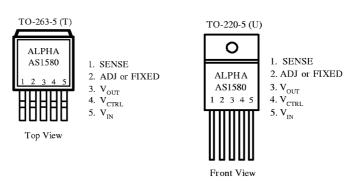
The AS1580 is offered in a 5 pin TO-220 and TO-263 compatible with industry standard 5-terminal regulators. For 5A, 3A and 1.5A ultra low dropout versions refer to AS1581, AS1582 and AS1583 data sheets respectively

ORDERING INFORMATION

TO-220	TO-263	OUTPUT
5 PIN	5 PIN	
AS1580U	AS1580T	Adjustable
AS1580U-X	AS1580T-X	Fixed

Consult with factory for other fixed output voltages. X= Output Voltage (3.3V, 2.8V or 2.5V)

PIN CONNECTIONS



ABSOLUTE MAXIMUM RATINGS

Power Dissipation	Internally Limited	Inpu
Lead Temp (soldering, 10 seconds)	300°C	V_{CTR}
Storage Temperature Range	65°C to +150°C	
Operating Junction Temperature Range		
AS1580 Control Section	0°C to +125°C	
AS1580 Power Transistor	0°C to +150°C	

Input Supply Voltage	6V
V _{CTRL} Input Voltage	. 13V

ELECTRICAL CHARACTERISTICS at $V_s = 14V$, $T_a = 25$ °C, $I_0 = 10$ mA, $C_2 = 100\mu$ F, unless otherwise specified. (Note 1)

(Boldface applies over full temperature range). AS1580 **Parameters** Conditions Units Min Typ Max 2.5V Version Output Voltage V_{CTRL} =6.0V to 12V, V_{IN} =3.0V to 5.0V, I_{O} =10mA 2.450 2.5 2.550 v 2.400 2.5 2.600 2.8V Version V_{CTRL} =6.3V to 12V, V_{IN} =3.3V to 5.0V, I_{O} =10mA Output Voltage 2.744 2.8 2.856 $\overline{\mathbf{v}}$ $I_0=10mA$ to 7A2.688 2.8 2.912 3.3V Version Output Voltage V_{CTRL} =6.3V to 12V, V_{IN} =3.3V to 5.0V, I_{O} =10mA 3.234 3.3 3.366 V 3.3 $I_0=10$ mA to 7A 3.168 3.432 All Voltage Options V_{CTRL}=2.75V, V_{IN}=2.00V, I_O=10mA Reference Voltage (Note 3) 1.2375 1.25 1.375 $\overline{\mathbf{v}}$ $V_{\text{CTRL}}\!\!=\!\!2.7V$ to 12V, $V_{\text{IN}}\!\!=\!\!2.05V$ to 5.5V, $I_{\text{O}}\!\!=\!\!10\text{mA}$ to 7A Line Regulation V_{CTRL} =2.5V to 12V, V_{IN} =1.75V to 5.5V, I_{O} =10mA $V_{ADJ}=0V$ 1.0 3.0 mVLoad Regulation (Note1) V_{CTRL} =2.75V, V_{IN} =2.1V, I_{O} =10mA to 7A, V_{ADJ} =0V 1.0 5.0 mV Dropout Voltage Minimum V_{CTRL}(Note2) V_{ADJ}=0V 1.00 1.20 V $V_{IN}=2.05V, I_{O}=1A$ $(V_{CTRL} - V_{OUT})$ Dropout Voltage Minimum V_{IN}(Note2) V_{ADJ}=0V 0.54 0.62 V $V_{IN}=2.75V, I_O=7A$ $(V_{IN} - V_{OUT})$ Current Limit V_{CTRL} =2.75V, V_{IN} =2.05V, dV_{O} =100mV, V_{ADJ} =0V 8 Α Minimum Load Current $V_{CTRL}=5V$, $V_{IN}=3.3V$, $V_{ADJ}=0V$ 5 10 mΑ Thermal Regulation 30ms Pulse 0.002 0.02 %W Ripple Rejection V_{CTRL} =3.75V V_{IN} =3.75V, I_{O} =2.7A, V_{ADJ} =0V 60 80 dB $T_J=25$, $V_{RIPPLE}=1Vpp$ at 120HzControl Pin Current V_{ADJ}=0V 60 130 $V_{CTRL} = 2.75V, V_{IN} = 2.05V, I_O = 7A$ mAAdjustable Pin Current $V_{\text{CTRL}} = 2.75 \text{V}, V_{\text{IN}} = 2.05 \text{V}, V_{\text{ADJ}} = 0 \text{V}$ 50 120 I₀=10mA μΑ Thermal Resistance TO-220-5 Junction to Case (θ_{JC}) 3 °C/W 50 °C/W Junction to Ambient (θ_{JA}) TO-263-5 3 °C/W Junction to Case (θ_{JC}) Junction to Ambient (θ_{JA}) 60 °C/W

The Bold specifications applying to the over full operating temperature range.

Note 1: Low duty cycle pulse testing with Kelvin connections is required to order to maintain accurate data.

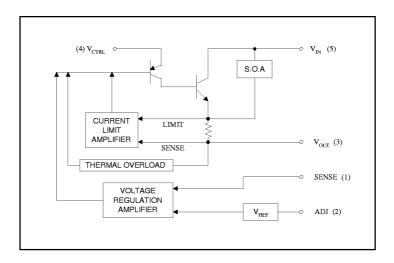
Note 2: Dropout voltage is defined as the minimum differential between V_{IN} and V_{OUT} or V_{CTRL} and V_{OUT} required to maintain regulation at V_{OUT} 99% Nominal V_{OUT} .

Note 3: V_{REF} is measured across Adjust pin to Sense pin.

PIN DESCRIPTION

- 1. Sense = Allows Kelvin sense of V_{OUT} at the load. (Positive side of the reference voltage of the device).
- 2. ADJ = Negative side of the reference voltage for the device. Adding a small bypass capacitor from the ADJ pin to ground will improve the transient response.
- 3. V_{OUT} = Power output of the device.
- 4. V_{CTRL} = Supply pin for the control circuitry of the device. The current flow into this pin will be about 1% of the output current. V_{CTRL} must be between 1.0V and 1.3V greater than the output voltage for the device to regulate.
- 5. V_{IN} = Output load current is supplied through this pin. V_{IN} must be between 0.1V and 0.8V greater than the output voltage for the device to regulate.

BLOCK DIAGRAM



APPLICATIONS NOTES

The AS1580 is designed as a high performance and low cost solution for application requiring a lower dropout than traditional NPN regulators.

The AS1580 uses a separate input voltage V_{CTRL} ($V_{CTRL} \ge V_{OUT} + 1.3V$) to minimize the dropout voltage. This allows the 2.5V power for the load to come from a 3.3V system supply. As added benefit this will reduce the heat dissipation*, and lower heatsink and cooling fan cost. A typical application would use 5V for Vin and 3.3V for V_{CTRL} from a motherboard power supply to provide a nominal 2.5V output. Using the sense pin allows to Kelvin measure the output, reducing resistive-associated errors.

The AS1580 can power the 2.5V core voltage for microprocessors such as PentiumTM, P55CTM, AMD5k86TM and K6TM and the IBM PowerPCTM 603EV and 604EV processors.

*The reduction of heat dissipation is a result of the increase of the regulator efficiency (efficiency = $V_{\rm OUT}$ / $V_{\rm IN}$).

Adjustable Regulator Design

1.25V reference voltage is being developed between the SENSE pin and the ADJ pin of the AS1580. Adding two external resistors (see fig 1.) will allow setting the output voltage from 1.25V to 6V. $R_{\rm 1}$ is chosen so that this current is specified minimum load current of 10mA. $R_{\rm 2}$ is given by the formula: $V_{\rm OUT} = V_{\rm REF} \, (1 + R_2/R_1) + I_{\rm ADJ} \, (R_2)$. The current flowing from the ADJ pin is typically 50 μ A. This ADJ pin contributes to the final VOUT but is usually neglected. Connecting the sense pin to the top of the resistor divider will improve load regulation.

Lowering Noise

Using the SENSE pin to Kelvin the load will increase accuracy of the output voltage during load regulation. For the fixed voltage devices, adding a capacitor at the GND pin will improve transient response. This capacitor is chosen in the range of $1\mu F$ to $0.1\mu F$ and will depend on the amount of output capacitance in the system.

TYPICAL APPLICATION

Fig. 1 Adjustable Regulator

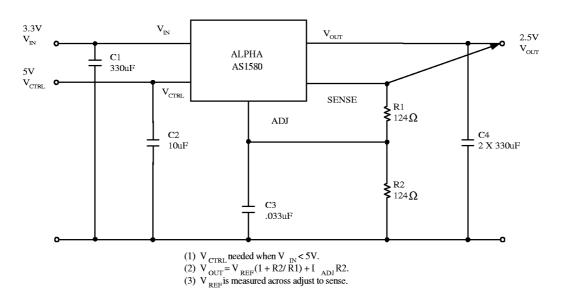


Fig.2 Typical Fixed Regulator

