

## FEATURES/BENEFITS

- Enhanced N channel FET with no inherent diode to  $V_{CC}$
- $5\Omega$  bidirectional switches connect inputs to outputs
- Pin compatible with the 74F245, 74FCT245, and 74FCT245T
- Low power CMOS proprietary technology
- Zero propagation delay, zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in SOIC (SO), & QSOP
- QS32245 is  $25\Omega$  version for low noise

## APPLICATIONS

- Hot-docking, hot-swapping (Application Note AN-13)
- Voltage translation (5V to 3.3V; Application Note AN-11)
- Logic replacement (data processing)
- Power conservation
- Capacitance reduction, and isolation
- Bus isolation
- Clock gating

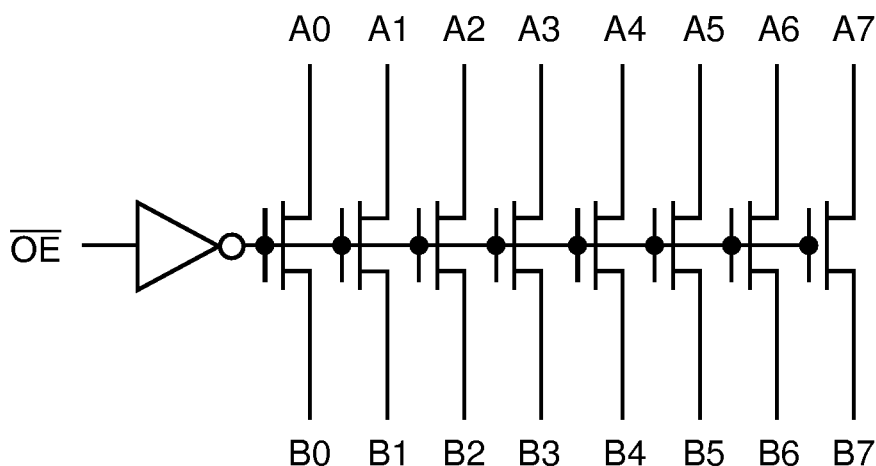
## DESCRIPTION

The QS3245 and QS32245 provide a set of eight high-speed CMOS TTL-compatible bus switches in a pinout compatible with 74FCT245, 74F245, 74ALS/AS/LS245 8-bit transceivers. The low ON resistance of the 3245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The Output Enable ( $\overline{OE}$ ) signal turns the switches on similar to the  $\overline{OE}$  signal of the 74'245. The QS32245 has  $25\Omega$  series resistors to reduce ground bounce noise.

QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

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**Figure 1. Functional Block Diagram**



**Table 1. Pin Description**

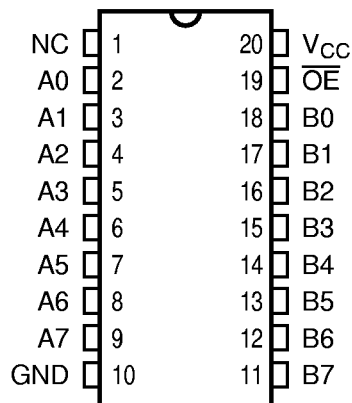
Name	Description
$\overline{OE}$	Output Enable
An	Data I/Os
Bn	Data I/Os

**Table 2. Function Table**

$\overline{OE}$	Outputs
H	Disconnected
L	An = Bn

**Figure 2. Pin Configuration  
(All Pins Top View)**

PDIP, SOIC (SO), QSOP



**Table 3. Absolute Maximum Ratings**

Supply Voltage to Ground .....	–0.5V to +7.0V
DC Switch Voltage $V_S$ .....	–0.5V to +7.0V
DC Input Voltage $V_{IN}$ .....	–0.5V to +7.0V
AC Input Voltage (for a pulse width $\leq 20$ ns) .....	–3.0V
DC Output Current Max. Sink Current/Pin .....	120mA
Maximum Power Dissipation .....	0.5 watts
$T_{STG}$ Storage Temperature .....	–65° to +150°C

**Note:** ABSOLUTE MAXIMUM CONTINUOUS RATINGS are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum conditions is not implied.

**Table 4. Capacitance**

$T_A = 25^\circ\text{C}$ ,  $f = 1\text{MHz}$ ,  $V_{IN} = 0\text{V}$ ,  $V_{OUT} = 0\text{V}$

Pins	QSOP, SOIC Typ	Max	Unit
Control Inputs	3	5	pF
QuickSwitch Channels (Switch OFF)	5	7	pF

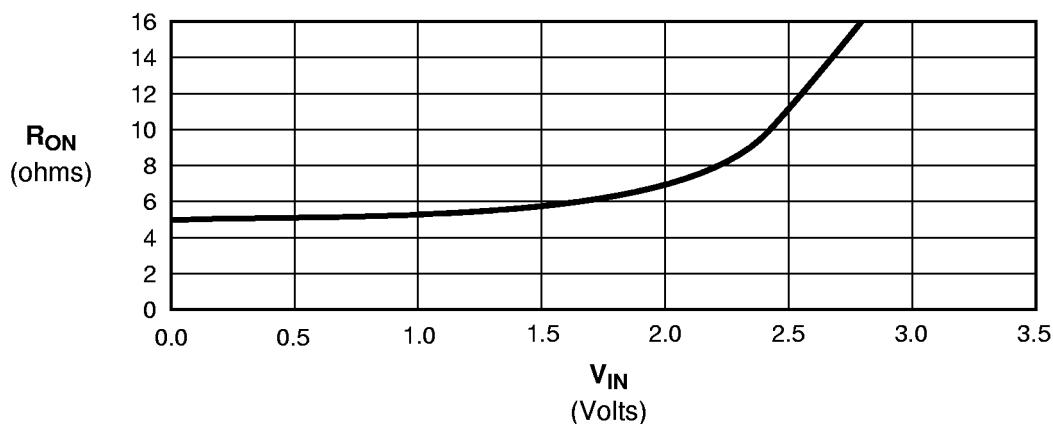
**Note:** Capacitance is guaranteed but not production tested. For total capacitance while the switch is ON, please see Section 1 under "Input and Switch Capacitance".

**Table 5. DC Electrical Characteristics Over Operating Range**Commerical:  $T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 5\%$ Military:  $T_A = -55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 10\%$ 

Symbol	Parameter	Test Conditions	Min	Typ <sup>(1)</sup>	Max	Unit
$V_{IH}$	Input HIGH Voltage	Guaranteed Logic HIGH for Control Inputs	2.0	—	—	V
$V_{IL}$	Input LOW Voltage	Guaranteed Logic LOW for Control Inputs	—	—	0.8	V
$ I_{IN} $	Input Leakage Current (Control Inputs)	$0\text{V} \leq V_{IN} \leq V_{CC}$	—	—	1	$\mu\text{A}$
$ I_{OZ} $	Off-State Current (Hi-Z)	$0\text{V} \leq V_{OUT} \leq V_{CC}$ , Switches OFF	—	0.001	1	$\mu\text{A}$
$R_{ON}$	Switch ON Resistance <sup>(2)</sup>	$V_{CC} = \text{Min.}$ , $V_{IN} = 0.0\text{V}$ 3245	—	5	7	$\Omega$
		$I_{ON} = 30\text{mA}$ 32245	20	28	40	
$R_{ON}$	Switch ON Resistance <sup>(2)</sup>	$V_{CC} = \text{Min.}$ , $V_{IN} = 2.4\text{V}$ 3245	—	10	15	$\Omega$
		$I_{ON} = 15\text{mA}$ 32245	20	35	48	
$V_P$	Pass Voltage <sup>(3)</sup>	$V_{IN} = V_{CC} = 5\text{V}$ , $I_{OUT} = -5\mu\text{A}$	3.7	4	4.2	V

**Notes:**

1. Typical values indicate  $V_{CC} = 5.0\text{V}$  and  $T_A = 25^{\circ}\text{C}$ .
2. For a diagram explaining the procedure for  $R_{ON}$  measurement, please see Section 1 under "DC Electrical Characteristics." Max value of  $R_{ON}$  guaranteed, but not production tested.
3. Pass voltage is guaranteed, but not production tested.

**Figure 3. Typical ON Resistance vs  $V_{IN}$  at  $V_{CC} = 5.0\text{V}$  (QS3245)****Note:** For QS32245, and  $23\Omega$  to  $R_{ON}$  shown.

**Table 6. Power Supply Characteristics**Commercial:  $T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 5\%$ Military:  $T_A = -55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 10\%$ 

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Max	Unit
$I_{CCQ}$	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$ , $V_{IN} = \text{GND}$ or $V_{CC}$ , $f = 0$	3.0	$\mu\text{A}$
$\Delta I_{CC}$	Power Supply Current <sup>(2)</sup> per Input HIGH	$V_{CC} = \text{Max.}$ , $V_{IN} = 3.4\text{V}$ , $f = 0$ per Control Input	1.5	mA
$Q_{CCD}$	Dynamic Power Supply Current per MHz <sup>(3)</sup>	$V_{CC} = \text{Max.}$ , A and B Pins Open, Controls Inputs Toggling @ 50% Duty Cycle	0.25	mA/MHz

**Notes:**

1. For conditions shown as Min. or Max., use the appropriate values specified under DC specifications.
2. Per TTL driven input ( $V_{IN} = 3.4\text{V}$ , control inputs only). A and B pins do not contribute to  $I_{CC}$ .
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed, but not production tested.

**Table 7. Switching Characteristics Over Operating Range**Commercial:  $T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 5\%$ Military:  $T_A = -55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 10\%$  $C_{LOAD} = 50\text{pF}$ ,  $R_{LOAD} = 500\Omega$  unless otherwise noted.

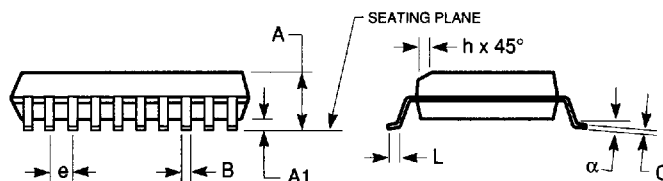
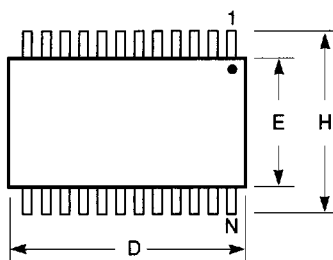
Symbol	Description <sup>(1)</sup>	QS3245			QS32245			Unit
		Min	Typ	Max	Min	Typ	Max	
$t_{PLH}$	Data Propagation Delay <sup>(2,3)</sup>	—	—	0.25	—	—	1.25	ns
$t_{PHL}$	An to/from Bn MIL	—	—	0.75	—	—	—	
$t_{PZL}$	Switch Turn-on Delay	0.5	—	5.6	0.5	—	6.6	ns
$t_{PZH}$	$\overline{\text{OE}}$ to An/Bn MIL	—	—	6.6	—	—	—	
$t_{PLZ}$	Switch Turn-off Delay <sup>(2)</sup>	0.5	—	4.5	0.5	—	4.5	ns
$t_{PHZ}$	$\overline{\text{OE}}$ to An/Bn MIL	—	—	5.5	—	—	—	

**Notes:**

1. See Test Circuit and Waveforms. Minimums guaranteed, but not production tested.
2. This parameter is guaranteed, but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for QS3245 and 1.25ns for QS32245 for  $C_L = 50\text{pF}$ . Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## 300-MIL SOIC - Package Code SO

Plastic Small Outline Gull-Wing



**Notes:**

1. Refer to applicable symbol list.
2. All dimensions are in inches.
3. N is the number of lead positions.
4. Dimensions D and E are to be measured at maximum material condition but do not include mold flash. Allowable mold flash is 0.006in. per side.
5. Lead coplanarity is 0.004in. maximum.

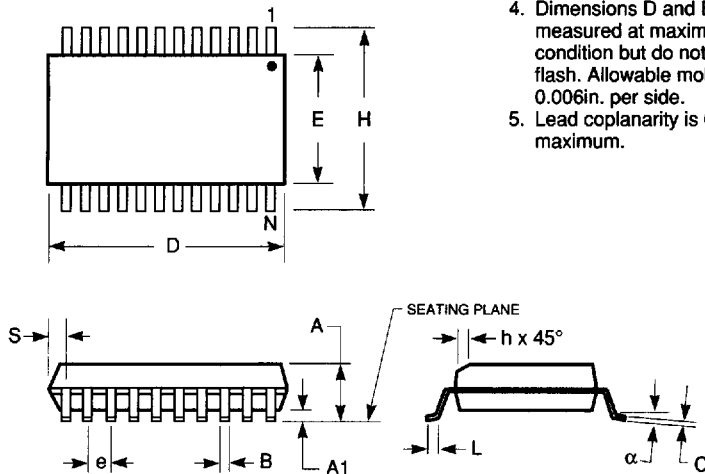
JEDEC#	MS-013AA		MS-013AC		MS-013AD		MS-013AE	
DWG#	PS16A		PS20A		PS24A		PS28A	
Symbol	Min	Max	Min	Max	Min	Max	Min	Max
A	0.096	0.104	0.096	0.104	0.096	0.104	0.096	0.104
A1	0.005	0.011	0.005	0.011	0.005	0.011	0.005	0.011
B	0.014	0.019	0.014	0.019	0.014	0.019	0.014	0.019
C	0.009	0.012	0.009	0.012	0.009	0.012	0.009	0.012
D	0.402	0.412	0.500	0.510	0.602	0.612	0.701	0.711
E	0.292	0.299	0.292	0.299	0.292	0.299	0.292	0.299
e	0.044	0.056	0.044	0.056	0.044	0.056	0.044	0.056
H	0.396	0.416	0.396	0.416	0.396	0.416	0.396	0.416
h	0.010	0.016	0.010	0.016	0.010	0.016	0.010	0.016
L	0.020	0.040	0.020	0.040	0.020	0.040	0.020	0.040
N	16		20		24		28	
$\alpha$	0°	8°	0°	8°	0°	8°	0°	8°

7466803 0003749 900

QUALITY SEMICONDUCTOR, INC.

## 150-MIL QSOP - Package Code Q

Quarter-Size Outline Package  
Plastic Small Outline Gull-Wing



**Notes:**

1. Refer to applicable symbol list.
2. All dimensions are in inches.
3. N is the number of lead positions.
4. Dimensions D and E are to be measured at maximum material condition but do not include mold flash. Allowable mold flash is 0.006in. per side.
5. Lead coplanarity is 0.004in. maximum.

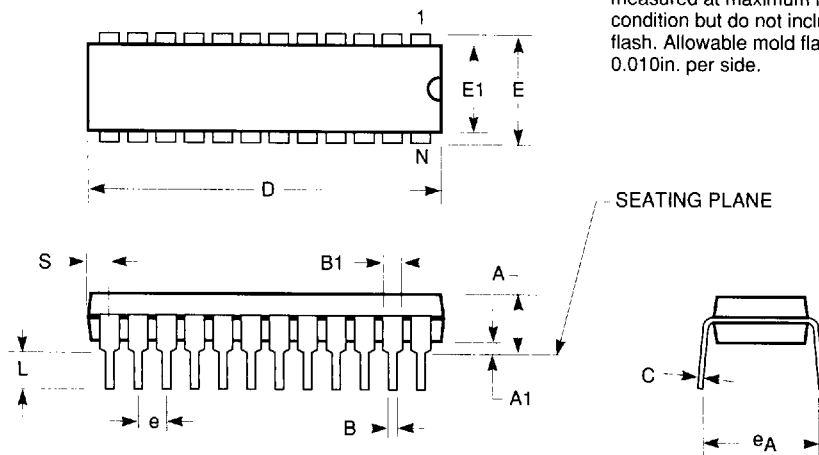
JEDEC#	MO-137AB			MO-137AD			MO-137AE			MO-137AF		
DWG#	PSS-16A			PSS-20A			PSS-24A			PSS-28A		
Symbol	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max
A	0.060	0.064	0.068	0.060	0.064	0.068	0.060	0.064	0.068	0.060	0.064	0.068
A1	0.004	0.006	0.008	0.004	0.006	0.008	0.004	0.006	0.008	0.004	0.006	0.008
B	0.009	0.010	0.012	0.009	0.010	0.012	0.009	0.010	0.012	0.009	0.010	0.012
C	0.007	0.008	0.010	0.007	0.008	0.010	0.007	0.008	0.010	0.007	0.008	0.010
D	0.189	0.193	0.197	0.337	0.341	0.344	0.337	0.341	0.344	0.386	0.390	0.394
E	0.150	0.154	0.157	0.150	0.154	0.157	0.150	0.154	0.157	0.150	0.154	0.157
e	0.025 BSC			0.025 BSC			0.025 BSC			0.025 BSC		
H	0.230	0.236	0.244	0.230	0.236	0.244	0.230	0.236	0.244	0.230	0.236	0.244
h	0.010	0.013	0.016	0.010	0.013	0.016	0.010	0.013	0.016	0.010	0.013	0.016
L	0.016	0.025	0.035	0.016	0.025	0.035	0.016	0.025	0.035	0.016	0.025	0.035
N	16			20			24			28		
$\alpha$	0°	5°	8°	0°	5°	8°	0°	5°	8°	0°	5°	8°
S	0.006	0.009	0.010	0.056	0.058	0.060	0.031	0.033	0.035	0.031	0.033	0.035

### 300-MIL PDIP - Package Code P

#### Plastic Dual In-line Package

**Notes:**

1. Refer to applicable symbol list.
2. All dimensions are in inches.
3. N is the number of lead positions.
4. Dimensions D and E1 are to be measured at maximum material condition but do not include mold flash. Allowable mold flash is 0.010in. per side.



JEDEC#	MS-001AC		MS001AA		MS-001AE		N/A		MS-001AF		MO-095AH	
DWG#	PD14A		PD16A		PD20A		PT22B		PT24A		PT28A	
Symbol	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max
A	0.130	0.170	0.130	0.170	0.130	0.170	0.130	0.170	0.130	0.170	0.130	0.180
A1	0.015	0.040	0.015	0.040	0.015	0.040	0.015	0.040	0.015	0.040	0.015	0.040
B	0.016	0.020	0.016	0.020	0.016	0.020	0.016	0.020	0.016	0.020	0.016	0.020
B1	0.045	0.070	0.045	0.070	0.045	0.070	0.045	0.070	0.045	0.070	0.045	0.060
C	0.009	0.012	0.009	0.012	0.009	0.012	0.009	0.012	0.009	0.012	0.009	0.012
D	0.745	0.765	0.745	0.765	1.020	1.040	1.020	1.040	1.150	1.260	1.345	1.385
E	0.300	0.325	0.300	0.325	0.300	0.325	0.300	0.325	0.300	0.325	0.300	0.325
E1	0.240	0.270	0.240	0.270	0.240	0.270	0.240	0.270	0.250	0.280	0.275	0.295
e	0.090	0.110	0.090	0.110	0.090	0.110	0.090	0.110	0.090	0.110	0.090	0.110
$e_A$	0.310	0.380	0.310	0.380	0.310	0.380	0.310	0.380	0.310	0.380	0.310	0.380
L	0.120	0.140	0.120	0.140	0.120	0.140	0.120	0.140	0.120	0.140	0.120	0.140
S	0.070	0.080	0.020	0.035	0.060	0.070	0.010	0.020	0.025	0.080	0.020	0.040
N	14		16		20		22		24		28	