

DIGITAL DELAY LINE SERIES A447 10 TAP

AVAILABLE IN FAST LOGIC
REQUEST A427 SERIES

TECHNICAL INFORMATION

TEST CONDITIONS

Pulse Voltage 3.2 Volts
Rise Time 3.0 Nsec (10%-90%)
Pulse Width $1.2 \times$ Total Delay
Pulse Period $4 \times$ Pulse Width
Supply Current, I_{CC1} 120.0 Milliamps typical
Supply Voltage, V_{CC} 5.0 Volts
Ambient Temperature 25°C

PERFORMANCE CHARACTERISTICS

Delay Tolerance From Input To Tap
 ± 2 Nsec or 5% whichever is greater
Delay Tolerance From Tap To Tap
 ± 2 Nsec or 7% whichever is greater
Performance Characteristics apply at
above listed Test Conditions.

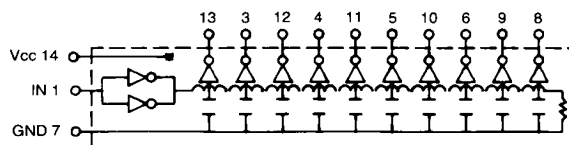
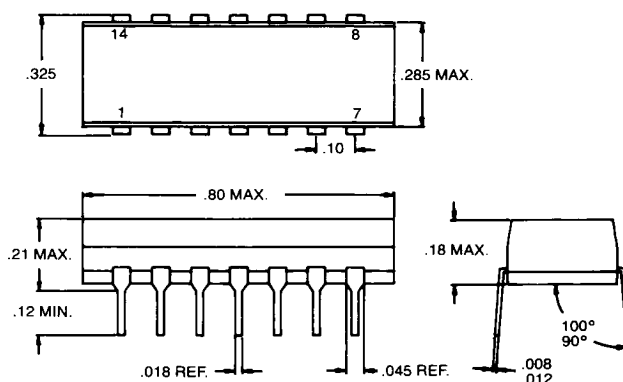
ELECTRICAL CHARACTERISTICS

Supply Voltage, V_{CC}
4.75 to 5.25 Volts
Logic 1 Input Current
100 Microamp max.
Logic 0 Input Current
-4 Milliamp max.
Logic 1 Output Voltage
2.7 Volts min.
Logic 0 Output Voltage
0.5 Volts max.
Operating Temperature Range
0°C To 70°C
Temperature Coefficient Of Total Delay
500PPM/°C Typical
Minimum Input Pulse Width
40% Of Total Delay
Maximum Duty Cycle
50%

DRIVE CAPABILITIES

10 TTL Loads/Tap max.
20 TTL Loads/Unit max.

—Compatible with TTL and DTL circuits
—Other delays and tolerances upon
request



Part Number	Total Delay 1, 3	Delay/Tap 1, 3	Rise Time 2, 3
A447-0050-10	50NS	5NS	4NS
A447-0100-10	100NS	10NS	4NS
A447-0150-10	150NS	15NS	4NS
A447-0200-10	200NS	20NS	4NS
A447-0250-10	250NS	25NS	4NS
A447-0300-10	300NS	30NS	4NS
A447-0350-10	350NS	35NS	4NS
A447-0400-10	400NS	40NS	4NS
A447-0500-10	500NS	50NS	5NS

1 Delays measured at 1.5 Volts level on Leading Edge only.
2 Rise Times measured from .75 Volts to 2.4 Volts.
3 Measured with no loads on taps.

Specifications Subject To Change Without Notice