

W91650 SERIES



10-MEMORY TONE/PULSE SWITCHABLE DIALER

GENERAL DESCRIPTION

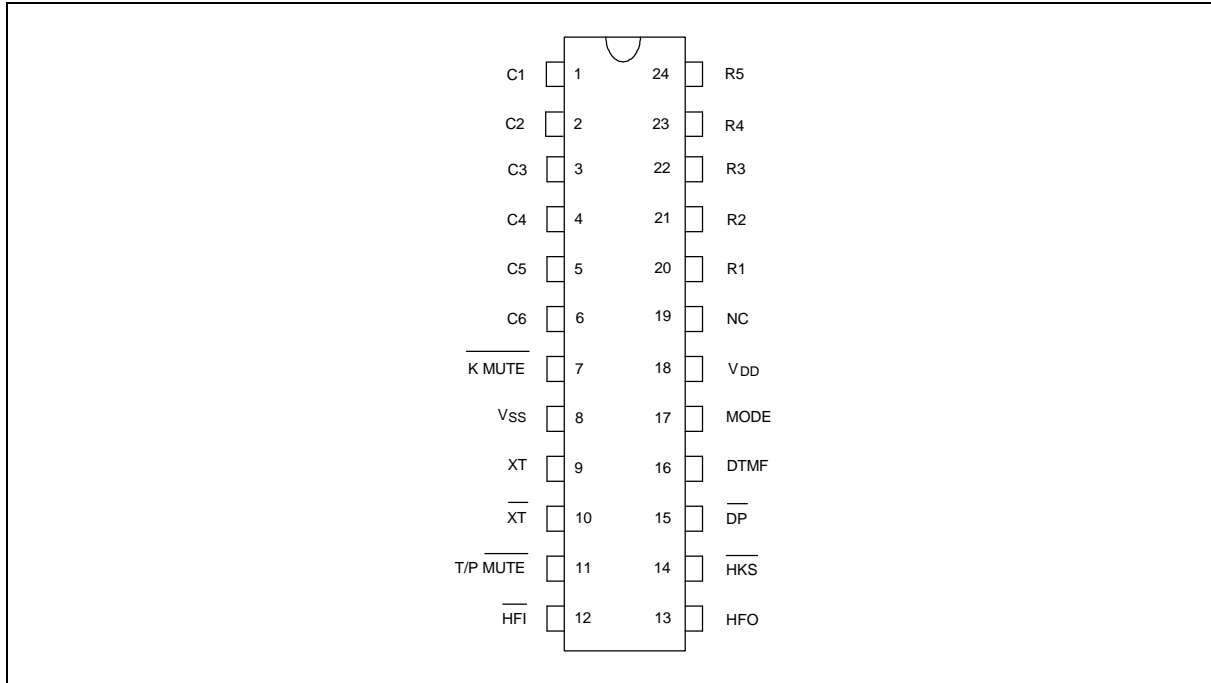
The W91650 series are Si-gate CMOS IC tone/pulse switchable dialers that include a 16-digit \times 10 one-touch memory and a 32-digit save or mercury memory. They also provide secrecy key, flash, handfree and redial functions.

FEATURES

- Tone/pulse switchable dialer
- 32-digit redial memory
- 32-digit save or mercury memory
- 16-digit \times 10 one-touch repertory memory
- Mixed dialing, cascade dialing allowed
- Use 5 \times 6 keyboard
- MUTE key for secrecy control
- Flash time: 98 mS
- Flash pause time: 1.2 sec.
- Pause time: 2.5 sec.
- Minimum tone output duration: 93 msec.
- Minimum intertone pause: 93 msec.
- Pause, */T (pulse-to-tone), flash can be stored as a digit in memory
- On-chip power-on reset
- Uses 3.579545 MHz crystal or ceramic resonator
- Packaged in 24-pin plastic DIP
- The different dialers in the W91650 series are described in the following table:

TYPE NO.	PULSE (ppS)	PAUSE (S)	B:M	FLASH (mS)	MERCURY MEMORY
W91650	10	2.5	2:1	98	SAVE
W91651	10	2.5	3:2	98	SAVE
W91650B	10	2.5	2:1	98	YES
W91651B	10	2.5	3:2	98	YES

PIN CONFIGURATION



PIN DESCRIPTION

SYMBOL	PIN	I/O	FUNCTION
Column-Row Inputs	1-6 & 20-24	I	The keyboard input may be used with either the standard 5 × 6 keyboard or the inexpensive single contact (form A) keyboard. Electronic input with μ C can also be used. A valid key entry is defined by a single row being connected to a single column.
XT, $\overline{\text{XT}}$	9, 10	I, O	A built-in inverter provides oscillation with an inexpensive 3.579545 MHz crystal or ceramic resonator.
T/P $\overline{\text{MUTE}}$	11	O	The T/P $\overline{\text{MUTE}}$ is a conventional CMOS N-channel open drain output. The output transistor is switched on during pulse and tone mode dialing sequence and flash break. Otherwise, it is switched off.
MODE	17	I	Pulling mode pin to Vss places the dialer in tone mode. Pulling mode pin to VDD places the dialer in pulse mode (10 ppS, M/B = 1:2 or 2:3).
$\overline{\text{HKS}}$	14	I	Hook switch input. $\overline{\text{HKS}} = 1$: On-hook state. Chip in sleeping mode, no operation. $\overline{\text{HKS}} = 0$: Off-hook state. Chip enabled for normal operation. $\overline{\text{HKS}}$ pin is pulled to VDD by internal resistor.

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Pin Description, continued

SYMBOL	PIN	I/O	FUNCTION																																								
\overline{DP}	15	O	N-channel open drain dialing pulse output (Figure 1). Flash key causes \overline{DP} to be active when in pulse mode.																																								
NC	19	-	No connection.																																								
DTMF	16	O	In pulse mode, remains in low state at all times. In tone mode, sends a dual or single tone. <table border="1" style="margin-left: 20px;"> <thead> <tr> <th colspan="4">Output Frequency</th> </tr> <tr> <th></th> <th>Specified</th> <th>Actual</th> <th>Error %</th> </tr> </thead> <tbody> <tr> <td>R1</td> <td>697</td> <td>699</td> <td>+0.28</td> </tr> <tr> <td>R2</td> <td>770</td> <td>766</td> <td>-0.52</td> </tr> <tr> <td>R3</td> <td>852</td> <td>848</td> <td>-0.47</td> </tr> <tr> <td>R4</td> <td>941</td> <td>948</td> <td>+0.74</td> </tr> <tr> <td>C1</td> <td>1209</td> <td>1216</td> <td>+0.57</td> </tr> <tr> <td>C2</td> <td>1336</td> <td>1332</td> <td>-0.30</td> </tr> <tr> <td>C3</td> <td>1477</td> <td>1472</td> <td>-0.34</td> </tr> </tbody> </table>	Output Frequency					Specified	Actual	Error %	R1	697	699	+0.28	R2	770	766	-0.52	R3	852	848	-0.47	R4	941	948	+0.74	C1	1209	1216	+0.57	C2	1336	1332	-0.30	C3	1477	1472	-0.34				
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VDD, VSS	18, 8	I	Power input pins.																																								
\overline{HFI} , HFO	12, 13	I, O	Handfree control pins. A low pulse on the \overline{HFI} input pin toggles the handfree control state. Status of the handfree control states is described in the following table: <table border="1" style="margin-left: 20px;"> <thead> <tr> <th colspan="2">CURRENT STATE</th> <th colspan="3">NEXT STATE</th> </tr> <tr> <th>Hook SW.</th> <th>HFO</th> <th>Input</th> <th>HFO</th> <th>Dialing</th> </tr> </thead> <tbody> <tr> <td>-</td> <td>Low</td> <td>\overline{HFI} ↓</td> <td>High</td> <td>Yes</td> </tr> <tr> <td>On Hook</td> <td>High</td> <td>\overline{HFI} ↓</td> <td>Low</td> <td>No</td> </tr> <tr> <td>Off Hook</td> <td>High</td> <td>\overline{HFI} ↓</td> <td>Low</td> <td>Yes</td> </tr> <tr> <td>On Hook</td> <td>-</td> <td>Off Hook</td> <td>Low</td> <td>Yes</td> </tr> <tr> <td>Off Hook</td> <td>Low</td> <td>On Hook</td> <td>Low</td> <td>No</td> </tr> <tr> <td>Off Hook</td> <td>High</td> <td>On Hook</td> <td>High</td> <td>Yes</td> </tr> </tbody> </table> \overline{HFI} pin is pulled to VDD by internal resistor.	CURRENT STATE		NEXT STATE			Hook SW.	HFO	Input	HFO	Dialing	-	Low	\overline{HFI} ↓	High	Yes	On Hook	High	\overline{HFI} ↓	Low	No	Off Hook	High	\overline{HFI} ↓	Low	Yes	On Hook	-	Off Hook	Low	Yes	Off Hook	Low	On Hook	Low	No	Off Hook	High	On Hook	High	Yes
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Off Hook	High	\overline{HFI} ↓	Low	Yes																																							
On Hook	-	Off Hook	Low	Yes																																							
Off Hook	Low	On Hook	Low	No																																							
Off Hook	High	On Hook	High	Yes																																							
\overline{KMUTE}	7	O	The \overline{KMUTE} is NMOS open drain output. \overline{KMUTE} is active low when MUTE key is pressed after OFF HOOK or when HFO is in high state. The \overline{KMUTE} state is toggled by the MUTE key.																																								



FUNCTIONAL DESCRIPTION

Keyboard Operation

C1	C2	C3	C4	C5	C6	
1	2	3	S	M1	M6	R1
4	5	6		M2	M7	R2
7	8	9		M3	M8	R3
*/T	0	#	MER	M4	M9	R4
F	P	MUTE	R	M5	M10	R5

Note: The **MER** key is for the W91650B/651B only. In the other type numbers (W91650/651) this key activates the save function.

- S: Memory store function key
- F: Flash key with 98 mS break time and 1.2 sec pause time
- R: Redial function key
- P: Pause function key
- Mn: one-touch direct memory
- */T: Pulse-to-tone switch function key in pulse mode, * key in tone mode
- SAVE: One-touch memory for save dialing
Save dialing can be executed after off-hook or handfree dialing is activated.
- MER: One-touch memory for mercury code dialing
- MUTE: Secrecy control key

Once the MUTE key is pressed, the \overline{KMUTE} output will be toggled.

Note: Dn = 0 to 9, */T, #, Mn = M1 to M10.

Normal Dialing

OFF HOOK (or **ON HOOK** & $\overline{HFI} \overline{i\dot{O}}$), **D1**, **D2**, ..., **Dn**

1. D1, D2, ..., Dn will be dialed out.
2. Dialing length is unlimited, but redial is inhibited if length exceeds 32 digits.

Redialing

1. Redialing is valid any time after off-hook or handfree dialing is activated.
2. The redial function timing diagram is shown in Figure 1.

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, , , ..., , Busy, Come , , (or &) or & , , , ..., BUSY, , Come ,

D1, D2, ..., Dn will be dialed out.

Number Store

1. (or &) , (or)
 , , ..., ,
 - a. D1, D2, ..., Dn will be stored in memory Mn (or save) location but will not be dialed out.
 - b. , , and keys can be stored as a digit in memory. The store mode is released after the store function is executed or the state of the hook switch is changed.
2. (or &) , (or) , ,
 - a. Redial buffer is transferred to Mn (or save memory).
 - b. If content of the redial buffer exceeds 16 digits, the content is not transferred to Mn.
3. (or &) , , ,
 - a. Save memory is transferred to Mn.
 - b. If content of the save buffer exceeds 16 digits, the content is not transferred to Mn.
4. , , , ..., ,

D1, D2, ..., Dn will be stored to save memory.

Mercury Store

(or &) , , , , ..., ,

D1, D2, ..., Dn will be stored in mercury memory but will not be dialed out.

Memory Clear

(or &) , , (or) ,

Mn (or save) will be cleared.



Repertory Dialing

1. OFF HOOK (or ON HOOK & $\overline{\text{HF1}} \overline{\text{i0}}$), Mn (or SAVE or R)

Mn (or save) will be dialing out.

Mn content = D1, D2, */T, D3, D4

2. OFF HOOK , Mn

a. D1, D2, P→T, D3, D4 will be dialed out.

b. Redial register is changed to D1, D2, P→T, D3, D4.

Access Pause

- OFF HOOK (or ON HOOK & $\overline{\text{HF1}} \overline{\text{i0}}$), D1 , D2 , P , D3 , ..., Dn

1. The pause function can be stored in memory.
2. The pause function is executed in normal dialing, redialing, or memory dialing.
3. The pause function timing diagram is shown in Figure 3.

Pulse-to-tone (*/ T)

- OFF HOOK (or ON HOOK & $\overline{\text{HF1}} \overline{\text{i0}}$), D1 , D2 , ..., Dn , */T ,
D1' , D2' , ..., Dn'

1. If the mode switch is set to pulse mode, then the output signal will be: D1, D2, ..., Dn, Pause (2.5s), D1', D2', ..., Dn'
(Pulse) (Tone)
2. If the mode switch is set to tone mode, then the output signal will be: D1, D2, ..., Dn, * , D1', D2', ..., Dn'
(Tone) (Tone) (Tone)
3. The dialer remains in tone mode when the digits have been dialed out and can be reset to pulse mode only by going on-hook.
4. The */T function timing diagram is shown in Figure 4.

Flash Key

- OFF HOOK (or ON HOOK & $\overline{\text{HF1}} \overline{\text{i0}}$), F

1. Flash key can be stored as a digit in memory.
2. The flash key function timing diagram is shown in Figure 5.



Mute Key

OFF HOOK (or ON HOOK & $\overline{\text{HFI}} \overline{\text{IO}}$), D1 , D2 , ..., Dn , ON LINE,
 MUTE , D1' , D2' , ..., Dn' , ON LINE, MUTE

1. The KMUTE output will go low when the first MUTE key is pressed.
2. The operation timing diagram is shown in Figure 6(a, b).

Cascade Dialing

Cascade Dialing

1. Definition of cascade dialing:

The next sequence may be pressed before the former sequence is sent out completely. Examples of cascade dialing are shown below:

Example 1:

Normal dialing + Repertory dialing 1 + Repertory dialing 2 + ...

Example 2:

Repertory dialing 1 + Normal dialing + Repertory dialing 2 + ...

Example 3:

Redialing + Normal dialing + Repertory dialing 2 + ...

2. Normal dialing, redialing, or repertory dialing as depicted above is treated as one sequence.
3. A most 32 digits are allowed in cascade dialing, with no limitation on the number of sequences.
4. The content of cascade dialing can be a combination of normal dialing, redialing, repertory dialing.
5. ON HOOK , OFF HOOK , R : All the cascade-dialing sequences described in the above examples will be dialed out only if there are not more than 32 digits. If the sequence exceeds 32 digits then redialing is inhibited. (The R key can be used any time after off-hook or handfree activity.)

Mixed Dialing

1. Definition of mixed dialing:

In the examples above, if each sequence is dialed only after the preceding sequence is dialed out completely, then this is mixed dialing.

2. There is no limitation on the number of digits and sequences in mixed dialing.
3. The content of mixed dialing can be a combination of normal dialing, redialing, and repertory dialing.



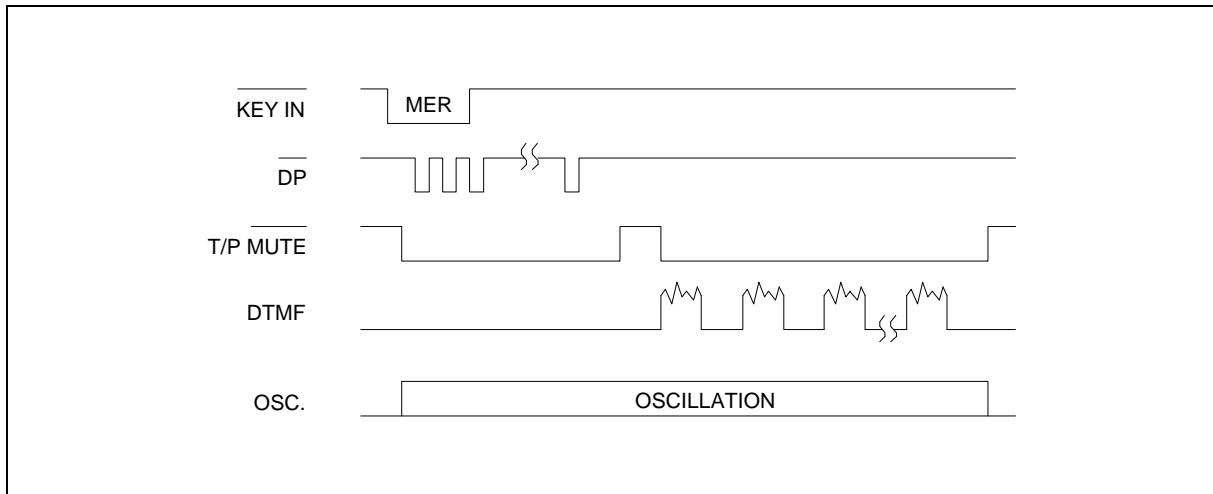
4. **ON HOOK** , **OFF HOOK** , **R** : All the mixed dialing sequences described in the above examples will be dialed out only if there are not more than 32 digits. If the sequence exceeds 32 digits then the redialing is inhibited.

Combination(s) of Cascade and Mixed Dialing

1. Cascade dialing and mixed dialing can be combined, and each follows the rules described above.
2. To apply redial to a combination of cascade and mixed dialing:
ON HOOK , **OFF HOOK** , **R** . Redialing will be executed only if the total number of digits does not exceed 32 digits. If it exceeds 32 digits, then redialing is inhibited.
3. If n cascaded sequences have been dialed, with a total of 30 digits, then for the (n+1)th cascade sequence, you can dial one 2-digit normal dialing sequence or one complete repertory dialing number (length up to 32 digits). The (n+2)th sequence is not accepted for cascade dialing.
4. After an a total of 32 digit of cascaded mixed dialing is completed, mixed dialing can be added.

Mercury Dialing

1. Up to 32 digits may be stored.
2. Mercury dialing is active only as the first key-in after off-hook or handfree dialing is activated.
3. The timing diagram for the mercury memory function is given below.



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ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD} -V _{SS}	-0.3 to +7.0	V
Input/Output Voltage	V _{IL}	V _{SS} -0.3	V
	V _{IH}	V _{DD} +0.3	V
	V _{OL}	V _{SS} -0.3	V
	V _{OH}	V _{DD} +0.3	V
Power Dissipation	P _D	120	mW
Operating Temperature	T _{OPR}	-20 to 70	°C
Storage Temperature	T _{STG}	-55 to 125	°C

Note: Exposure to conditions beyond those listed under Absolute Maximum Ratings may adversely affect the life and reliability of the device.

DC CHARACTERISTICS

(V_{DD}-V_{SS} = 2.5V, F_{osc.} = 3.58 MHz, T_A = 25° C, all outputs unloaded)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V _{DD}		2.0	-	5.5	V
Operating Current	I _{OP}	Tone mode	-	0.5	1.0	mA
		Pulse mode	-	0.3	0.5	
Standby Current	I _{SB}	$\overline{HKS} = 0$, no load and no key entry	-	-	15	μA
Memory Retention Current	I _{MR}	$\overline{HKS} = 1$, V _{DD} = 1.0V	-	-	0.2	μA
DTMF Output Voltage	V _{TO}	Row group, R _L = 5 KΩ	130	150	170	V _{rms}
Pre-emphasis		Col/Row, V _{DD} = 2.0 to 5.5V	1	2	3	dB
DTMF Distortion	THD	R _L = 5 KΩ, V _{DD} = 2.0 to 5.5V	-	-30	-23	dB
DTMF Output DC Level	V _{TDC}	R _L = 5 KΩ, V _{DD} = 2.0 to 5.5V	1.0	-	3.0	V
DTMF Sink Current	I _{TL}	V _{TO} = 0.5V	0.2	-	-	mA
\overline{DP} Sink Current	I _{PL}	V _{PO} = 0.5V	0.5	-	-	mA
\overline{KMUTE} , T/P \overline{MUTE} Output Sink Current	I _{ML}	V _{MO} = 0.5V	0.5	-	-	mA

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DC Characteristics, continued

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
HKS I/P Pull High Resistor	RKH		-	500	-	K Ω
HFO Drive Current	IHFH	VHFH = 2.0V	0.5	-	-	mA
HFO Sink Current	IHFL	VHFL = 0.5V	0.5	-	-	mA
Keypad Drive Current	IKD	V _I = 0V	30	-	-	μ A
Keypad Sink Current	IKS	V _I = 2.5V	200	400	-	μ A
Keypad Resistance			-	-	5.0	K Ω

AC CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Keypad Active in Debounce	TKID		-	20	-	mS
Key Release Debounce	TKRD		-	20	-	mS
Pre-digit Pause	TPDP	M/B = 1/2	-	33.3	-	mS
		M/B = 2/3	-	40	-	mS
Interdigit Pause (Auto dialing)	TIDP	10 ppS	-	800	-	mS
Make/Break Ratio	M/B	W91650/650B	-	33:67	-	%
		W91651/651B	-	40:60	-	%
Tone Output Duration	TTD	Auto Dialing	-	93	-	mS
Intertone Pause	TITP	Auto Dialing	-	93	-	mS
Flash Break Time	TFB		-	98	-	mS
Flash Pause Time	TFP		-	1.2	-	S
Pause Time	TP		-	2.5	-	S

Notes:

- Crystal parameters suggested for proper operation are $R_s < 100 \Omega$, $L_m = 96 \text{ mH}$, $C_m = 0.02 \text{ pF}$, $C_n = 5 \text{ pF}$, $C_l = 18 \text{ pF}$, $f_{osc} = 3.579545 \text{ MHz} \pm 0.02\%$.
- Crystal oscillator accuracy directly affects these times.

TIMING WAVEFORMS

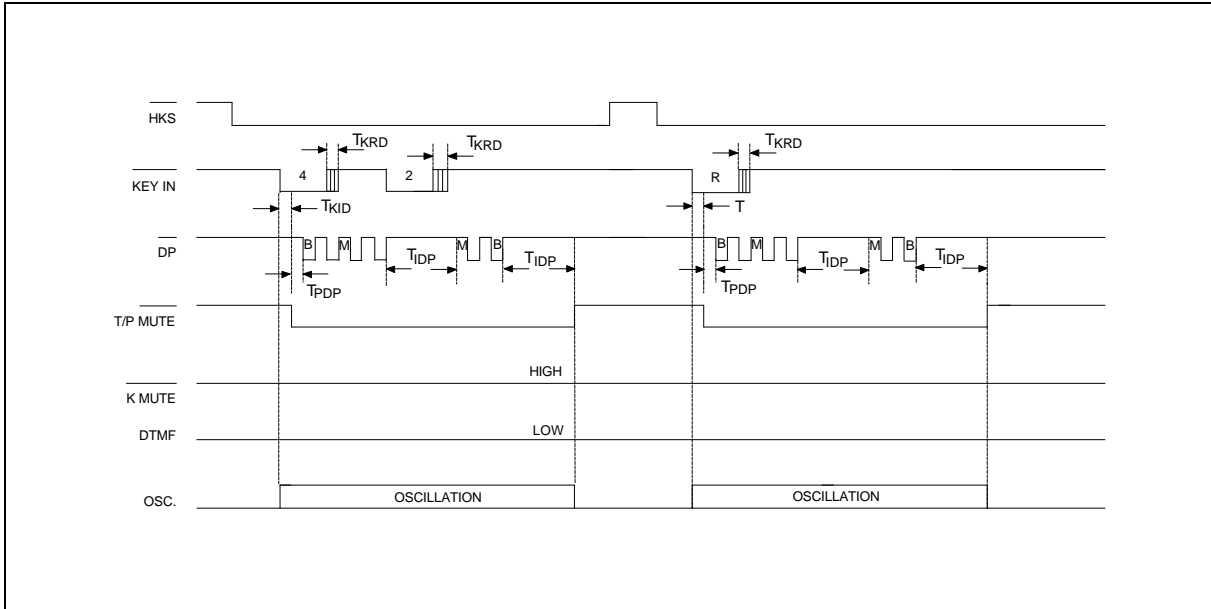


Figure 1. Pulse Mode Timing Diagram

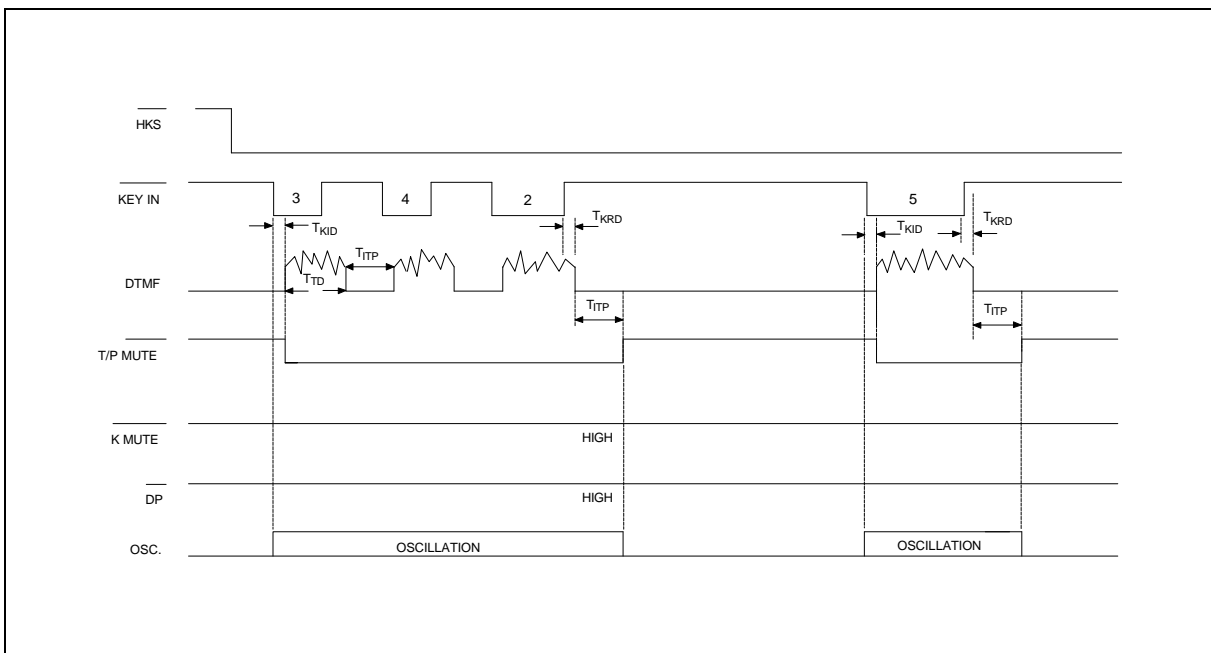


Figure 2(a). Tone Mode Normal Dialing Timing Diagram



Timing Waveforms, continued

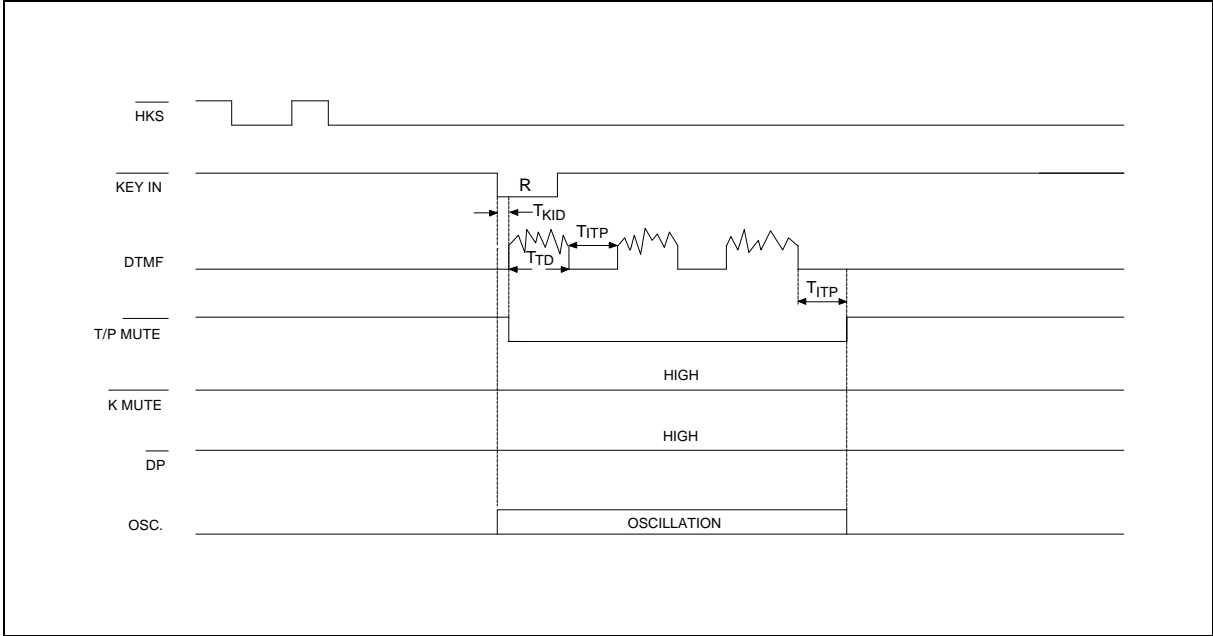


Figure 2(b). Tone Mode Auto Dialing Timing Diagram

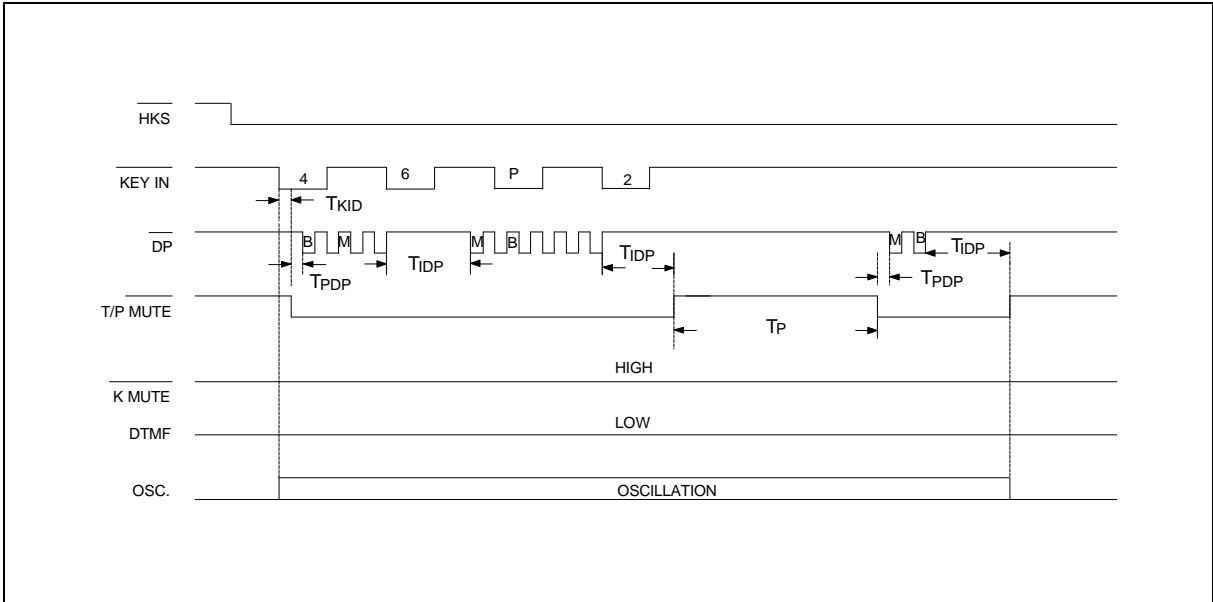


Figure 3. Pause Function Timing Diagram

Timing Waveforms, continued

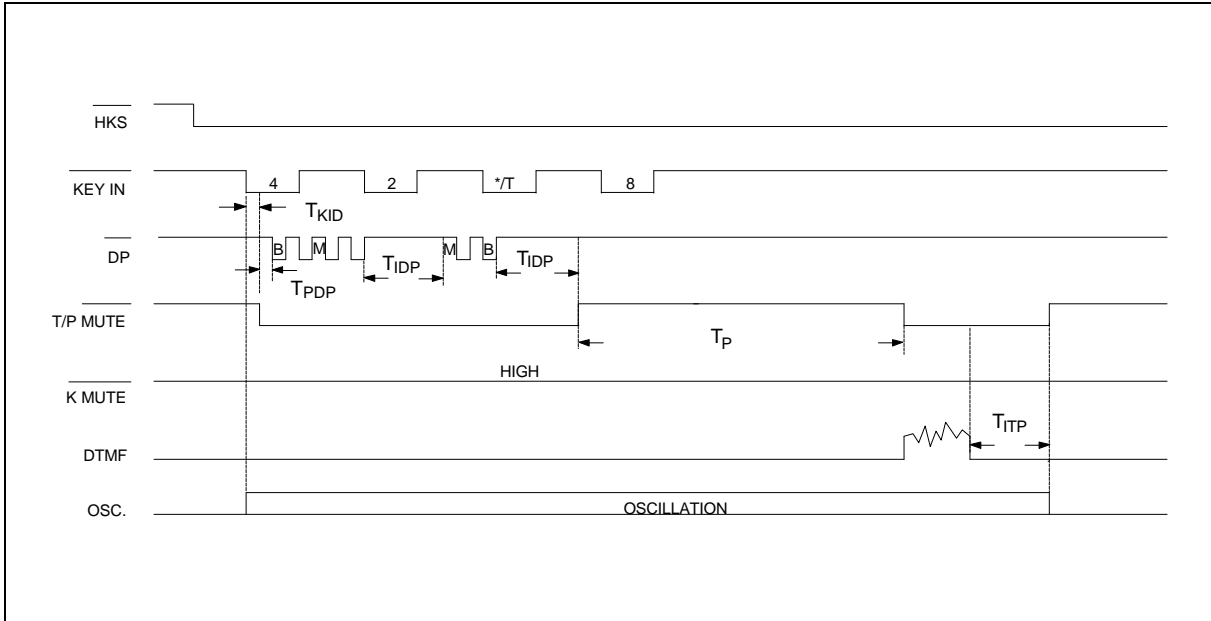


Figure 4. Pulse-to-tone Operation Timing Diagram

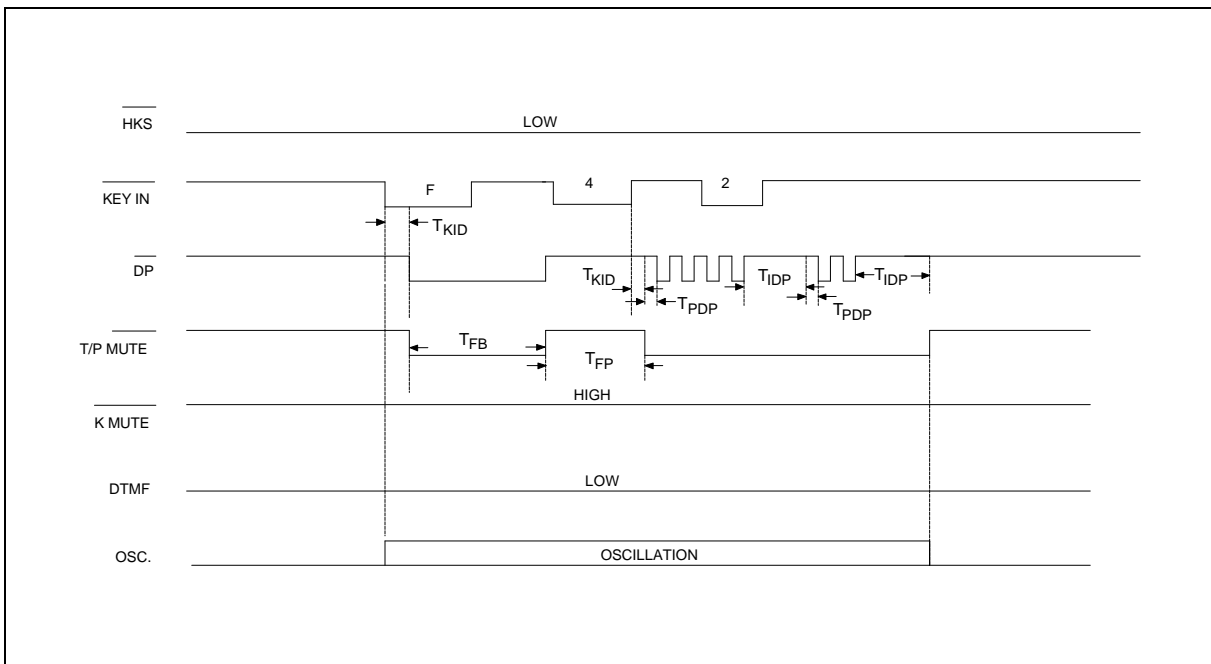


Figure 5. Flash Operation Timing Diagram



Timing Waveforms, continued

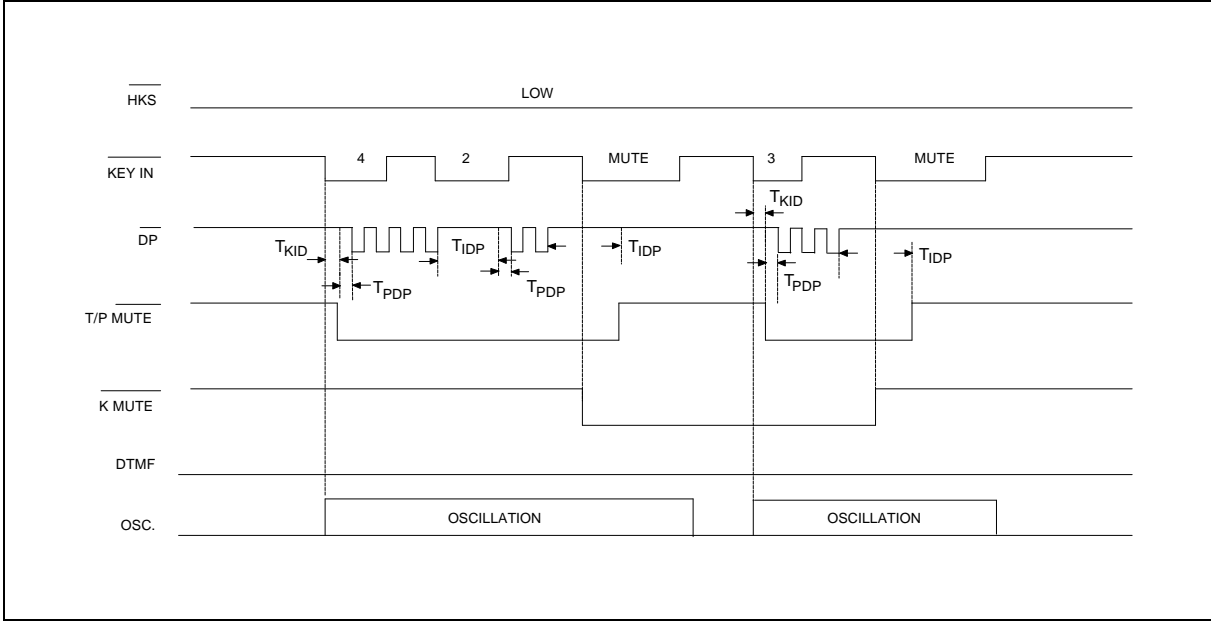


Figure 6(a). Mute Key Timing Diagram

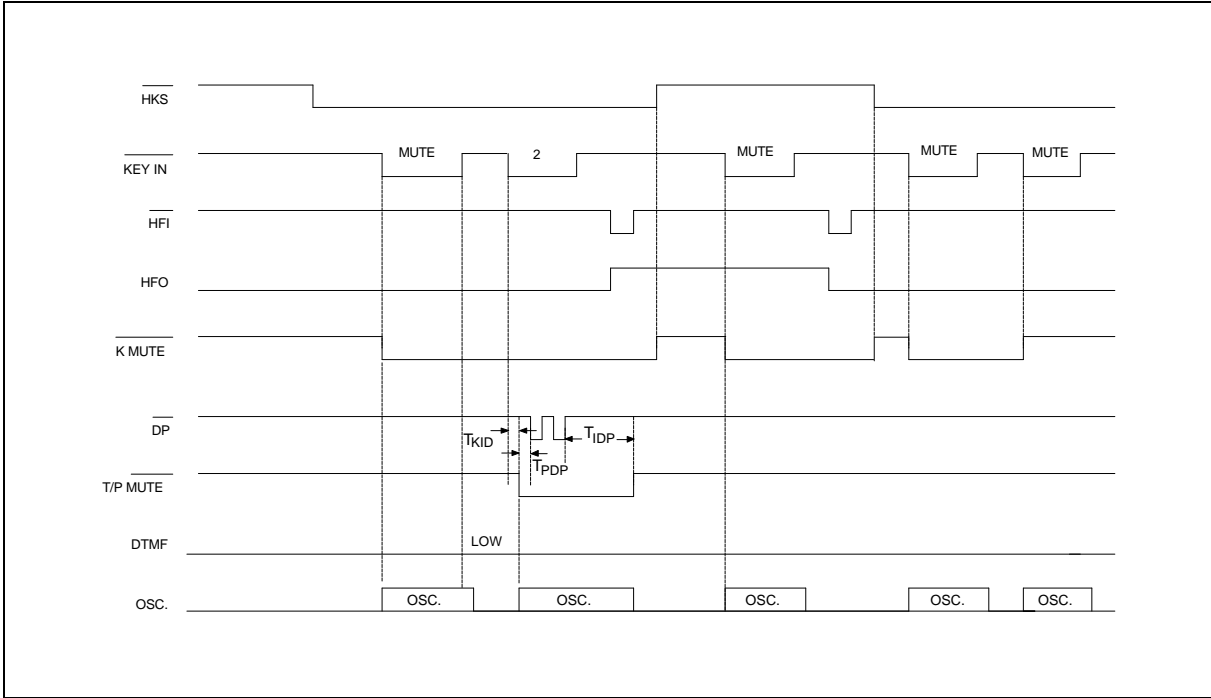


Figure 6(b). Mute Key Operation with HFI /HFO Timing Diagram

W91650 SERIES



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Note: All data and specifications are subject to change without notice.