# SPECIFICATION FOR LCD MODULE

Model No. \_\_\_\_\_TM128128ACCWF

Prepared by:	Date:
Checked by :	Date:
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Approved by:	Date:

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### **REVISION RECORD**

Date	Ver.	Ref. Page	Revision No.	<b>Revision Item</b>

### **1 General Specifications:**

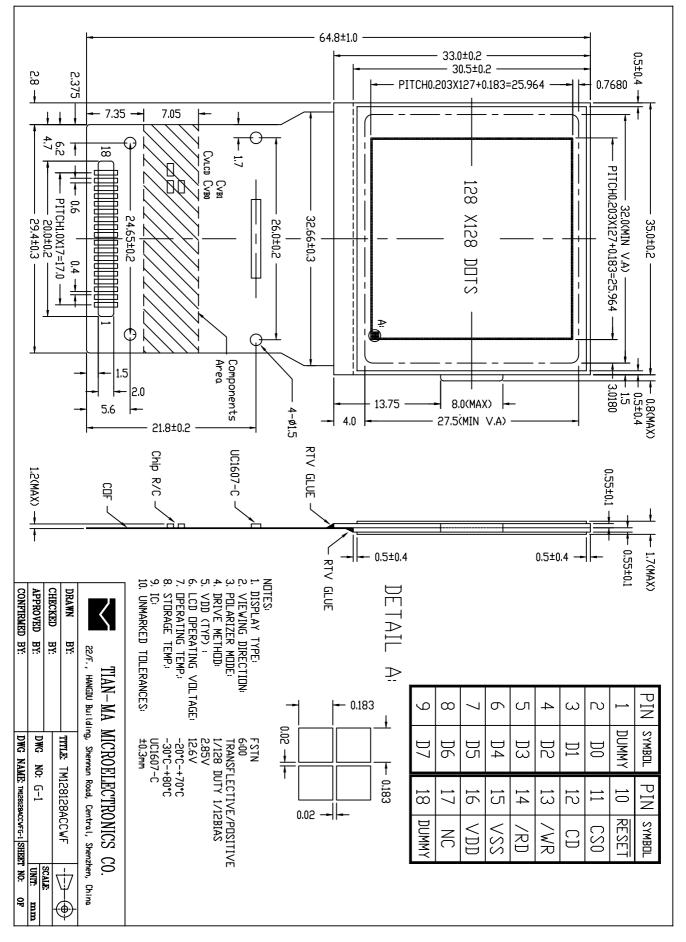
- 1.1 Display type: FSTN
- 1.2 Display color\*:

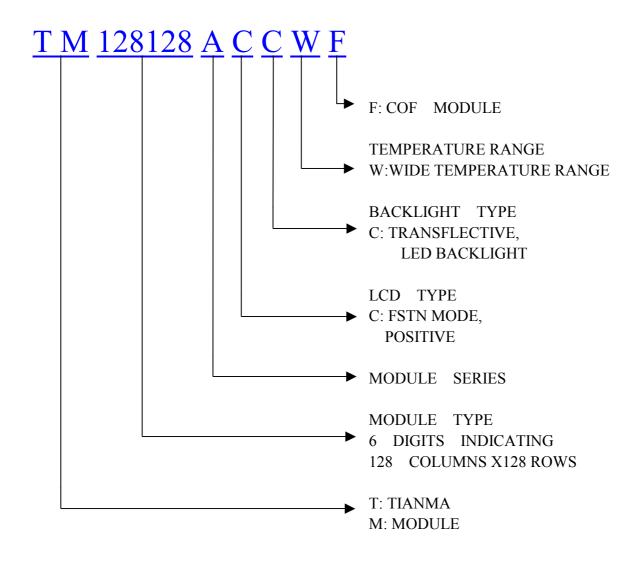
Display color: Blue-Black

Background: Gray

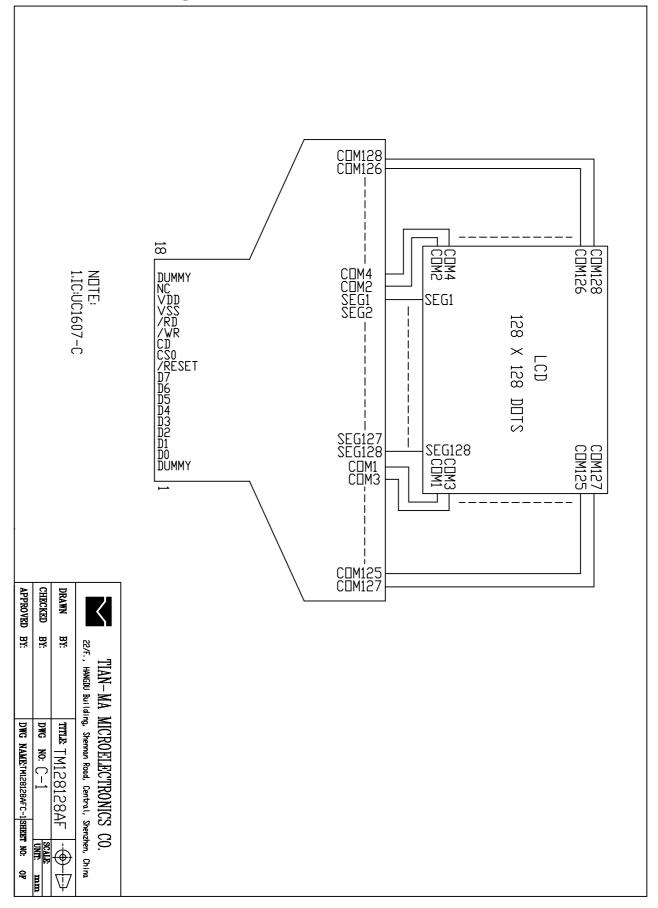
- 1.3 Polarizer mode: Transflective/Positive
- 1.4 Viewing Angle: 6:00
- 1.5 Driving Method: 1/128 Duty 1/12 Bias
- 1.6 Without Backlight
  - Color tone is slightly changed by temperature and driving voltage.
- 1.7 Controller: UC1607-C
- 1.8 Data Transfer: 8 Bit Parallel
- 1.9 Operating Temperature: -20----+70℃
  - Storage Temperature: -30----+80°C
- 1.10 Outline Dimensions: Refer to outline drawing on next page
- 1.11 Dot Matrix: 128X 128 DOTS
- 1.12 Dot Size: 0.183X0.183 (mm)
- 1.13 Dot Pitch: 0.203X0.203 (mm)
- 1.14 Weight: 15g(approx.)

### **2** Outline Drawing





### 4 Circuit Block Diagram



### **5** Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	Vdd-Vss	-0.3	6.0	v	
LCD Driving Voltage	VLCD	-	25.0	v	
Operating Temperature Range	Тор	-20	+70	°C	No
Storage Temperature Range	Тѕт	-30	+80		Condensation

# **6 Electrical Specifications and Instruction Code** 6.1 Electrical characteristics

Iten	n	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)		Vdd-Vss	1.8	3.0	3.3	V
Supply Voltage (LCD Drive)		Vlcd	6.5	12.6	14	V
Input	High	$V_{IH}$ ( $V_{DD}=3.0$ )	$0.8V_{DD}$	-	V <sub>DD</sub> +0.3	V
Signal Voltage	Low	$V_{IL}$ (V <sub>DD</sub> =3.0)	0	-	0.2 V <sub>DD</sub>	V
Supply current (Logic)		$I_{\text{DD}}$ $(V_{\text{DD}}-V_{\text{SS}}=3.0)$	-	-	100	uA
Supply c (LED Bac		I <sub>BL</sub>	-			mA

6.2 Interface Signals

Pin	Symbol	Level	Description
No.			
1	DUMMY	-	No Connection
2	D0	H/L	Data bit0
3	D1	H/L	Data bit1
4	D2	H/L	Data bit2
5	D3	H/L	Data bit3
6	D4	H/L	Data bit4
7	D5	H/L	Data bit5
8	D6	H/L	Data bit6
9	D7	H/L	Data bit7
10	RESET	L	Reset signal input
11	CS0	L	Chip select input
12	CD	H/L	Selects registers, L:Control data, H:Display data
13	/WR	L	Write Signal
14	/RD	L	Read Signal
15	VSS	0V	Ground
16	VDD	3.0V	Power supply voltage for logic
17	NC	-	No Connection
18	DUMMY	-	No Connection

### 6.3 Interface Timing Chart

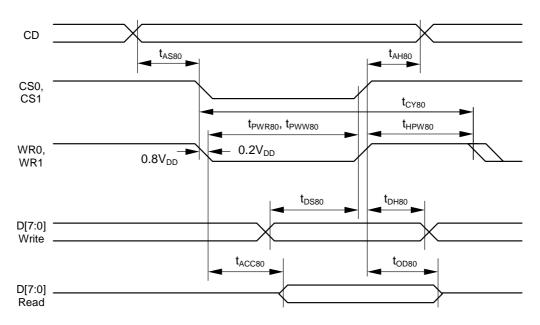


Figure 21: Parallel Bus Timing Characteristics (for 8080 MCU)

VDD=2.4V to 3.0V, Ta= -30 to +85°C)	
vDD = 2.4 v (0.5.0 v, 1a = -30 (0.405 C))	

Symbol	Signal	Description	Condition	Min.	Max.	Units
t <sub>AS80</sub> t <sub>AH80</sub>	CD	Address setup time Address hold time		20 40	Ι	ns
t <sub>CY80</sub>		System cycle time		100	-	ns
t <sub>PWR80</sub>	WR1	Pulse width (read)		45	-	ns
t <sub>PWW80</sub>	WR0	Pulse width (write)		45	-	ns
t <sub>HPW80</sub>	WR0, WR1	High pulse width		40	-	ns
t <sub>DS80</sub> t <sub>DH80</sub>	D0~D7	Data setup time Data hold time		30 10	-	ns
t <sub>ACC80</sub> t <sub>OD80</sub>		Read access time Output disable time	C <sub>L</sub> = 100pF	- 10	50 50	ns

### 6.4 Instruction Code

The following is a list of host commands support by UC1607

C/D: 0: Control, W/R: 0: Write Cycle, 1: Data 1: Read Cycle

# Useful Data bits

- Don't Care

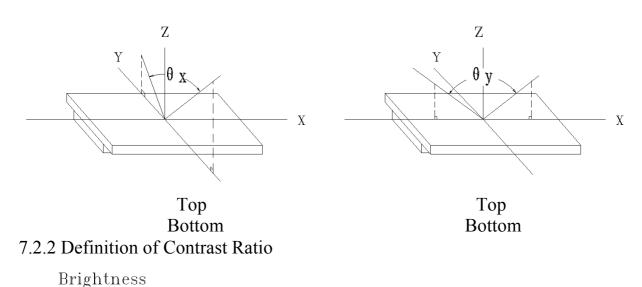
Command	C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action
Write Data Byte	1	0	#	#	#	#	#	#	#	#	Write 1 byte @ PA/CA
Read Data Byte	1	1	#	#	#	#	#	#	#	#	Read 1 byte @ PA/CA
Get Status	0	1	ΒZ	MX	DE	RS	WA	GN1	GN0	1	Get Status Summary
Set Column Address LSB	0	0	0	0	0	0	#	#	#	#	Set CA[3:0]=D[3:0]
Set Column Address MSB	0	0	0	0	0	1	#	#	#	#	Set CA[7:4] =D[3:0]
Set Mux rate.	0	0	0	0	1	0	0	0	#	#	Set MR[1:0]=D[1:0]
Set Temp. Compensation.	0	0	0	0	1	0	0	1	#	#	Set TC[1:0]=D[1:0]
Set Panel Loading	0	0	0	0	1	0	1	0	#	#	Set PC[1:0]=D[1:0]
Set Pump Control	0	0	0	0	1	0	1	1	#	#	Set PC[3:2]=D[1:0]
Set Adv. Program Control	0	0	0	0	1	1	0	0	0	R	Set APC[R][7:0]=D[7:0],
(double byte command)	0	0	#	#	#	#	#	#	#	#	where $R = 0$ , or 1
Set Max CA	0	0	0	0	1	1	0	0	1	0	Set MC = D[6:0]
(double byte command)	0	0	-	#	#	#	#	#	#	#	Set MC = D[0.0]
Set Start Line LSB	0	0	0	1	0	0	#	#	#	#	Set SL[3:0]=D[3:0]
Set Start Line MSB	0	0	0	1	0	1	#	#	#	#	Set SL[7:4]=D[3:0]
Set Page Address LSB	0	0	0	1	1	0	#	#	#	#	Set PA[3:0]=D[3:0]
Set Page Address MSB	0	0	0	1	1	1	#	#	#	#	Set PA[7:4]=D[3:0]
Set V <sub>REF</sub> potential meter	0	0	1	0	0	0	0	0	0	1	Set PM[5:0]=D[5:0]
(double-byte command)	0	0	#	#	#	#	#	#	#	#	Set GN[1:0]=D[7:6]
Set RAM Address Control	0	0	1	0	0	0	1	#	#	#	Set AC[2:0]=D[2:0]
Set Frame Rate	0	0	1	0	1	0	0	0	#	#	Set LC[4:3]=D[1:0]
Set All-Pixel-ON	0	0	1	0	1	0	0	1	0	#	Set DC[1]=D0
Set Inverse Display	0	0	1	0	1	0	0	1	1	#	Set DC[0]=D0
Set Display Enable	0	0	1	0	1	0	1	#	#	#	Set DC[4:2]=D[2:0]
Set LCD Control	0	0	1	1	0	0	0	#	#	#	Set LC[2:0]=D[2:0]
Set LCD Gray Shade	0	0	1	1	0	1	-	-	#	#	Set LC[6:5]=D[1:0]
System Reset	0	0	1	1	1	0	0	0	1	0	System Reset sequence
NOP	0	0	1	1	1	0	0	0	1	1	No operation
Set LCD Bias Ratio	0	0	1	1	1	0	1	0	#	#	Set BR[1:0]= D[1:0]
Reset Cursor Update Mode	0	0	1	1	1	0	1	1	1	0	Set AC[3]=0, CA=CR;
Set Cursor Update Mode	0	0	1	1	1	0	1	1	1	1	Set AC[3]=1, CR=CA;
Set Test Control	0	0	1	1	1	0	0	1	Т		For testing only.
(double byte command)	0	0	#	#	#	#	#	#	#	#	Do not use.

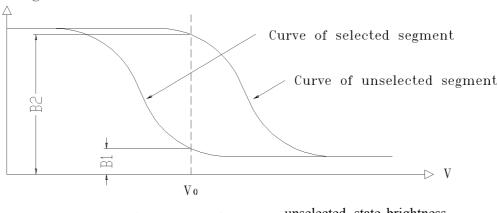
\* Other than commands listed above, all other bit patterns result in NOP (No Operation).

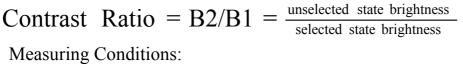
### 7 Optical Characteristics

7.1 Optical	7.1 Optical Characteristics Ta=25										
Item		Symbol	Condition		Min.	Тур.	Max.	Unit			
		θx		θy=0°	-	Dec					
Viewing A	Angle	θγ	Cr≥2	$\theta_{x}=0^{\circ}$	-30	Deg					
Contrast	Ratio	Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3	-	-				
Response Turn on		Ton		=0°	-	-	300	ms			
Time	Turn off	Toff	θy=	=0°	-	-	300	ms			

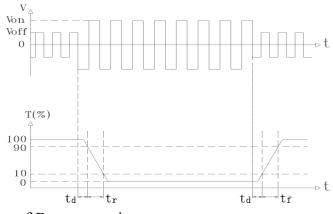
# 7.2 Definition of Optical Characteristics7.2.1 Definition of Viewing Angle







1) Ambient Temperature:  $25^{\circ}$ C 2) Frame frequency: 85.0Hz



### 7.2.3 Definition of Response time

Turn on time:  $t_{on} = t_d + t_r$ Measuring Condition: 1) Operating Voltage:12.6V Turn off time:  $t_{off} = t_d + t_f$ 

2) Frame frequency: 85.0Hz

### 8 Reliability

8.1 (	Content of Reliability	Ta=25℃	
No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	80°C
	Storage	storage temperature for a long time	240H
2	Low Temperature	Endurance test applying the low	-30°C
	Storage	storage temperature for a long time	240H
	High Temperature	Endurance test applying the	
3	Operation	electric stress (voltage & current)	70°C
5		and the thermal stress to the	240H
		element for a long time	
	Low Temperature	Endurance test applying the	<b>-20</b> °C
4	Operation	electric stress under low	240H
		temperature for a long time	
	High Temperature	Endurance test applying the high	60℃
5	/Humidity Storage	temperature and high humidity	95%RH
		storage for a long time	240H
	Temperature	Endurance test applying the low	<b>-30°</b> ℃/80°℃
	Cycle	and high temperature cycle	
6		$-30^{\circ}C \longleftrightarrow 25^{\circ}C \longleftrightarrow 80^{\circ}C \longleftrightarrow 25^{\circ}C$	10 cycles
		30min 5min 30min 5min ←	
		1 cycle	
	Vibration Test	Endurance test applying the	10Hz~500Hz,
7	(package state)	vibration during transportation	$100 {\rm m/s}^2$ ,
			120min
	Shock Test	Endurance test applying the shock	Half-sinewave,
8	(package state)	during transportation	$300 \text{m/s}^2$ ,
			18ms
_	Atmospheric	Endurance test applying the	25kPa
9	Pressure Test	atmospheric pressure during	16H
		transportation by air	

### 8.2 Failure Judgment Criterion

Criterion			Te	est	Iten	n N	0.			Failura Judgament Criterian				
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion				
Basic Specification	~	~	~	~	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	Out of the basic Specification				
Electrical specification	~	~	~	~	$\checkmark$					Out of the electrical specification				
Mechanical Specification							$\checkmark$	$\checkmark$		Out of the mechanical specification				
Optical Characteristic	~	~	~	~	$\checkmark$	$\checkmark$			$\checkmark$	Out of the optical specification				
Note	Fc	or te	est i	ten	n re	fer	to 8	8.1	_ <b>I</b>					
Remark	Remark Basic specification = Optical specification + Mechanic specification								ical specification + Mechanical					

### 9 QUALITY LEVEL

Examination	At T <sub>amb</sub> =25 °C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See anr	nex A		П	Major 1.0 Minor 2.5	
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See anr	nex B		II	Major 1.0 Minor 2.5	
Miner d	lefects: Open segment or com lefects: Others ng standard conforms to GB28	2	ort, Serio	ous dam	ages, Le	akage	

### **10 Precautions for Use of LCD Modules**

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.

If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}$ C $\sim 40^{\circ}$ CRelatively humidity: $\leq 80\%$ 

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

## Appendix A

### Inspection items and criteria for appearance defects

Items	Contents	Criteria			
Protective Glue		No clear defects			
Cover Tape	Covering all of the chip and no clear crimple				
Leakage	Not permitted				
Rainbow		According to the limit specimen			
	Wrong polarizer attachment	Not permitted			
Polarizer	Bubble between polarizer and glass	Not counted		Max. 3 defects allowed	
		ф<0.3mm	.3mm 0.3mm≤φ≤0.5		mm
	Scratches of polarizer	According to the limit specimen			
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3
		X<0.20mm	$0.20$ mm $\leqslant$ X $\leqslant$ 0.5mm		
		X=(a+b)/2			spots (lines)
Black line (in viewing area)		Not counted	Max. 3 lines allowed		allowed
		a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm		
Progressive cracks		Not permitted			

### Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents	Contents Criteria		Criteria			
	Cracks on pads	a	b		с	Max. 2	
	W	≪3mm	$\leq v$	V/5	≪T/2	Cracks	
		≤2mm	≪W	V/5	T/2 <c<t< td=""></c<t<>		
	Cracks on contact side	a			b		
		≪3m	$\leq 3$ mm $\leq T/2$		$\leq T/2$	-	
		≤2m	m	J	[/2 <b<t< td=""><td></td><td>Mary 5</td></b<t<>		Mary 5
Glass Cracks		C shall be not reach the seal area			Max. 2 cracks allowed	Max. 5 cracks allowed	
	Cracks on non-contact side	a b					
		≤3m	m		≪T/2		
		≤2m	m	]	[/2 <b<t< td=""><td colspan="2"></td></b<t<>		
		C≤0.5mm					
	יי <i>ב</i>	d≤SW/3					
	Corner cracks	e<2.0mm <sup>2</sup> f<2.0mm <sup>2</sup>			Max. 3 cracks allowed		
	f-r e-r						

### Appendix B

### Inspection items and criteria for display defects

Items		Contents	Critera			
Open segment or open common		Not permitted				
Short		Not permitted				
Wrong viewing angle		Not permitted				
Contrast radio uneven		According to the limit specimen				
Crosstalk		According to the limit specimen				
		1 <u>1</u> -a	Not counted	Max.3 dots allowed		
			X<0.1mm	0.1mm≤X≤0.2mm		
Pin holes and cracks in segment (DOT)		X=(a+b)/2	Max.3 dots			
		Not counted	Max.2 dots allowed	allowed		
		A<0.1mm	0.1mm≪A≪0.2mm D<0.25mm			
Black spot (in viewing area)			Not counted	Max.3 spots allowed		
		X<0.1mm	0.1mm≪X≪0.2mm			
		X=(a+b)/2	Max.3 spots			
Black line		Not counted	Max.3 lines allowed	- (lines) allowed		
(in viewing area)		a<0.02mm	0.02mm≤a≤0.05mm			

### Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Critera			
		Not counted	Max. 2 defects allowed		
		x<0.1mm	$0.1$ mm $\leq x \leq 0.2$ mm		
		x=(a+b)/2	- Max.3		
	D-+1+1+-a	Not counted	Max. 1 defects allowed	defects allowed	
Transfor- mation of segment		a<0.1mm	0.1mm≪a≪0.2mm D>0	anowed	
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va	2W alue of width		