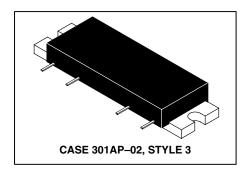
The RF Line PCS Band RF Linear LDMOS Amplifier

Designed for Class AB amplifier applications in 50 ohm systems operating in the PCS frequency band. A silicon FET design provides outstanding linearity and gain. In addition, the excellent group delay and phase linearity characteristics are ideal for digital modulation systems, such as TDMA and CDMA.

- Typical CDMA Performance: 1960 MHz, 28 Volts IS-95 CDMA Pilot, Sync, Paging, Traffic Codes 8 Through 13
- Adjacent Channel Power: -51 dBc @ 30 dBm, 885 kHz Channel Spacing
- Power Gain: 24 dB Min (@ f = 1960 MHz)
- · Excellent Phase Linearity and Group Delay Characteristics
- Ideal for Feedforward Base Station Applications

MHPA19010

1930–1990 MHz 10 W, 24 dB RF HIGH POWER LDMOS AMPLIFIER



ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

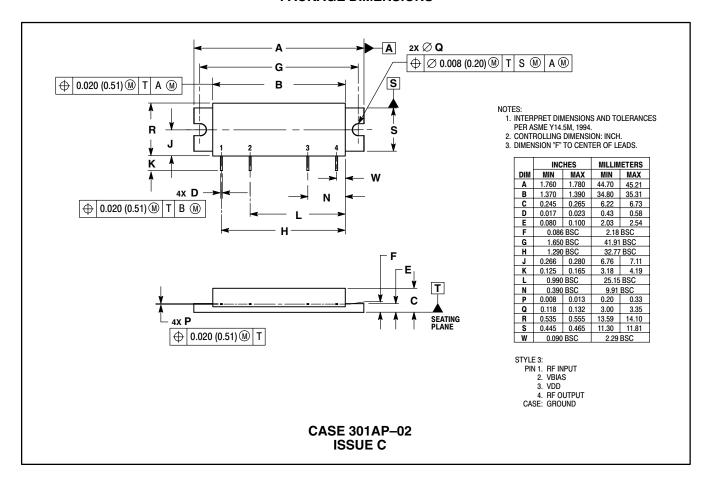
Rating	Symbol	Value	Unit
DC Supply Voltage	V_{DD}	30	Vdc
RF Input Power (Single Carrier CW)	P _{in}	+20	dBm
Storage Temperature Range	T _{stg}	-40 to +100	°C
Operating Case Temperature Range	T _C	-20 to +100	°C

ELECTRICAL CHARACTERISTICS (V_{DD} = 28 Vdc, V_{BIAS} \cong 8 V Set for Supply Current of 590 mA, T_C = 25°C, 50 Ω System)

Characteris	stic	Symbol	Min	Тур	Max	Unit
Supply Current		I _{DD}	_	590	_	mA
Power Gain	(f = 1960 MHz)	G _p	24	25	_	dB
Gain Flatness	(f = 1930-1990 MHz)	G _F	_	0.2	0.6	dB
Power Output @ 1 dB Comp.	(f = 1960 MHz)	P1dB	_	41.5	_	dBm
Input VSWR	(f = 1930-1990 MHz)	VSWR _{in}	_	1.5:1	2:1	
Noise Figure	(f = 1960 MHz)	NF	_	8	10	dB
Adjacent Channel Power Rejection @ 30 885 kHz Channel Spacing	dBm, 1.23 MHz BW,	ACPR	_	-58	-51	dBc



PACKAGE DIMENSIONS



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