REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Superseded drawing with 5962-38294.	1990 OCT 29	M. A. Frye

This drawing 5962-89691, is superseded by drawing 5962-38294 as of 29 OCT 1990. Specific part substitution information is included in 5962-38294.

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	SHEET	1																							

PMIC N/A	PREPARED BY Kenneth Rice	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444
STANDARDIZED MILITARY DRAWING	CHECKED BY Charles Reusing	MICROCIRCUIT, MEMORY, DIGITAL CMOS, 8K x 8 STATIC RANDOM ACCESS MEMORY (SRAM), MONOLITHIC SILICON
THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE	APPROVED BY M. A. Frye DRAWING APPROVAL DATE 14 NOVEMBER 1989	SIZE A · CAGE CODE 67268 · 5962-89691
AMSC N/A	REVISION LEVEL A	SHEET 1 OF 1

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