

131072-Word × 8-Bit CMOS One Time Electrically Programmable ROM (with Low V_{CC})

The HN27V101ATT/ARR series are 131072-word × 8-bit one time electrically programmable ROM. Initially, all bits of the HN27V101ATT/ARR series are in the "1" state (output high). Data is introduced by selectively programming "0" into the desired bit location. This device is packaged in a 32-pin plastic package, therefore, this device cannot be rewritten and erased. The packages of the HN27V100ATT/ARR series are surface mount thin and small outline packages. They are suitable for hand-held equipment such as a memory card.

Features

- Low voltage and wide range operation: 2.7 V to 5.5 V
- Fast high-reliability programming mode and fast high-reliability page programming mode
 - Programming voltage: +12.5 V DC
 - Fast high-reliability page programming: 14 sec typ
- High speed inputs and outputs TTL compatible during both read and program modes
- Low power dissipation: 50 mW/MHz typ (active)
5 μW typ (standby)
- Pin arrangement: 32-pin JEDEC standard except HN27V101ARR series
- Package
 - Surface mount thin and small outline package (TSOP) type II: HN27V101ATT series
 - TSOP type II reverse type: HN27V101ARR series
- Device identifier mode: manufacturer code and device code

Ordering Information

| Type No. | Access time | Package |
|----------------|-------------|---|
| HN27V101ATT-20 | 200 ns | 32-pin plastic TSOP-(II) (TTP-32D) |
| HN27V101ATT-25 | 250 ns | |
| HN27V101ARR-20 | 200 ns | 32-pin plastic TSOP-(II) (TTP-32DR) |
| HN27V101ARR-25 | 250 ns | |

Pin Arrangement

