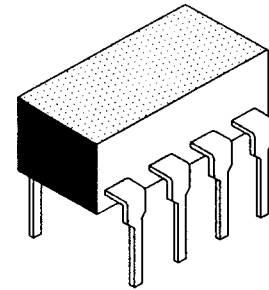


DC Electrical Characteristics			Min	Max	Unit
Parameter	Test Conditions				
V _{OH}	High-Level Output Voltage	V _{CC} =min. V _L =max. I _{OH} =max	2.7		V
V _{OL}	Low Level Output Voltage	V _{CC} =min. V _H =min. I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} =min. I _I =-18mA		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} =max. V _{IN} =2.7V		50	uA
I _{IL}	Low-Level Input Current	V _{CC} =max. V _{IN} =5.25V		1.0	mA
I _{OS}	Short Circuit Output Current	V _{CC} =max. V _{IN} =0.5V		-2	mA
		V _{CC} =max. V _{OUT} =0 (One output at a time)	-40	-100	mA
I _{CC}	High-Level Supply Current	V _{CC} =max. V _{IN} =OPEN		75	mA
I _{CL}	Low-Level Supply Current	V _{CC} =max. V _{IN} =0		75	mA
T _{RO}	Output Rise Time	T _d ≤ 500nS (0.75 to 2.4 Volts)		4	nS
		T _d > 500nS		5	nS
N _H	Fanout High-Level Output	V _{CC} =max. V _{OH} =2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} =max. V _{OH} =0.5V		10 TTL LOAD	

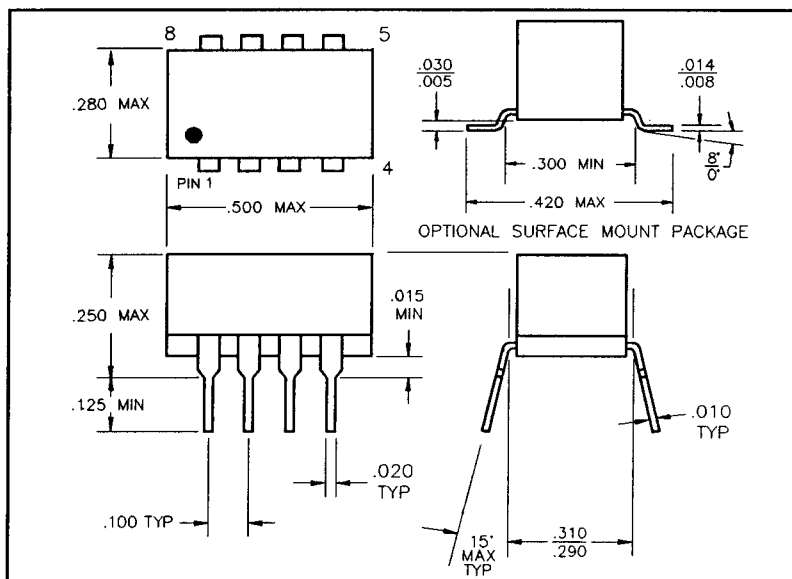
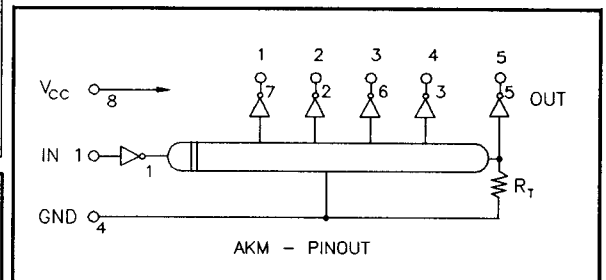


Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-level input voltage	2.0		V
V _{IL}	Low-Level input Voltage		0.8	V
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW	Pulse Width % of Total Delay	40		%
T _A	Operating Free-Air Temperature	0	+70	°C

**ALSO AVAILABLE IN FOLLOWING
LOGIC FAMILIES**

**CMOS (HCT) ARM-SERIES
FAST ARF-SERIES**

Input Pulse Test Conditions @ 25°C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts



TOTAL DELAYS nS ± 5%	TAP DELAYS ± 2 nS OR 5%	POLARA P/N AKM - PINOUT
25	5	AKM-0025
30	6	AKM-0030
35	7	AKM-0035
40	8	AKM-0040
45	9	AKM-0045
50	10	AKM-0050
60	12	AKM-0060
75	15	AKM-0075
100	20	AKM-0100
125	25	AKM-0125
150	30	AKM-0150
175	35	AKM-0175
200	40	AKM-0200
225	45	AKM-0225
250	50	AKM-0250
300	60	AKM-0300
400	80	AKM-0400
500	100	AKM-0500

ADD "S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.

* ANY VALUES NOT SHOWN, AVAILABLE UPON REQUEST.