Silicon PNP Epitaxial

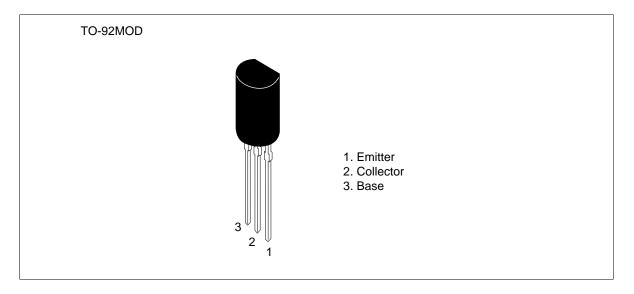
HITACHI

ADE-208-1032 (Z) 1st. Edition Mar. 2001

Application

- Low frequency power amplifier
- Complementary pair with 2SD789

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	-70	V
Collector to emitter voltage	V _{CEO}	-50	V
Emitter to base voltage	V _{EBO}	-6	V
Collector current	Ι _c	-1	А
Collector power dissipation	Pc	0.9	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Electrical Characteristics (Ta = 25°C)

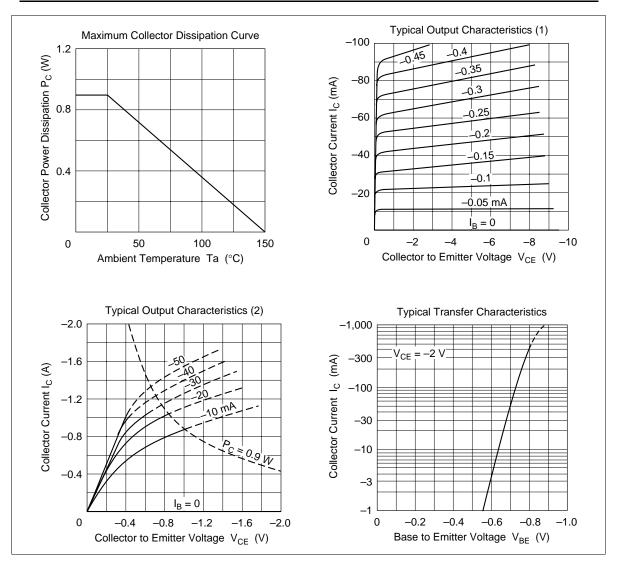
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	-70	_	_	V	$I_{c} = -10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	-50	_	_	V	$I_c = -1 \text{ mA}, \text{ R}_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	-6	_	_	V	$I_{\rm E} = -10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	—	—	-1	μΑ	$V_{CB} = -55 \text{ V}, \text{ I}_{E} = 0$
Emitter cutoff current	I _{EBO}	_	—	-0.2	μΑ	$V_{EB} = -6 V, I_{C} = 0$
DC current transfer ratio	$h_{\rm FE}^{*1}$	100	—	320		$V_{ce} = -2 V, I_c = -0.1 A$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	—	_	-0.6	V	$I_{\rm c} = -1$ A, $I_{\rm B} = -0.1$ A
Gain bandwidth product	f _⊤	_	150		MHz	$V_{ce} = -2 V, I_c = -10 mA$
Collector output capacitance	Cob	—	35	_	pF	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0,$ f = 1 MHz

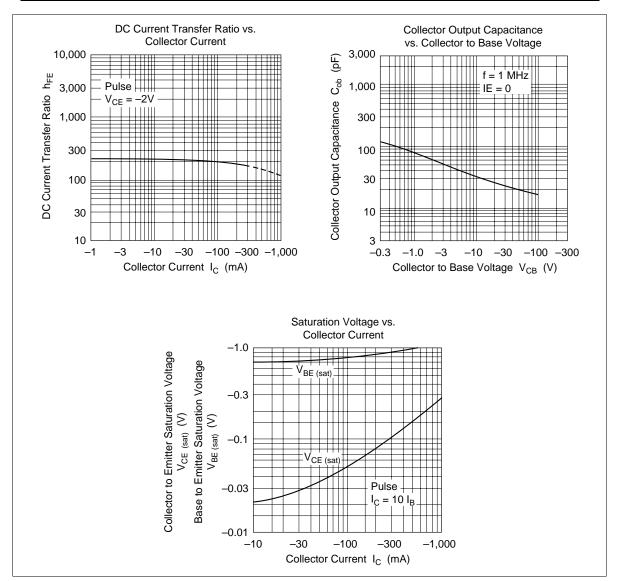
Note: 1. The 2SB740 is grouped by h_{FE} as follows.

В

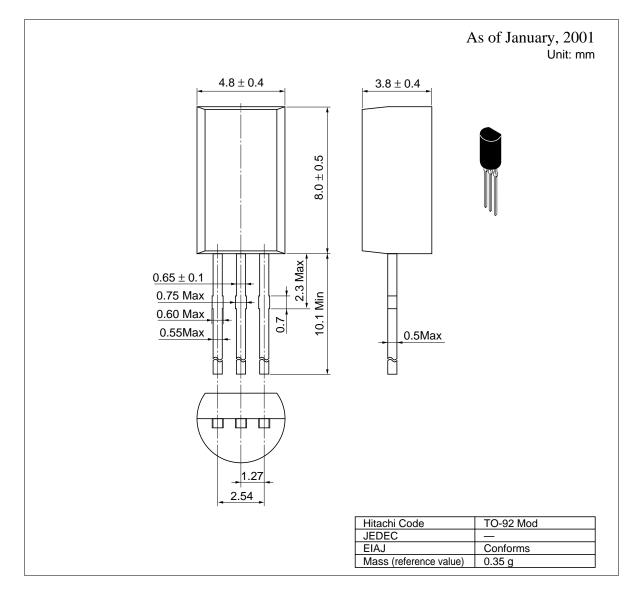
100 to 200 160 to 320

С





Package Dimensions



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