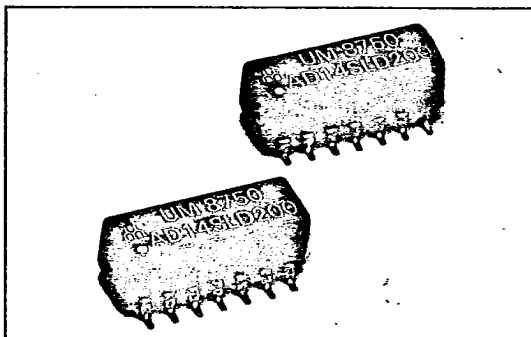




# AD14SLD SERIES: 14-PIN 10-TAP SURFACE MOUNTING, L LEAD



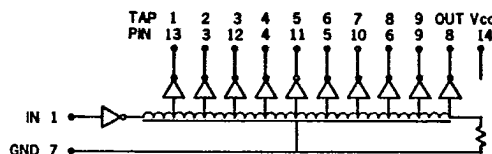
## FEATURES:

- Compatible with TTL Circuits
- 10 Equally-Spaced Delay Taps
- Surface Mount L Lead Package
- Operating Temperature Range: 0°C to +70°C
- Custom Designs (Delays or Pin Layouts) Available upon Request

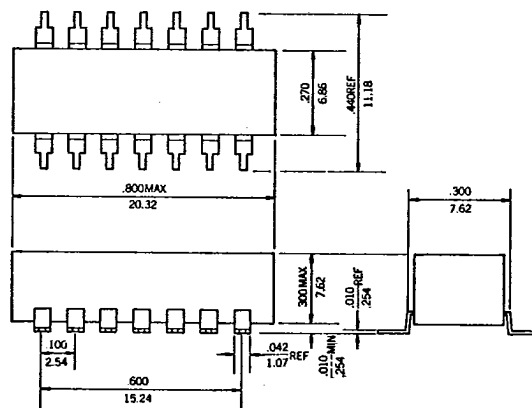
## ELECTRICAL CHARACTERISTICS

Supply Voltage  $V_{CC}$ :  $5.0 \pm 0.25$  VDC  
 Logic 1 Input Voltage: 2.0V min.  
 Logic 1 Input Current:  $50 \mu A$  max.  
 Logic 0 Input Voltage: 0.8V max.  
 Logic 0 Input Current:  $-2.0 mA$  max.  
 Logic 1 Output Voltage: 2.4V min.  
 Logic 0 Output Voltage: 0.5V max.  
 All measurements made at  $V_{CC}=5.0V$ , 25°C

## CIRCUIT AND PIN CONNECTIONS:



## PACKAGE DIMENSIONS:



## FAN OUT CAPABILITIES

Logic 0 Output: 10 TTL loads/tap max.  
 20 TTL loads/unit max.  
 Logic 1 Output: 20 TTL loads/unit max.

## INPUT TEST CONDITIONS

Pulse Voltage: 3.2V  
 Rise Time: 3.0ns  
 Supply Current: 120mA typical  
 Pulse Width: min. 100% of total delay  
 Duty Cycle: 33% or less

## ELECTRICAL SPECIFICATIONS:

| PART NO.   | TOTAL DELAY(1)<br>ns $\pm 5\%$ | TAP DELAY(1)<br>ns $\pm 10\%$ | RISE TIME(2)<br>ns max. |
|------------|--------------------------------|-------------------------------|-------------------------|
| AD14SLD050 | 50                             | 5 $\pm 2$                     | 4                       |
| AD14SLD100 | 100                            | 10 $\pm 2$                    | 4                       |
| AD14SLD150 | 150                            | 15 $\pm 2$                    | 4                       |
| AD14SLD200 | 200                            | 20 $\pm 3$                    | 4                       |
| AD14SLD250 | 250                            | 25 $\pm 3$                    | 4                       |
| AD14SLD300 | 300                            | 30                            | 5                       |
| AD14SLD350 | 350                            | 35                            | 5                       |
| AD14SLD400 | 400                            | 40                            | 5                       |
| AD14SLD450 | 450                            | 45                            | 5                       |
| AD14SLD500 | 500                            | 50                            | 5                       |

(1) Delays. measured at 1.5V level on leading edge only with no loads on taps.

(2) Rise Time measured from 0.75V to 2.4V with no loads.

INCHES .XXX  $\pm 0.010$   
 MILLIMETERS .XX  $\pm .25$