

RD74LVC139B

Dual 2-to-4-line Decoders / Demultiplexers

REJ03D0503-0100 Rev.1.00 Dec. 02, 2004

Description

The RD74LVC139B has two independent two-to-four-line decoders each with a single active low enable input in a 16 pin package. Data on the select inputs cause one of the four normally high outputs to go low. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ $V_{CC} = 3.3$ V, Ta = 25°C)
- High output current $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$

 $\pm 8 \text{ mA } (@V_{CC} = 2.3 \text{ V})$

 $\pm 12 \text{ mA } (@V_{CC} = 2.7 \text{ V})$

 ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)

• Ordering Information

Part Name	Part Name Package Type		Package	Taping Abbreviation	
			Abbreviation	(Quantity)	
RD74LVC139BFPEL	SOP-16 pin (JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)	
RD74LVC139BTELL	TSSOP-16 pin	TTP-16DAV	Т	ELL (2,000 pcs/reel)	

Function Table

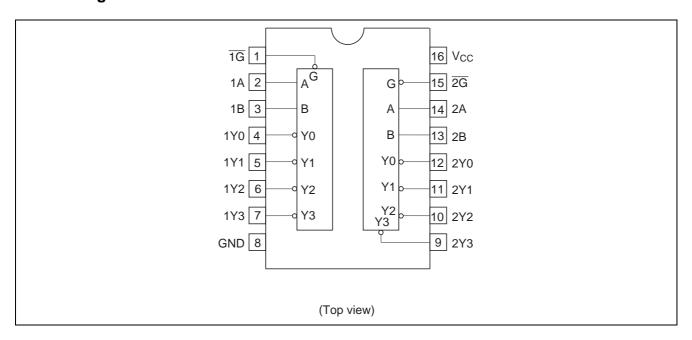
	Input					
Enable	Sel	ect	Outputs			
G	В	А	Y0	Y1	Y2	Y3
Н	Х	Х	Н	Н	Н	Н
L	L	L	L	Н	Н	Н
L	L	Н	Н	L	Н	Н
L	Н	L	Н	Н	L	Н
L	Н	Н	Н	Н	Н	L

H: High levelL: Low level

X: Immaterial

Pin Arrangement

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Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{cc}	-0.5 to 7.0	V	
Input diode current	I _{IK}	-50	mA	V _I = -0.5 V
Input voltage	Vı	-0.5 to 7.0	V	
Output diode current	I _{OK}	-50	mA	V _O = -0.5 V
		50		$V_0 = V_{CC} + 0.5 \text{ V}$
Output voltage	Vo	-0.5 to V _{CC} +0.5	V	
Output current	Io	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to 150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.





Recommended Operating Conditions

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Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	1.5 to 5.5	V	Data retention
		1.65 to 5.5		At operation
Input / output voltage	Vı	0 to 5.5	V	G, A, B
	Vo	0 to V _{CC}	V	Y0 to Y3
Operating temperature	Та	-40 to 85	°C	
Output current	Іон	-4	mA	V _{CC} = 1.65 V
		-8		V _{CC} = 2.3 V
		-12		V _{CC} = 2.7 V
		-24		V _{CC} = 3.0 V to 5.5 V
	I _{OL}	4	mA	V _{CC} = 1.65 V
		8		V _{CC} = 2.3 V
		12		V _{CC} = 2.7 V
		24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
Input rise / fall time *1	t _r , t _f	20	ns/V	V _{CC} = 1.65 V to 2.7 V
		10		V _{CC} = 3.0 V to 5.5 V

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



Electrical Characteristics

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			Ta = -4	0 to 85°C		
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.65	_	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V _{CC} ×0.7	_		
	V _{IL}	1.65 to 1.95	_	V _{CC} ×0.35	V	
		2.3 to 2.7	_	0.7		
		2.7 to 3.6	_	0.8		
		4.5 to 5.5	_	V _{CC} ×0.3		
Output voltage	V _{OH}	165 to 5.5	V _{CC} -0.2	_	V	I _{OH} = -100 μA
		1.65	1.2	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.7	_		$I_{OH} = -8 \text{ mA}$
		2.7	2.2	_		$I_{OH} = -12 \text{ mA}$
		3.0	2.4	_		
		3.0	2.2	_		$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_		
	V_{OL}	165 to 5.5	_	0.2	V	I _{OL} = 100 μA
		1.65	_	0.45		I _{OL} = 4 mA
		2.3		0.7		I _{OL} = 8 mA
		2.7		0.4		I _{OL} = 12 mA
		3.0	_	0.55		I _{OL} = 24 mA
		4.5		0.55		
Input current	I _{IN}	0 to 5.5	_	±5.0	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply	Icc	2.7 to 3.6		±5.0	μΑ	V _{IN} = 3.6 V to 5.5 V
current		2.7 to 5.5		5.0		V _{IN} = V _{CC} or GND
	ΔI_{CC}	2.7 to 3.6	_	500	μΑ	V_{IN} = one input at $(V_{CC} - 0.6)V$, other inputs at V_{CC} or GND





Switching Characteristics

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			Ta	a = -40 to	85°C		From	То
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	1.8±0.15	1.0	_	20.6	ns	A, B	Y0 to Y3
	t _{PHL}	2.5±0.2	1.0	_	9.3			
		2.7	1.0	_	7.3			
		3.3±0.3	1.0	_	6.2			
		5.0±0.5	1.0	_	5.5			
	t _{PLH}	1.8±0.15	1.0	_	19.5	ns	G	Y0 to Y3
	t _{PHL}	2.5±0.2	1.0	_	7.2			
		2.7	1.0	_	5.2			
		3.3±0.3	1.0	_	4.7			
		5.0±0.5	1.0	_	4.5			
Output skew between	t _{OSLH}	1.8±0.15	_	_	_	ns		
pins*1	t _{OSHL}	2.5±0.2	_	_	_			
		2.7	_	_	_			
		3.3±0.3	_	_	1.0	7		
		5.0±0.5			1.0			

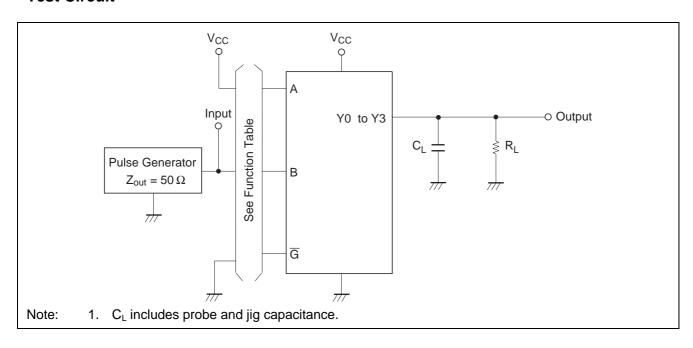
Note: 1. This parameter is characterized but not tested.

 $t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|$

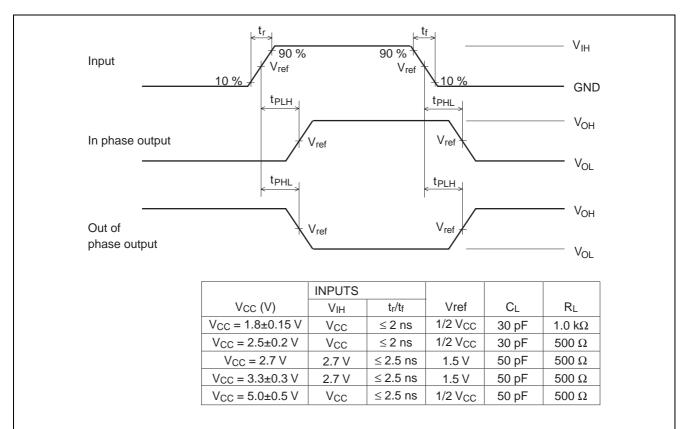
Operating Characteristics

			Ta = 25°C				
Item	Symbol	VCC = (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C _{PD}	1.8	_	28	_	pF	f = 10 MHz
		2.5	_	29	_		
		3.3	_	30	_		
		5.0	_	32	_		

Test Circuit

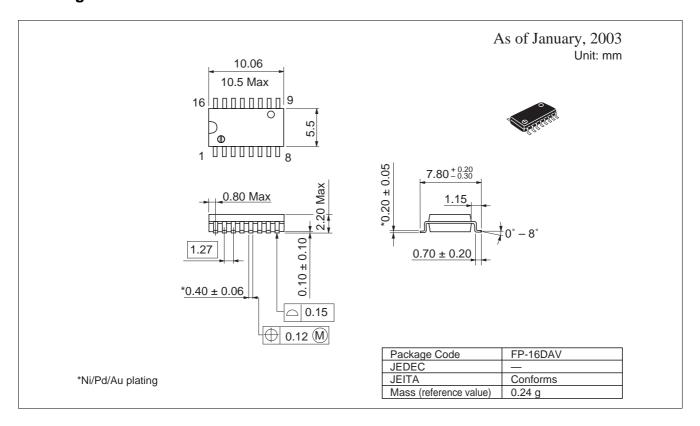


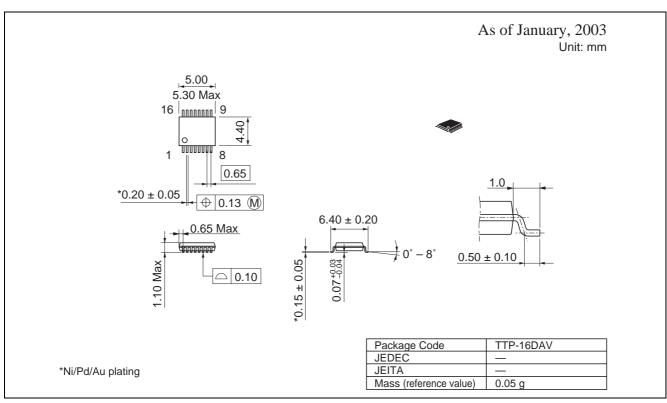
Waveforms www.DataSheet4U.com



Note: 1. Input waveform: PRR = 10 MHz, duty cycle 50%

Package Dimensions





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