SRDA05-4R2

Advance Information

Low Capacitance Surface Mount TVS for High-Speed Data Interfaces

The SRDA05–4 transient voltage suppressor is designed to protect equipment attached to high speed communication lines from ESD, EFT, and lighting.

Features:

- SO-8 Package
- Peak Power 500 Watts 8 x 20 μS
- ESD Rating:

IEC 61000-4-2 (ESD) 15 kV (air) 8 kV (contact)

IEC 61000-4-4 (EFT) 40 A (5/50 ns)

IEC 61000-4-5 (lighting) 23 (8/20 μs)

• UL Flammability Rating of 94V-0

Typical Applications:

• High Speed Communication Line Protection

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation 8 x 20 μS @ T _A = 25°C (Note 1)	P_{pk}	500	W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C
Lead Solder Temperature – Maximum 10 Seconds Duration	TL	260	°C

1. Non-repetitive current pulse 8 x 20 μ S exponential decay waveform

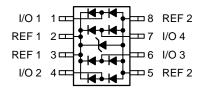
This document contains information on a new product. Specifications and information herein are subject to change without notice.



http://onsemi.com

SO-8 LOW CAPACITANCE VOLTAGE SUPPRESSOR 500 WATTS PEAK POWER 6 VOLTS

PIN CONFIGURATION AND SCHEMATIC





CASE 751 PLASTIC

MARKING DIAGRAM



SRDA5= Device Code L = Location Code

Y = Year WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
SRDA05-4R2	SO-8	2500/Tape & Reel

SRDA05-4R2

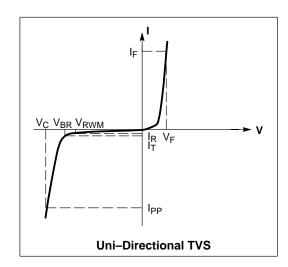
ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage @ I _t = 1.0 mA	V _{BR}	6.0	-	_	V
Reverse Leakage Current @ V _{RWN} = 5.0 Volts	I _R	N/A	_	10	μΑ
Maximum Clamping Voltage @ I _{PP} = 1.0 A, 8 x 20 μS	V _C	N/A	_	9.8	V
Maximum Clamping Voltage @ I _{PP} = 10 A, 8 x 20 μS	V _C	N/A	_	12	V
Between I/O Pins and Ground @ V _R = 0 Volts, 1.0 MHz	Capacitance	-	_	15	pF
Between I/O Pins @ V _R = 0 Volts, 1.0 MHz	Capacitance	ı	-	_	pF

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted) **UNIDIRECTIONAL** (Circuit tied to Pins 1 and 3 or 2 and 3)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V_{BR}	Breakdown Voltage @ I _T
Ι _Τ	Test Current
ΘV _{BR}	Maximum Temperature Coefficient of V _{BR}
l _F	Forward Current
V _F	Forward Voltage @ I _F
Z _{ZT}	Maximum Zener Impedance @ I _{ZT}
I _{ZK}	Reverse Current
Z _{ZK}	Maximum Zener Impedance @ I _{ZK}



SRDA05-4R2

TYPICAL CHARACTERISTICS

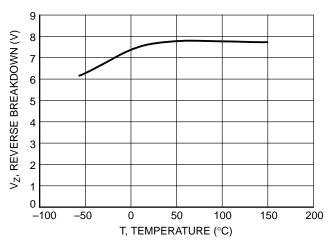


Figure 1. Reverse Breakdown versus Temperature

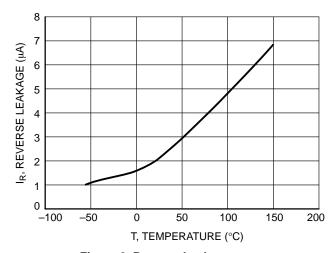


Figure 2. Reverse Leakage versus Temperature

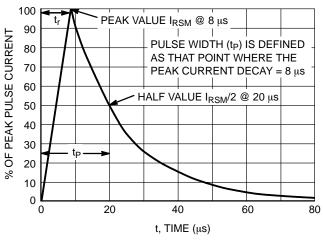


Figure 3. $8 \times 20 \mu s$ Pulse Waveform

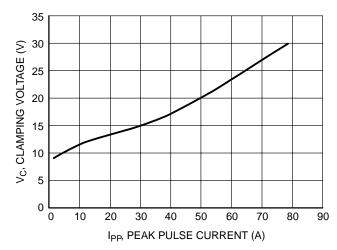
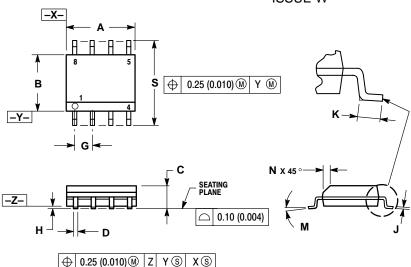


Figure 4. Clamping Voltage versus Peak Pulse Current

Transient Voltage Suppressor – Surface Mount

500 Watts Peak Power

SO-8 CASE 751-07 ISSUE W



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
- 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSION A AND B DO NOT INCLUDE MOLD
- PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
- 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

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