

2SJ234 (L) , 2SJ234 (S)

Silicon P Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device - - - can be driven from 5 V source
- Suitable for DC – DC convertor, motor drive, power switch, solenoid drive

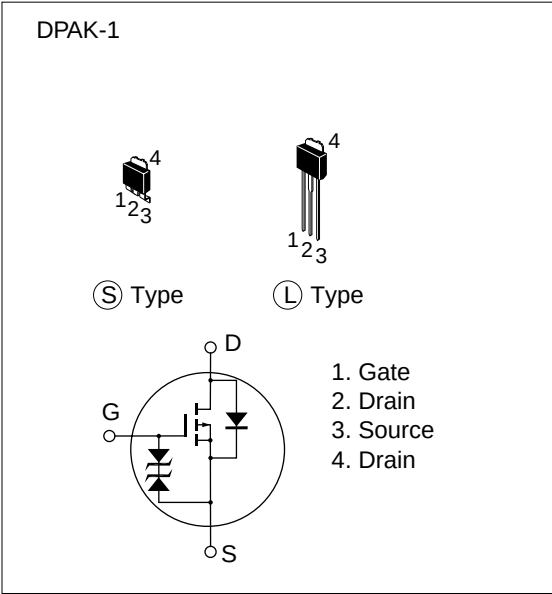


Table 1 Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	−30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	−2.5	A
Drain peak current	$I_{D(pulse)}^*$	−10	A
Body–drain diode reverse drain current	I_{DR}	−2.5	A
Channel dissipation	P_{ch}^{**}	10	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

* $PW \leq 10 \mu s$, duty cycle $\leq 1 \%$

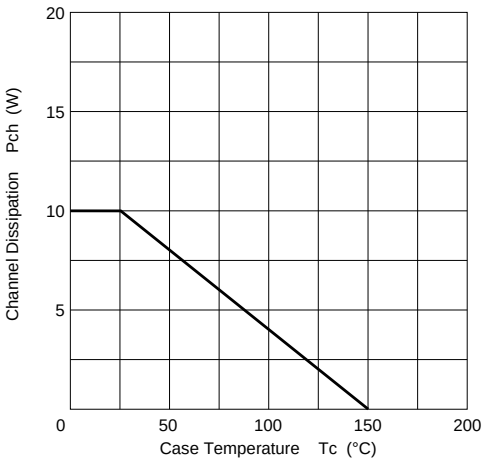
** Value at $T_c = 25 \text{ }^\circ\text{C}$

Table 2 Electrical Characteristics (Ta = 25°C)

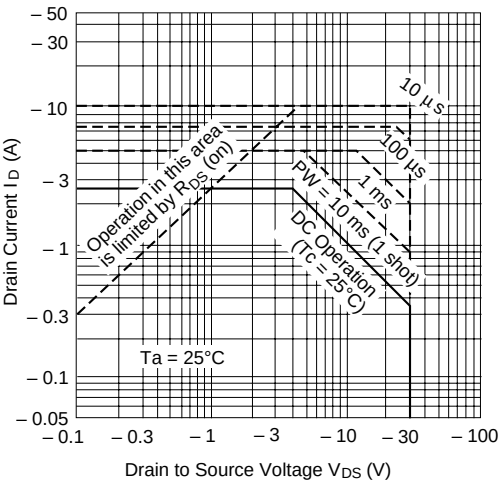
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	—	—	V	$I_D = -10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	-100	μA	$V_{DS} = -25 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	—	-2.0	V	$I_D = -1 \text{ mA}$ $V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.3	0.4	Ω	$I_D = -1.5 \text{ A}$ $V_{GS} = -10 \text{ V}^*$
		—	0.5	0.7		$I_D = -1.5 \text{ A}$ $V_{GS} = -4 \text{ V}^*$
Forward transfer admittance	$ y_{fs} $	1.0	1.8	—	S	$I_D = -1.5 \text{ A}$ $V_{DS} = -10 \text{ V}^*$
Input capacitance	C_{iss}	—	245	—	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	C_{oss}	—	170	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	60	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	7	—	ns	$I_D = -1.5 \text{ A}$
Rise time	t_r	—	25	—	ns	$V_{GS} = -10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	85	—	ns	$R_L = 20 \text{ }\Omega$
Fall time	t_f	—	72	—	ns	
Body-drain diode forward voltage	V_{DF}	—	-1.1	—	V	$I_F = -2.5 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	80	—	ns	$I_F = -2.5 \text{ A}$, $V_{GS} = 0$, $di_F / dt = 50 \text{ A} / \mu\text{s}$

* Pulse Test

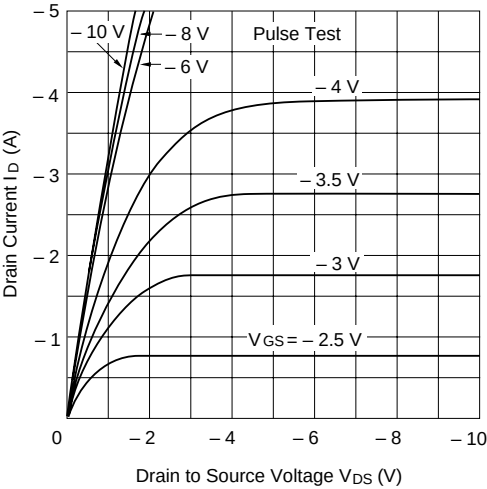
Power vs. Temperature Derating



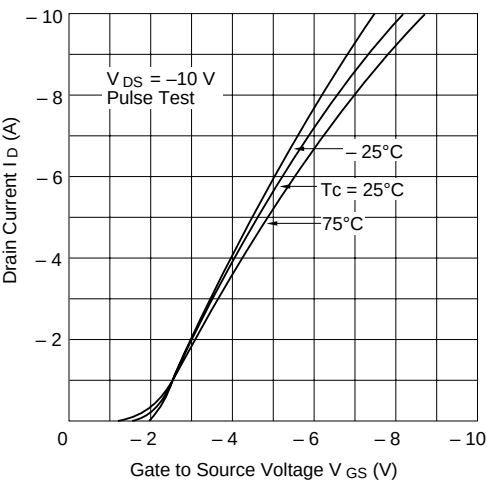
Maximum Safe Operation Area



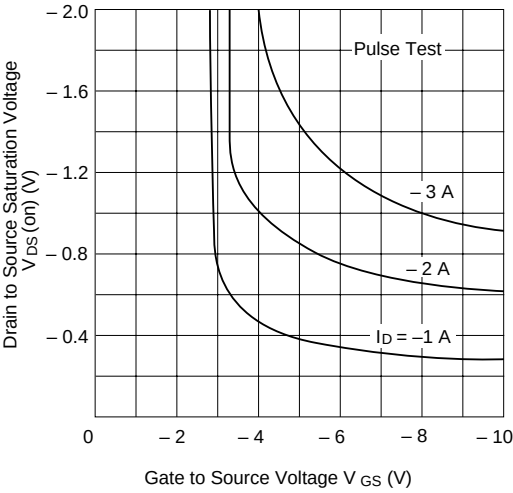
Typical Output Characteristics



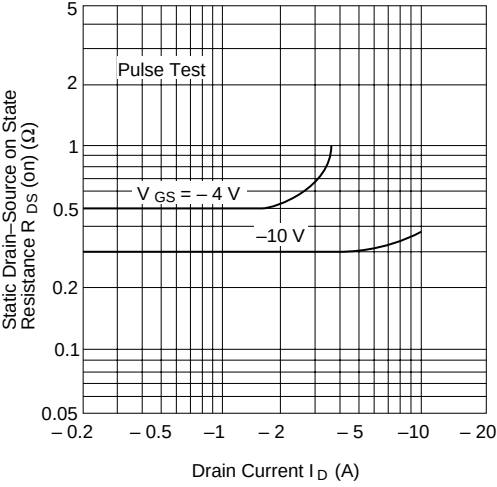
Typical Transfer Characteristics



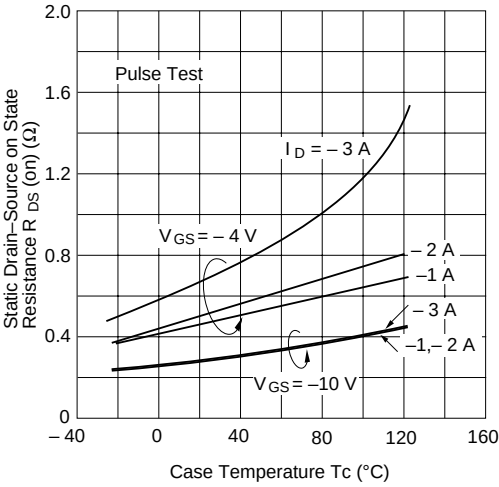
Drain to Source Saturation Voltage
vs. Gate to Source Voltage



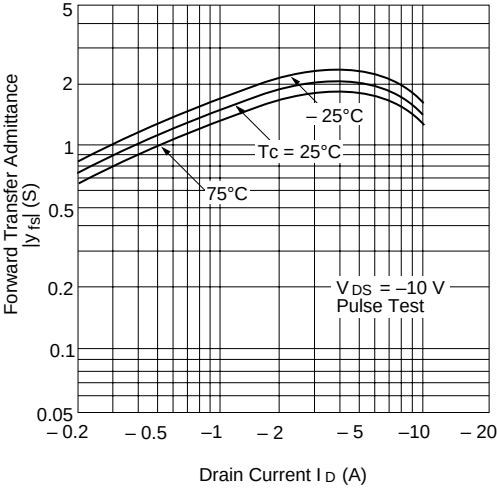
Static Drain to Source on State Resistance
vs. Drain Current

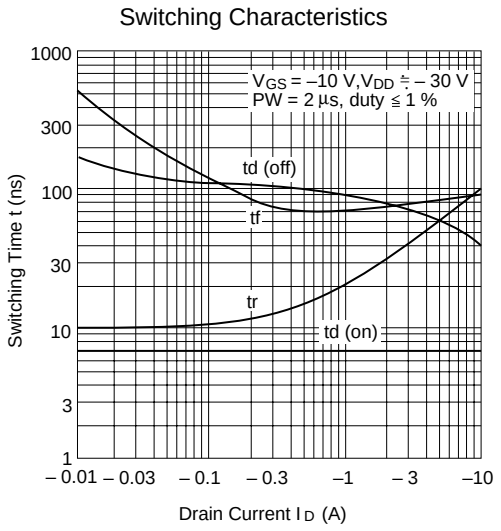
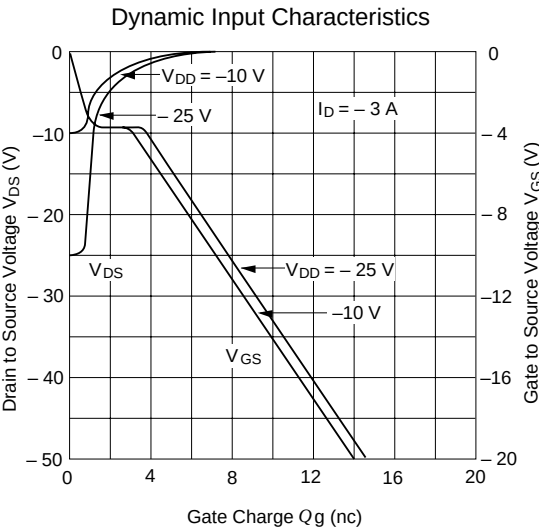
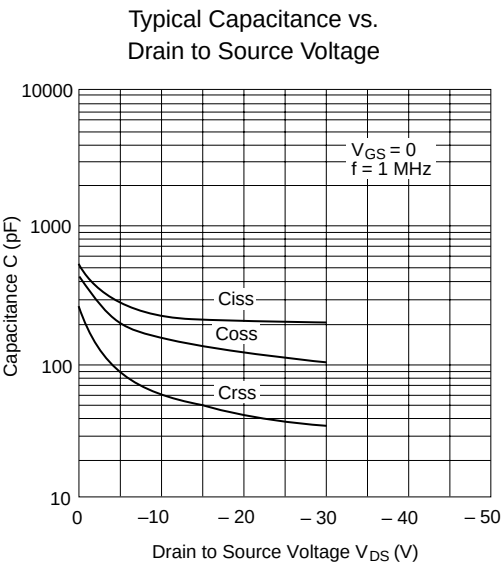
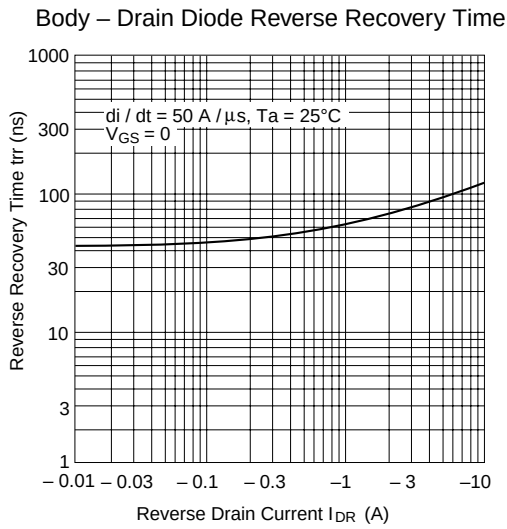


Static Drain to Source on State
Resistance vs. Temperature

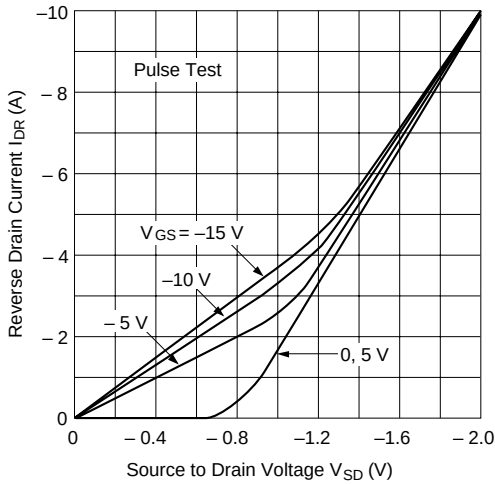


Forward Transfer Admittance vs.
Drain Current





Reverse Drain Current vs.
Source to Drain Voltage



Normalized Transient Thermal Impedance vs. Pulse Width

