TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ($L^2-\pi$ -MOSV)

2SK2507

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4 V gate drive

• Low drain-source ON resistance : $RDS(ON) = 0.034 \Omega \text{ (typ.)}$

• High forward transfer admittance : $|Y_{fs}| = 16 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 50 \text{ V)}$

• Enhancement-mode : $V_{th} = 0.8 \sim 2.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	50	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	50	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	25	А	
	Pulse (Note 1)	I_{DP}	75		
Drain power dissipatio	n (Tc = 25°C)	P_{D}	30	W	
Single pulse avalanche energy (Note 2)		E _{AS}	138	mJ	
Avalanche current		I _{AR}	25	Α	
Repetitive avalanche	energy (Note 3)	E _{AR}	3	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Weight: 1.9 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	4.17	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 272 μ H, R_{G} = 25 Ω , I_{AR} = 25 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device.

Please handle with caution.

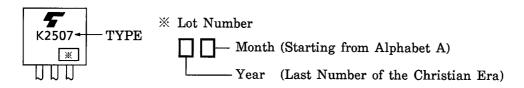
Electrical Characteristics (Ta = 25°C)

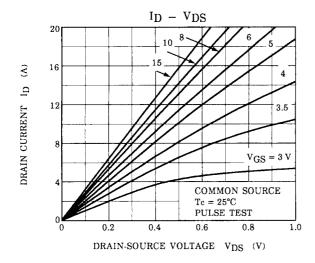
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rrent	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	50	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	8.0	_	2.0	V
Drain-source ON resistance		Pro (out)	V _{GS} = 4 V, I _D = 6 A	_	0.058	0.08	Ω
		R _{DS (ON)}	V _{GS} = 10 V, I _D = 12 A	-	0.034	0.046	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 12 A	8.0	16	_	S
Input capacitano	e	C _{iss}		_	900	_	
Reverse transfer capacitance		C _{rss}	Coss	_	130	_	pF
Output capacitance		Coss		_	370	_	
Switching time	Rise time	t _r	V_{GS} V_{OU}	_	15	_	
	Turn-on time	t _{on}		I	25		ns
	Fall time	t _f		I	30		115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \ \mu s$		110	_	
Total gate charge (Gate-source plus gate-drain)		Qg	V _{DD} ≈ 40 V, V _{GS} = 10 V, I _D = 25 A		25		nC
Gate-source charge		Q _{gs}			19		
Gate-drain ("miller") charge		Q _{gd}		_	6	_	

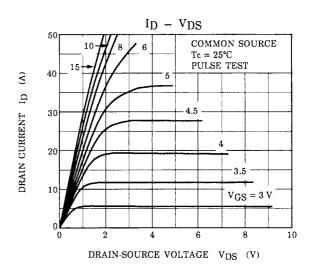
Source-Drain Ratings and Characteristics (Ta = 25°C)

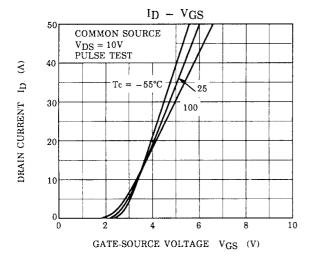
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}		_	_	25	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	75	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 25 A, V _{GS} = 0 V	_	_	-1.6	V
Reverse recovery time	t _{rr}	I_{DR} = 25 A, V_{GS} = 0 V, dI_{DR} / dt = 50 A / μ s	_	60	_	ns
Reverse recovery charge	Q_{rr}		_	45	_	μC

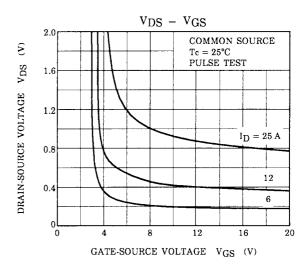
Marking

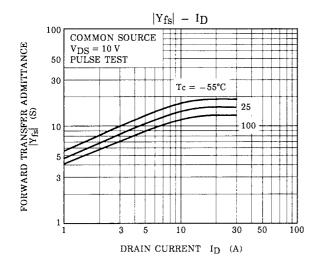


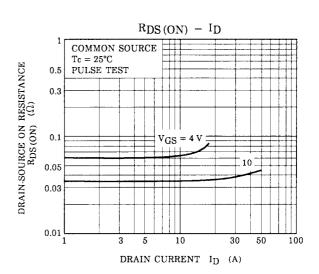




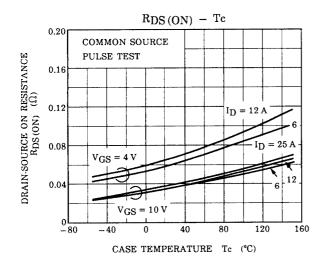


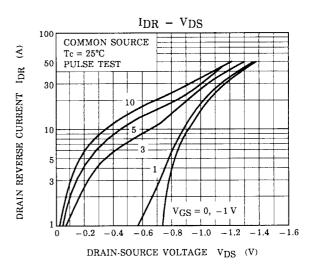


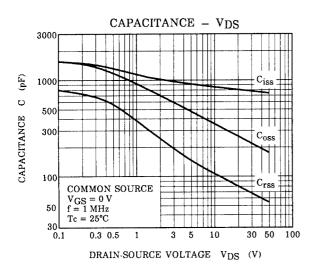


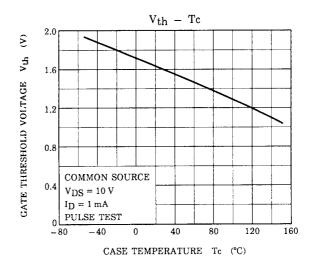


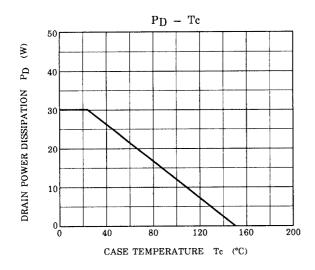
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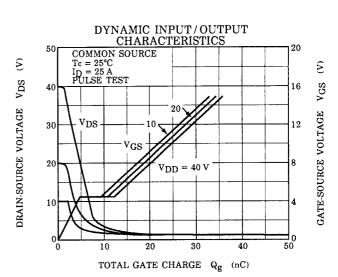




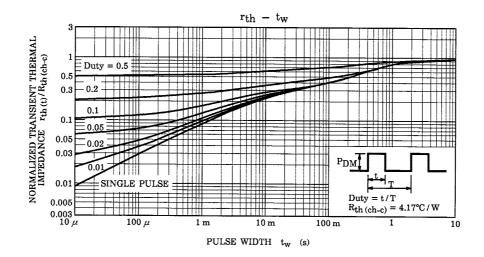


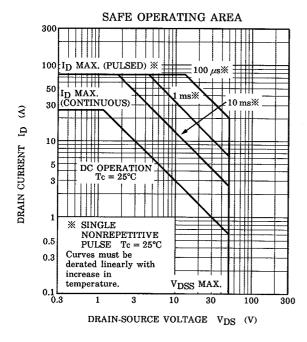


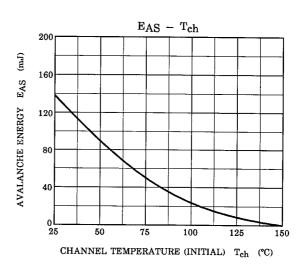


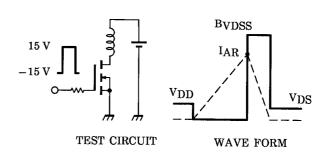


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 272~\mu H \end{aligned} \qquad EAS &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

RESTRICTIONS ON PRODUCT USE

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