# 16 bit analog-to-digital converter

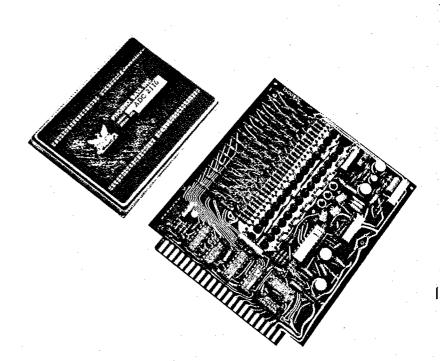
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T-51-10-16

## **FEATURES**

- ☐ RESOLUTION
  - 16 Bits; 1 part in 65,535
- ☐ EXTREMELY LOW POWER
  - 500 mw total
- ☐ HIGH ACCURACY
  - ±.004%
- □ VERY LINEAR
  - ±0.002%
- ☐ HIGH STABILITY
  - ±1.5 ppm/°C Temperature Coefficient
- ☐ FULLY MONOTONIC
- □ OUTPUT CODE
  - Offset Binary



## **GENERAL DESCRIPTION**

Phoenix Data's Model ADC 2000/2100 Series Analog to Digital Converters are specially designed high performance units for stringent applications especially where power requirements are very critical. Performance characteristics include a resolution of one part in 65,535, at an accuracy of  $\pm 0.004\%$ , and an overall stability of  $\pm 1.5$  ppm/°C. Its unique design utilizes the succesive approximation technique and CMOS circuitry, with the total power consumption at an incredibly low 500mw!

The high impedence analog input circuit accepts a full-range  $\pm$  8.192V signal with optimum system isolation.

All output and control signals are fully CMOS-

TTL - LS compatible, with a variety of control signal functions to meet the needs of virtually any system design application.

Each ADC 2000/2100 Series Converter is a fully assembled, tested, and calibrated "System Ready" module, designed for convenient mounting into a standard 44-pin card edge connector.

Simplicity of design, ease of implementation and time-proven reliability has been the trademark of Phoenix Data converters. This coupled with an extensive knowledge of converter technology and wide range of application experience, has made Phoenix Data Converters the choice of discriminating designers everywhere.



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## **ADC 2000/2100 SERIES**

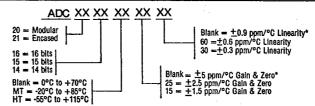
Low Power

# analog-to-digital converter

## **SPECIFICATIONS**

☐ ANALOG INPUT SIGNAL Range Impedence Overvoltage Blas Current	±8.192V, +8.192V, -8.192V, ±4.096V (Programable) 100 Megohm with buffer amplifier, Single ended ±15V max 30 na, typical
<ul> <li>☐ ACCURACY (% of Full Scale Range)</li> <li>Gain &amp; Zero</li> <li>Linearity</li> </ul>	±0.004% FSR (adjustable to Zero) ±0.002% FSR (15 & 16 Bits), monotonic ±0.004% FSR (14 Bits), monotonic
TEMPERATURE COEFFICIENT     Gain & Zero     Linearity	±5 ppm/° C (±2.5 & ±1.5 ppm/°C optional) ±0.9 ppm/°C (±0.6 and ±0.3 ppm/°C optional)
☐ ENCODING ADC 2016/ ADC 2116 ADC 2015/ADC 2115 ADC 2014/ADC 2114	16 bits; 80 usec conversion time 15 bits; 50 usec conversion time 14 bits; 40 usec conversion time
□ DIGITAL INPUTS CTC Output Disable Levels	Leading edge of Positive going pulse 4 lines for tri-state Control CMOS (5 to 12V) or TTL depending on Logic Power supplied
□ DIGITAL OUTPUTS (High True)  Data Drive Tri-State Levels Parallel Data Serial Busy	14, 15 or 16 bits; offset binary code C-MOS or 1 ea TTL-LS load May be used as 4-4 bit bytes; 2-8 bit bytes or 1-16 bit byte 0 & 5V up to 0 & 12V depending on logic power supplied to ADC Latched until following EOC MSB first Low output during conversion & goes true when data has settled on outputs.
☐ ENVIRONMENTAL Temperature Operating Storage Humidity	0°C to +70°C -55°C to +100°C 0 to 99% Relative, Non Condensing
☐ POWER REQUIREMENTS +12V to +15V at 20 ma max -12V to -15V at 15 ma max +5V for TTL or +12V for CMOS logic at 1 ma max	
☐ PACKAGE SIZE ADC 2000 Series ADC 2100 Series	5" x 4.5" x 0.5" Modular P.C. Card 3" x 4" x 0.4 Encased

**Ordering Information** 



\*These TC's Cover the 0°C to +70°C Range; beyond this range the TC doubles.



Phoenix Data, Inc. reserves the right, at any time and without notice, to change specifications presented within this data sheet, and assumes no responsibility for the application or use of products herein described.

For additional information or for your evaluation sample, contact your local Phoenix Data representative or the factory; also please check our short form catalog as advertised in EEM.

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