DPS84H08

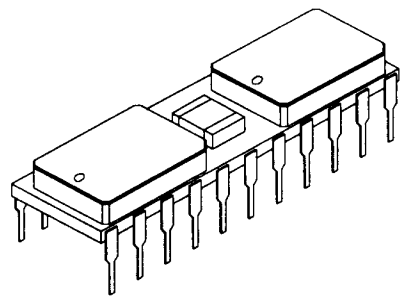
8K X 4 CMOS SRAM MODULE

NOT RECOMENDED FOR NEW DESIGNS

DESCRIPTION:

The DPS84H08 is a 8K X 4 CMOS Static Random Access Memory (SRAM) module built with two 4K X 4 SRAMs. The memory utilizes asyncronous circuitry and may be maintained in any state for an indefinite period of time. All pins are TTL compatible, and a single +5V power supply is required.

The DPS84H08 is ideal for use in large computers or signal processors. The DPS84H08 offers the advantages of high speed and low cost.

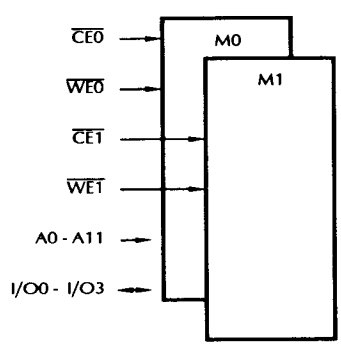


FEATURES:

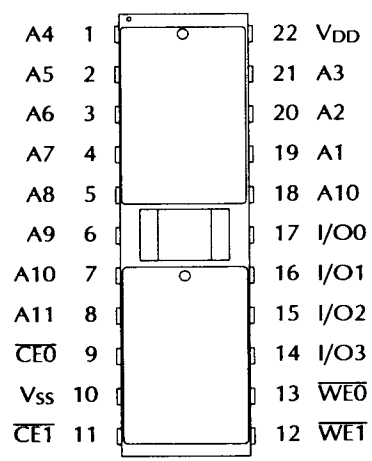
- 4K X 8 Organization
- Access Times: 25, 35, 45ns (max.)
- Fully Static Operation; No Clock or Refresh Required
- TTL-compatible Inputs and Outputs
- Common Data Inputs and Outputs
- Single +5V Operation (± 10% Tolerance)
- Available with All Semiconductor Components used in the Construction of the Module Compliant to MIL-STD-883; Class B
- 22-Pin, Ceramic DIP Package

PIN NAMES	
A0 - A11	Address Inputs
I/O0 - I/O3	Data Inputs/Outputs
$\overline{CE0}$ / $\overline{CE1}$	Chip Enables
$\overline{WE0}$ / $\overline{WE1}$	Write Enables
V_{DD}	Power (+5V)
V_{SS}	Ground

FUNCTIONAL BLOCK DIAGRAM



PIN-OUT DIAGRAM



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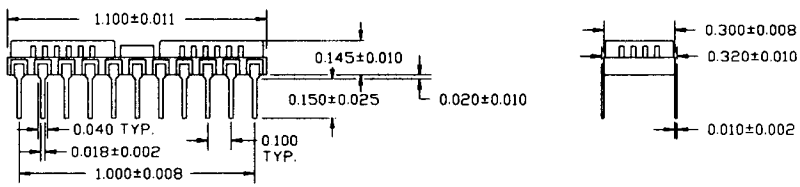
MECHANICAL DRAWING



- CONTACT DENSE-PAC SALES FOR MORE INFORMATION -

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