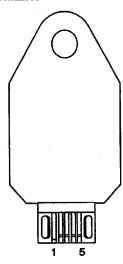
FEATURES

- Greater than 50,000 cycle connector life
- Durable and rugged
- · Ground pin makes first and breaks last
- 3-wire serial interface (DQ, CLK, and RST) simplifies microprocessor interconnect
- Guided entry on mating connector overcomes orientation problems
- Greater than 10 years of data retention with no limitations or restrictions on write cycles
- Low-power CMOS circuitry
- Applications include software authorization, computer identification, system access control, calibration, data storage, automatic system setup, and travelling work record

PIN ASSIGNMENT



See Mech. Drawing - Sect. 16, Pg. 13

PIN DESCRIPTION

- Ground 1
- Clock 2
- 3 Data
- RST
- Vcc

DESCRIPTION

CyberKeys are miniature electronic memories with self-contained lithium energy sources. upon the memory device internal to the CyberKey, secure, non-secure, time-related, and combinations of these functions are available. Interface cost to a microprocessor is minimized by on-chip circuitry which permits data transfers with only three signals: CLK (clock), RST (reset), and DQ (data). Low pin count and a guided entry for a mating receptacle overcome mechanical

problems normally encountered when a conventional integrated circuit package is inserted by the end user. CyberKeys are designed to be rugged and durable enough to withstand normal handling with a life expectancy of over ten years. Small, lightweight construction makes the devices suitable for carrying in a pocket or direct attachment to an object. Figure 1 lists the memory devices utilized in the different CyberKeys. For further information please see the referenced data sheets.

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CYBERKEY DEVICES Figure 1

CYBERKEY	DESCRIPTION	RELATED DATA SHEET
DS6200	64-bit unique serial number with CRC Checking	DS2400
DS6201	1024 bits non-secure static RAM	DS1200
DS6204	128-bit secure static RAM: 64-bit pass- word and 64-bit ID	DS1204
DS6205	3 secure 384-bit subkeys, 512-bit scratchpad	DS1205
DS6207	384-bit secure static RAM: Internal Time Key (1 to 512 days)	DS1207