

**NPN Silicon Transistor** 

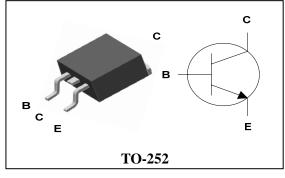
### Description

- Suitable for low voltage large current drivers
- Excellent h<sub>FE</sub> Linearity
- Complementary pair with STB772D
- Switching Application

#### **Features**

 Low collector saturation voltage V<sub>CE(sat)</sub>=0.4V(Max.)

### **PIN Connection**



#### **Ordering Information**

Type NO.	Marking	Package Code	
STD882D	STD882	TO-252	

### Absolute maximum ratings

Absolute maximum ratings	(Ta=25°C)		
Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V <sub>CBO</sub>	40	V
Collector-Emitter voltage	$V_{CEO}$	15	V
Emitter-Base voltage	V <sub>EBO</sub>	7	V
Collector current	Ι <sub>C</sub>	5	A(DC)
	I <sub>CP</sub> *	10	A(Pulse)
Collector Power dissipation (Tc=25 $^{\circ}$ C)	Pc	15	W
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

\*: Single pulse, tp=  $300 \ \mu s$ 

### **Electrical Characteristics**

<b>Electrical Characteristics</b>					(Ta:	=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	$BV_{CBO}$	$I_{C} = 50 \mu A, I_{E} = 0$	40	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_{C}=1mA$ , $I_{B}=0$	15	-	-	V
Emitter-Base breakdown voltage	$BV_{EBO}$	$I_{E} = 50 \mu A, I_{C} = 0$	7	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 30V$ , $I_{E} = 0$	-	-	0.1	μA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=5V$ , $I_{C}=0$	-	-	0.1	μΑ
DC current gain	$h_{FE}^{1}$	$V_{CE} = 2V$ , $I_{C} = 0.5A$	160	-	320	-
	${h_{FE}}^2$	$V_{CE}=2V$ , $I_{C}=2A$	100	-	-	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	$I_{C}=3A$ , $I_{B}=100mA$	-	-	0.4	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ =6V, $I_{E}$ =-50mA	-	150	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}$ =20V, $I_E$ =0, f=1MHz	-	-	50	pF

\* HFE rank :160~320 Only

### **Electrical Characteristic Curves**

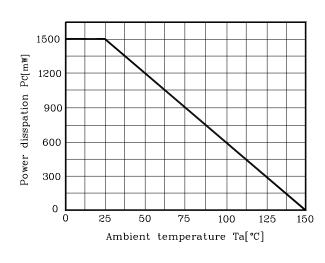
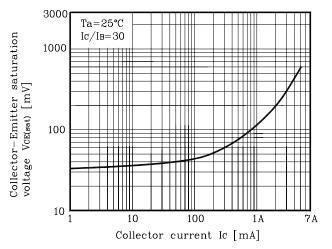


Fig. 1 Pc - Ta

Fig. 3 V<sub>CE(sat)</sub> - I<sub>C</sub>





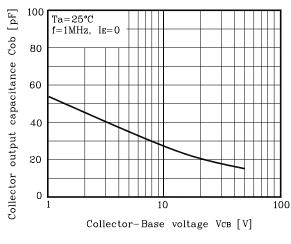


Fig. 2  $h_{\rm FE}$  -  $I_{\rm C}$ 

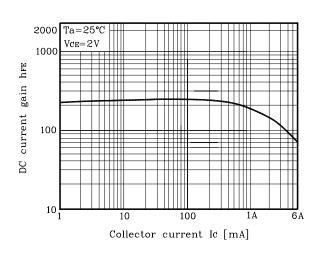
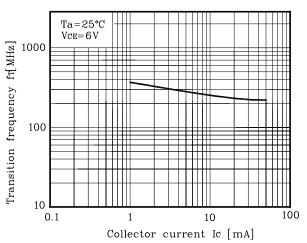
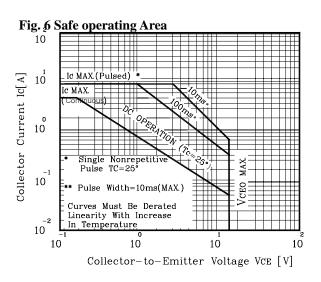
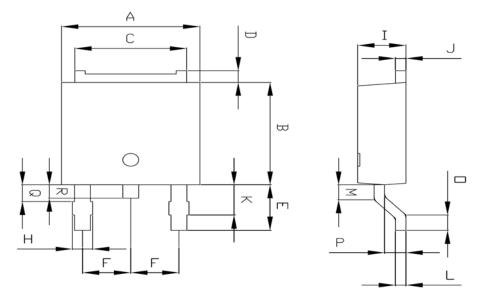


Fig. 4  $f_T$  -  $I_C$ 



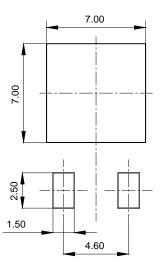


### **Outline Dimension**



	MILLIMETERS			NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	6.40	6.60	6.80	
В	5.90	6.10	6.30	
C	5.04	5.34	5.64	
D	0.50	0.70	0.90	
E	2.50	2.70	2.90	
F	2.10	2.30	2.50	
Н	0.96 MAX			
- I	2.20	2.30	2.40	
J	0.40	0.50	0.60	
K	1.60	1.80	2.00	
L	0.40	0.50	0.60	
М	0.81	0.91	1.01	
0	0.80	0.90	1.00	
Ρ	0.90	1.00	1.10	
Q		0.95 MAX		
R	0.60	0.80	1.00	

#### \*Recommend PCB solder land [Unit: mm]



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