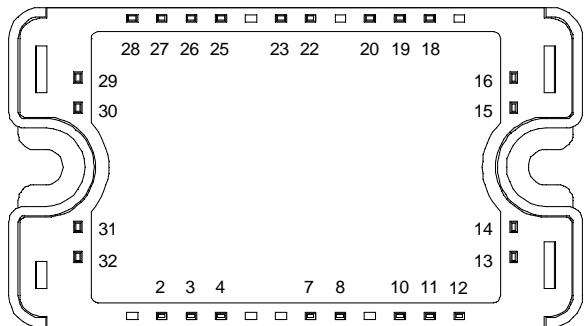
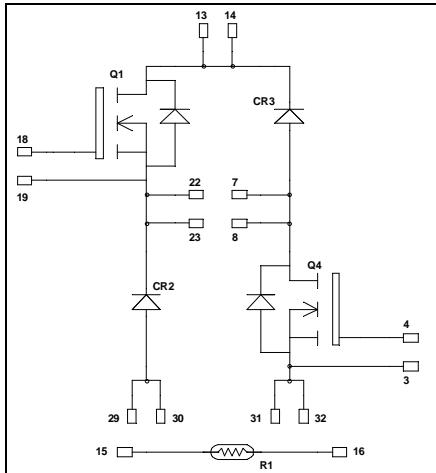


**Asymmetrical - Bridge
MOSFET Power Module**

V_{DSS} = 500V
R_{DSon} = 65mΩ typ @ T_j = 25°C
I_D = 51A @ T_c = 25°C



All multiple inputs and outputs must be shorted together
 Example: 13/14 ; 29/30 ; 22/23...

Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	500	V
I _D	Continuous Drain Current	T _c = 25°C	A
		T _c = 80°C	
I _{DM}	Pulsed Drain current	270	
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	78	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	W
I _{AR}	Avalanche current (repetitive and non repetitive)	42	A

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500V V _{GS} = 0V	T _j = 25°C			250	μA
			T _j = 125°C			1000	
R _{DS(on)}	Drain – Source on Resistance	V _{GS} = 10V, I _D = 42A			65	78	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 2.5mA		3	4	5	V
I _{GSS}	Gate – Source Leakage Current	V _{GS} = ±30 V				±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} = 25V f = 1MHz		10800			pF
C _{oss}	Output Capacitance			1164			
C _{rss}	Reverse Transfer Capacitance			148			
Q _g	Total gate Charge	V _{GS} = 10V V _{Bus} = 250V I _D = 42A		340			nC
Q _{gs}	Gate – Source Charge			75			
Q _{gd}	Gate – Drain Charge			155			
T _{d(on)}	Turn-on Delay Time	Resistive switching @ 25°C V _{GS} = 15V V _{Bus} = 333V I _D = 42A R _G = 2.2Ω		60			ns
T _r	Rise Time			70			
T _{d(off)}	Turn-off Delay Time			155			
T _f	Fall Time			50			

Diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V	
I _{RM}	Maximum Reverse Leakage Current	V _R =600V	T _j = 25°C			250	μA	
			T _j = 125°C			500		
I _F	DC Forward Current			T _c = 80°C	60		A	
V _F	Diode Forward Voltage	I _F = 60A			1.7	2.3	V	
		I _F = 120A			2			
		I _F = 60A	T _j = 125°C		1.4			
t _{rr}	Reverse Recovery Time	I _F = 60A V _R = 400V di/dt = 200A/μs	T _j = 25°C		70		ns	
			T _j = 125°C		140			
Q _{rr}	Reverse Recovery Charge		T _j = 25°C		100		nC	
			T _j = 125°C		690			



Thermal and package characteristics

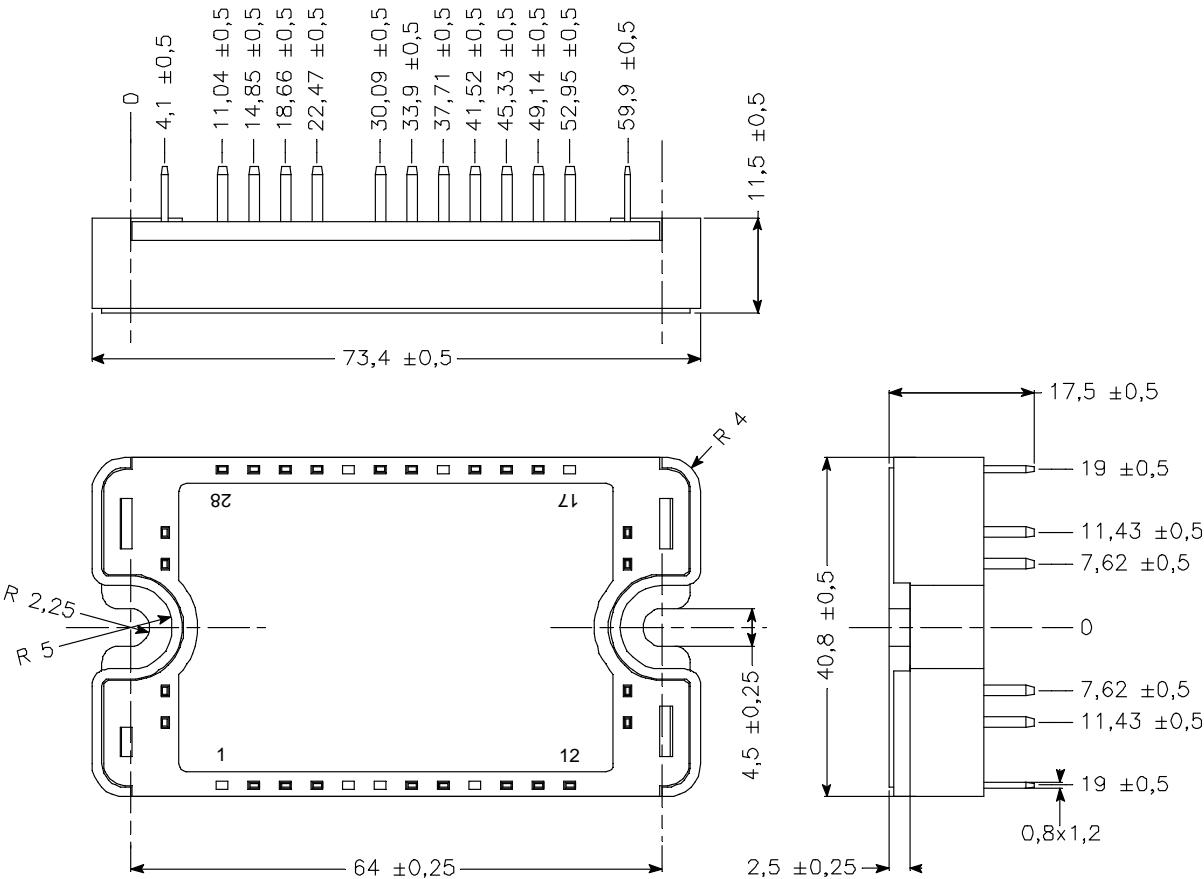
Symbol	Characteristic		Min	Typ	Max	Unit
R _{thJC}	Junction to Case Thermal Resistance	MOSFET			0.32	°C/W
		Diode			0.85	
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, I isol < 1mA, 50/60Hz	4000				V
T _J	Operating junction temperature range	-40		150		
T _{STG}	Storage Temperature Range	-40		125		°C
T _C	Operating Case Temperature	-40		100		
Torque	Mounting torque	To Heatsink	M4	2.5	4.7	N.m
Wt	Package Weight				110	g

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
ΔR ₂₅ /R ₂₅				5		%
B _{25/85}	T ₂₅ = 298.15 K			3952		K
ΔB/B		T _C =100°C		4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]} \quad \begin{array}{l} T: \text{Thermistor temperature} \\ R_T: \text{Thermistor value at } T \end{array}$$

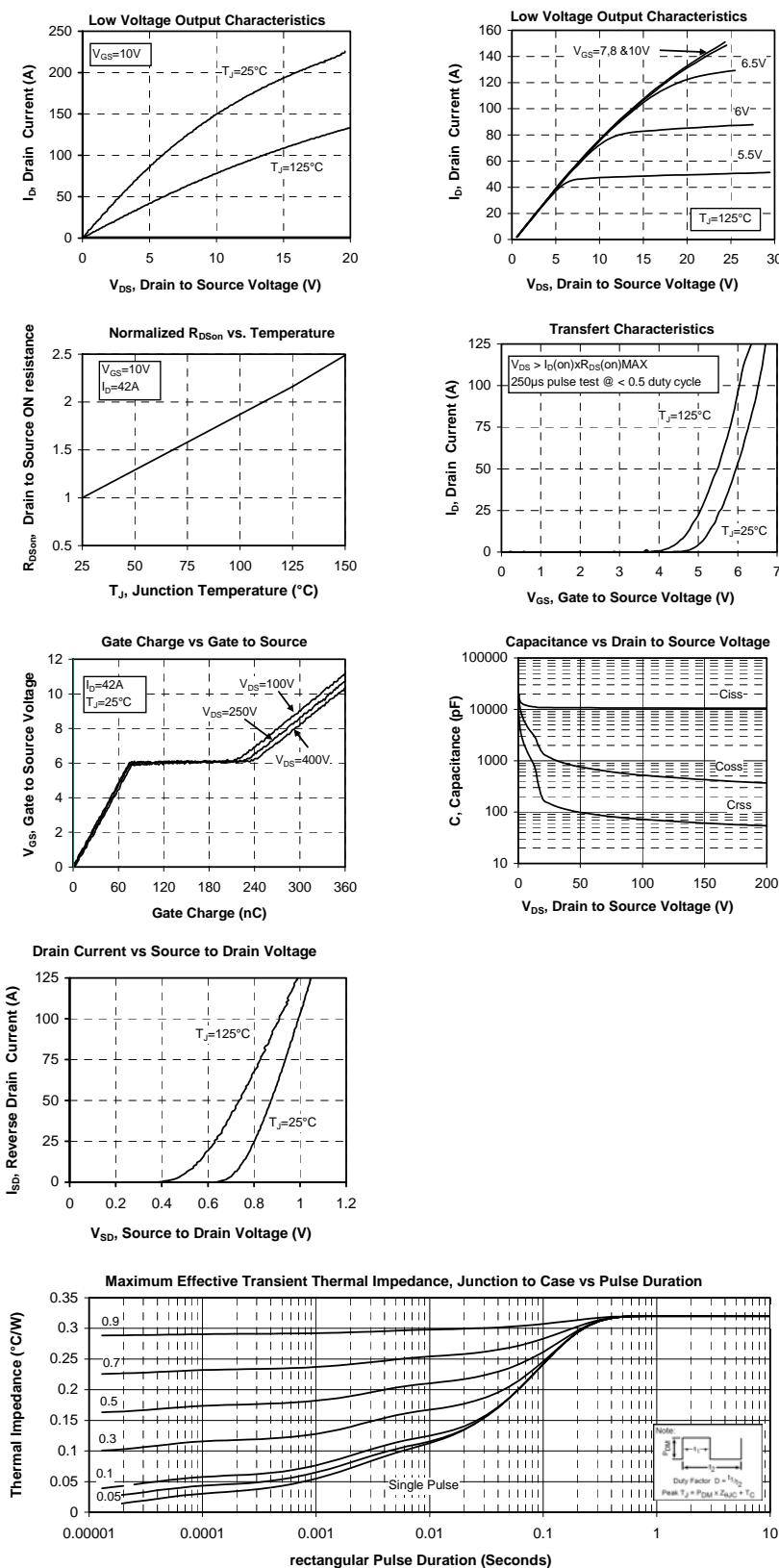
SP3 Package outline (dimensions in mm)



See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

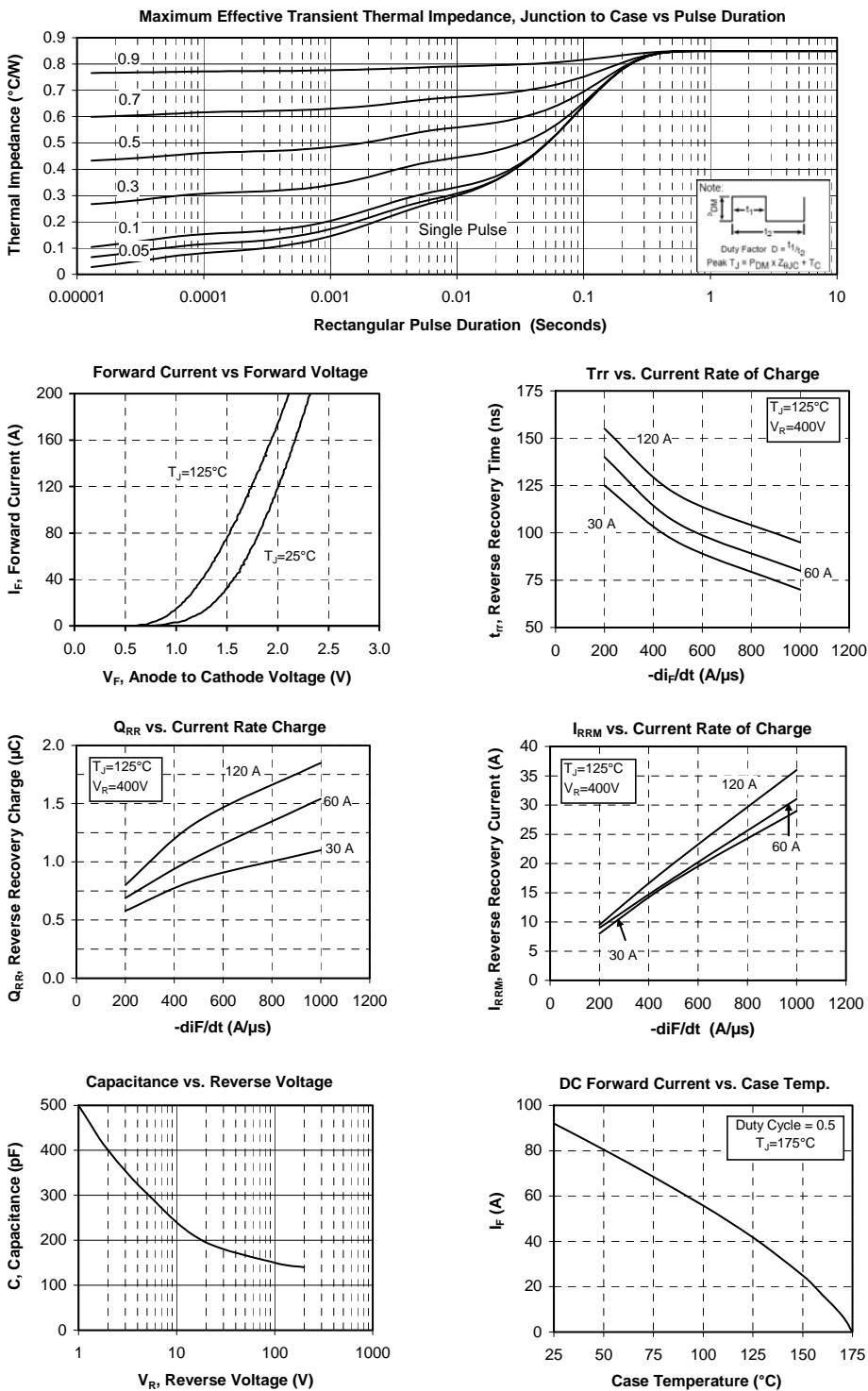


Typical MOSFET Performance Curve





Typical diode Performance Curve



Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 6,939,743 7,352,045 5,283,201 5,801,417 5,648,283 7,196,634 6,664,594 7,157,886 6,939,743 7,342,262 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.