

### FEATURES

- 10μs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Copper Baseplate

### APPLICATIONS

- Inverters
- Motor Controllers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

The DIM400WHS17-A000 is a half bridge switch 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10μs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### ORDERING INFORMATION

Order As:

**DIM400WHS17-A000**

Note: When ordering, please use the whole part number.

### KEY PARAMETERS

|                 |       |       |
|-----------------|-------|-------|
| $V_{CES}$       |       | 1700V |
| $V_{CE(sat)}$ * | (typ) | 2.7V  |
| $I_C$           | (max) | 400A  |
| $I_{C(PK)}$     | (max) | 800A  |

\*(measured at the power busbars and not the auxiliary terminals)

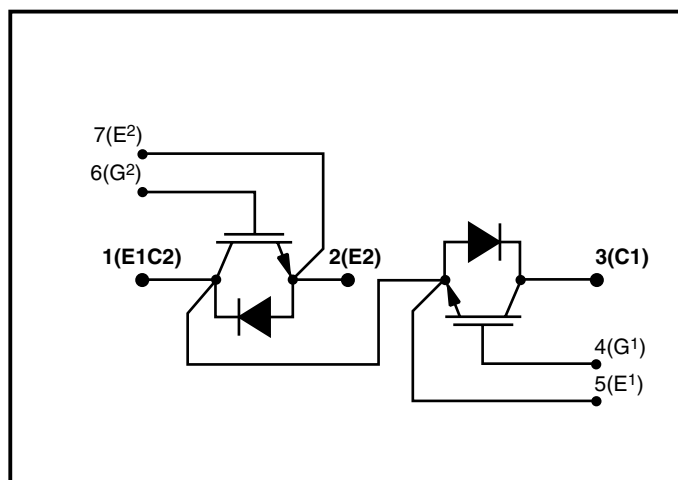


Fig. 1 Half bridge circuit diagram



Fig. 2 Electrical connections - (not to scale)

## ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

| Symbol      | Parameter                         | Test Conditions                                                  | Max.     | Units                 |
|-------------|-----------------------------------|------------------------------------------------------------------|----------|-----------------------|
| $V_{CES}$   | Collector-emitter voltage         | $V_{GE} = 0\text{V}$                                             | 1700     | V                     |
| $V_{GES}$   | Gate-emitter voltage              | -                                                                | $\pm 20$ | V                     |
| $I_C$       | Continuous collector current      | $T_{case} = 65^{\circ}\text{C}$                                  | 400      | A                     |
| $I_{C(PK)}$ | Peak collector current            | 1ms, $T_{case} = 110^{\circ}\text{C}$                            | 800      | A                     |
| $P_{max}$   | Max. transistor power dissipation | $T_{case} = 25^{\circ}\text{C}$ , $T_j = 150^{\circ}\text{C}$    | 2780     | W                     |
| $I^2t$      | Diode $I^2t$ value                | $V_R = 0$ , $t_p = 10\text{ms}$ , $T_{vj} = 125^{\circ}\text{C}$ | 30       | $\text{kA}^2\text{s}$ |
| $V_{isol}$  | Isolation voltage - per module    | Commoned terminals to base plate. AC RMS, 1 min, 50Hz            | 4000     | V                     |
| $Q_{PD}$    | Partial discharge - per module    | IEC1287. $V_1 = 1800\text{V}$ , $V_2 = 1300\text{V}$ , 50Hz RMS  | 10       | PC                    |

## THERMAL AND MECHANICAL RATINGS

Internal insulation:  $\text{Al}_2\text{O}_3$   
 Baseplate material: Cu  
 Creepage distance: 24mm

Clearance: 13mm  
 CTI (Critical Tracking Index): 175

| Symbol        | Parameter                                             | Test Conditions                               | Min. | Typ. | Max. | Units                        |
|---------------|-------------------------------------------------------|-----------------------------------------------|------|------|------|------------------------------|
| $R_{th(j-c)}$ | Thermal resistance - transistor (per arm)             | Continuous dissipation -<br>junction to case  | -    | -    | 45   | $^{\circ}\text{C}/\text{kW}$ |
| $R_{th(j-c)}$ | Thermal resistance - diode (per arm)                  | Continuous dissipation -<br>junction to case  | -    | -    | 100  | $^{\circ}\text{C}/\text{kW}$ |
| $R_{th(c-h)}$ | Thermal resistance - case to heatsink<br>(per module) | Mounting torque 5Nm<br>(with mounting grease) | -    | -    | 15   | $^{\circ}\text{C}/\text{kW}$ |
| $T_j$         | Junction temperature                                  | Transistor                                    | -    | -    | 150  | $^{\circ}\text{C}$           |
|               |                                                       | Diode                                         | -    | -    | 125  | $^{\circ}\text{C}$           |
| $T_{stg}$     | Storage temperature range                             | -                                             | -40  | -    | 125  | $^{\circ}\text{C}$           |
| -             | Screw torque                                          | Mounting - M6                                 | 3    | -    | 5    | Nm                           |
|               |                                                       | Electrical connections - M6                   | 2.5  | -    | 5    | Nm                           |

## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise.

| Symbol                            | Parameter                                | Test Conditions                                                                           |                | Min. | Typ. | Max. | Units |
|-----------------------------------|------------------------------------------|-------------------------------------------------------------------------------------------|----------------|------|------|------|-------|
| I <sub>CES</sub>                  | Collector cut-off current                | V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>                                  |                | -    | -    | 1    | mA    |
|                                   |                                          | V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C      |                | -    | -    | 12   | mA    |
| I <sub>GES</sub>                  | Gate leakage current                     | V <sub>GE</sub> = ±20V, V <sub>CE</sub> = 0V                                              |                | -    | -    | 2    | μA    |
| V <sub>GE(TH)</sub>               | Gate threshold voltage                   | I <sub>C</sub> = 20mA, V <sub>GE</sub> = V <sub>CE</sub>                                  |                | 4.5  | 5.5  | 6.5  | V     |
| V <sub>CE(sat)</sub> <sup>†</sup> | Collector-emitter saturation voltage     | V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A                                              |                | -    | 2.7  | 3.4  | V     |
|                                   |                                          | V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A, , T <sub>case</sub> = 125°C                 |                | -    | 3.4  | 4.0  | V     |
| I <sub>F</sub>                    | Diode forward current                    | DC                                                                                        |                | -    | -    | 400  | A     |
| I <sub>FM</sub>                   | Diode maximum forward current            | t <sub>p</sub> = 1ms                                                                      |                | -    | -    | 800  | A     |
| V <sub>F</sub> <sup>†</sup>       | Diode forward voltage                    | I <sub>F</sub> = 400A                                                                     |                | -    | 2.2  | 2.5  | V     |
|                                   |                                          | I <sub>F</sub> = 400A, T <sub>case</sub> = 125°C                                          |                | -    | 2.3  | 2.6  | V     |
| C <sub>ies</sub>                  | Input capacitance                        | V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz                                     |                | -    | 30   | -    | nF    |
| L <sub>M</sub>                    | Module inductance - per arm              | -                                                                                         |                | -    | 20   | -    | nH    |
| R <sub>INT</sub>                  | Internal transistor resistance - per arm | -                                                                                         |                | -    | 0.23 | -    | mΩ    |
| SC <sub>Data</sub>                | Short circuit. I <sub>SC</sub>           | T <sub>j</sub> = 125°C, V <sub>CC</sub> = 1000V,                                          | I <sub>1</sub> | -    | 1850 | -    | A     |
|                                   |                                          | t <sub>p</sub> ≤ 10μs, V <sub>CE(max)</sub> = V <sub>CES</sub> - L*. di/dt<br>IEC 60747-9 | I <sub>2</sub> | -    | 1600 | -    | A     |

**Note:**

$\dagger$  Measured at the power busbars and not the auxiliary terminals)

\* L is the circuit inductance +  $L_M$

## ELECTRICAL CHARACTERISTICS

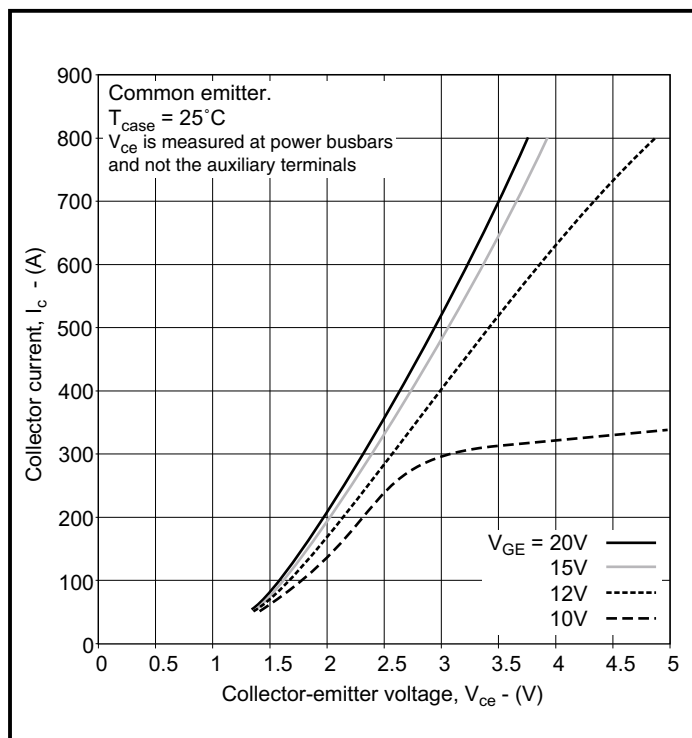
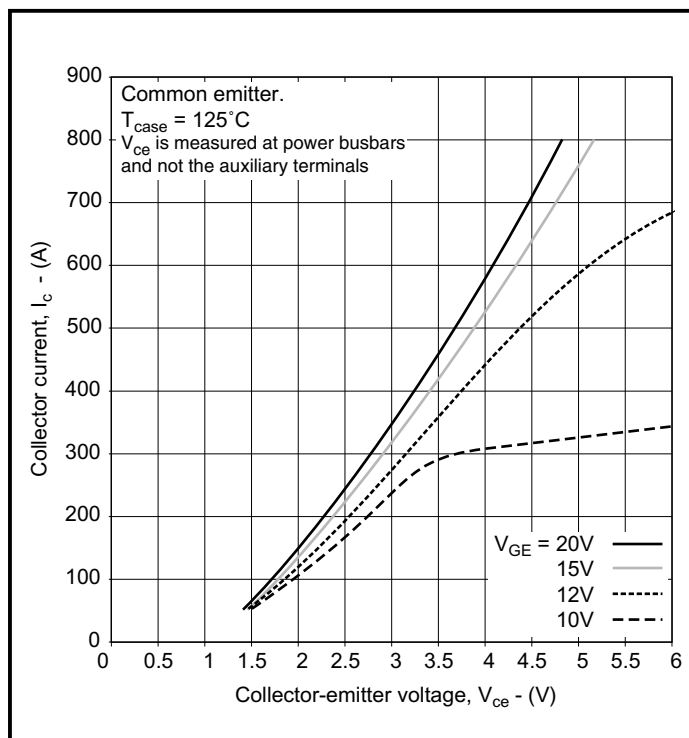
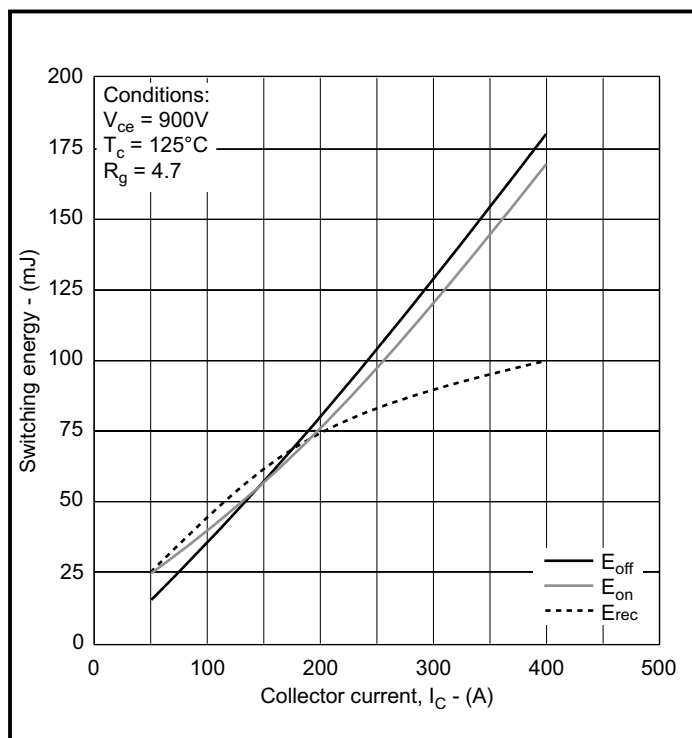
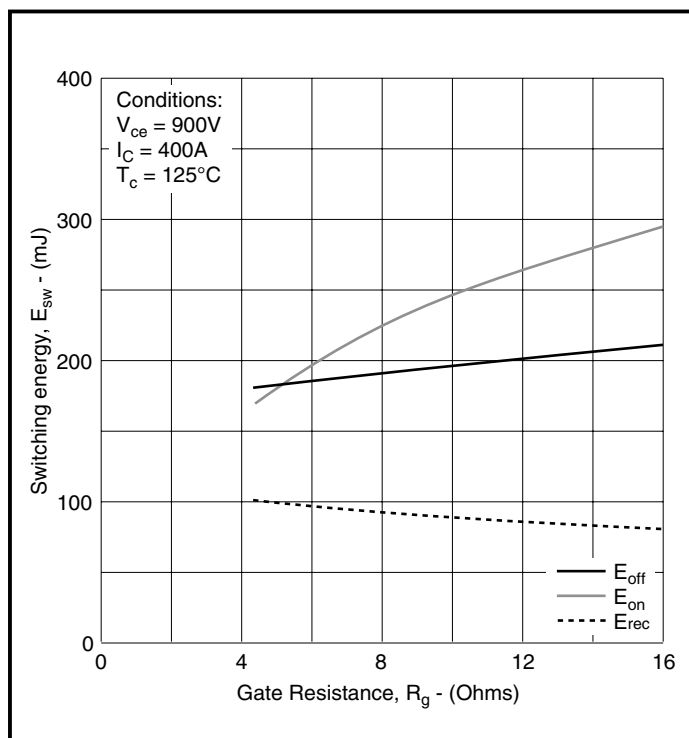
 $T_{\text{case}} = 25^{\circ}\text{C}$  unless stated otherwise

| Symbol              | Parameter                     | Test Conditions                                                                                                                                           | Min. | Typ. | Max. | Units         |
|---------------------|-------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------|------|------|------|---------------|
| $t_{d(\text{off})}$ | Turn-off delay time           | $I_C = 400\text{A}$<br>$V_{GE} = \pm 15\text{V}$<br>$V_{CE} = 900\text{V}$<br>$R_{G(\text{ON})} = R_{G(\text{OFF})} = 4.7\Omega$<br>$L \sim 100\text{nH}$ | -    | 1150 | -    | ns            |
| $t_f$               | Fall time                     |                                                                                                                                                           | -    | 100  | -    | ns            |
| $E_{\text{OFF}}$    | Turn-off energy loss          |                                                                                                                                                           | -    | 120  | -    | mJ            |
| $t_{d(\text{on})}$  | Turn-on delay time            |                                                                                                                                                           | -    | 250  | -    | ns            |
| $t_r$               | Rise time                     |                                                                                                                                                           | -    | 250  | -    | ns            |
| $E_{\text{ON}}$     | Turn-on energy loss           |                                                                                                                                                           | -    | 150  | -    | mJ            |
| $Q_g$               | Gate charge                   |                                                                                                                                                           | -    | 4.5  | -    | $\mu\text{C}$ |
| $Q_{rr}$            | Diode reverse recovery charge | $I_F = 400\text{A}, V_R = 900\text{V},$<br>$dI_F/dt = 3000\text{A}/\mu\text{s}$                                                                           | -    | 100  | -    | $\mu\text{C}$ |
| $I_{rr}$            | Diode reverse current         |                                                                                                                                                           | -    | 230  | -    | A             |
| $E_{\text{REC}}$    | Diode reverse recovery energy |                                                                                                                                                           | -    | 70   | -    | mJ            |

 $T_{\text{case}} = 125^{\circ}\text{C}$  unless stated otherwise

| Symbol              | Parameter                     | Test Conditions                                                                                                                                           | Min. | Typ. | Max. | Units         |
|---------------------|-------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------|------|------|------|---------------|
| $t_{d(\text{off})}$ | Turn-off delay time           | $I_C = 400\text{A}$<br>$V_{GE} = \pm 15\text{V}$<br>$V_{CE} = 900\text{V}$<br>$R_{G(\text{ON})} = R_{G(\text{OFF})} = 4.7\Omega$<br>$L \sim 100\text{nH}$ | -    | 1400 | -    | ns            |
| $t_f$               | Fall time                     |                                                                                                                                                           | -    | 130  | -    | ns            |
| $E_{\text{OFF}}$    | Turn-off energy loss          |                                                                                                                                                           | -    | 180  | -    | mJ            |
| $t_{d(\text{on})}$  | Turn-on delay time            |                                                                                                                                                           | -    | 400  | -    | ns            |
| $t_r$               | Rise time                     |                                                                                                                                                           | -    | 250  | -    | ns            |
| $E_{\text{ON}}$     | Turn-on energy loss           |                                                                                                                                                           | -    | 170  | -    | mJ            |
| $Q_{rr}$            | Diode reverse recovery charge | $I_F = 400\text{A}, V_R = 900\text{V},$<br>$dI_F/dt = 2500\text{A}/\mu\text{s}$                                                                           | -    | 170  | -    | $\mu\text{C}$ |
| $I_{rr}$            | Diode reverse current         |                                                                                                                                                           | -    | 270  | -    | A             |
| $E_{\text{REC}}$    | Diode reverse recovery energy |                                                                                                                                                           | -    | 100  | -    | mJ            |

## TYPICAL CHARACTERISTICS


**Fig. 3 Typical output characteristics**

**Fig. 4 Typical output characteristics**

**Fig. 5 Typical switching energy vs collector current**

**Fig. 6 Typical switching energy vs gate resistance**

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

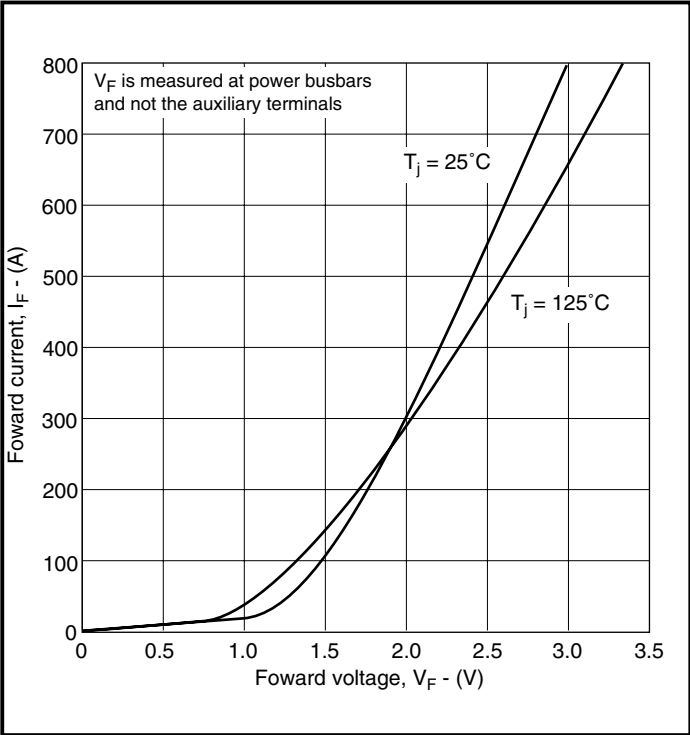


Fig. 7 Diode typical forward characteristics

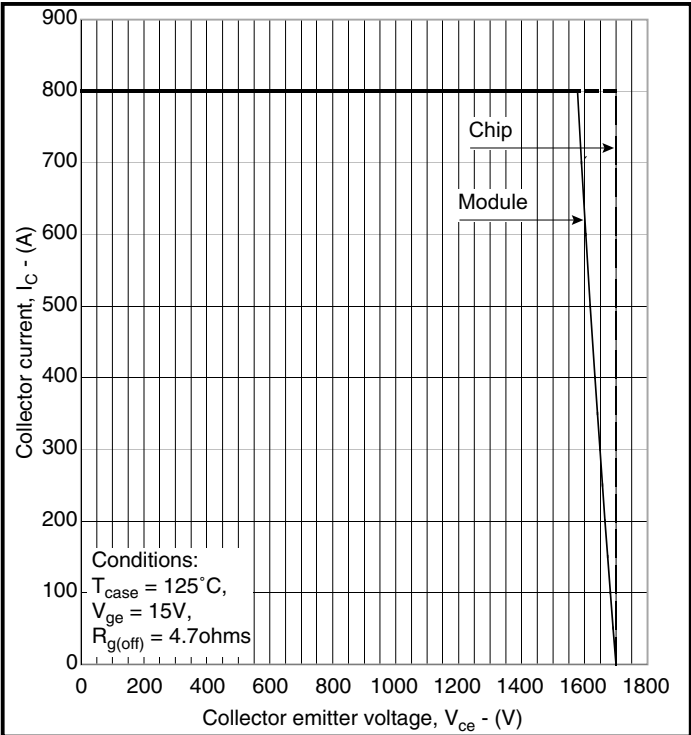


Fig. 8 Reverse bias safe operating area

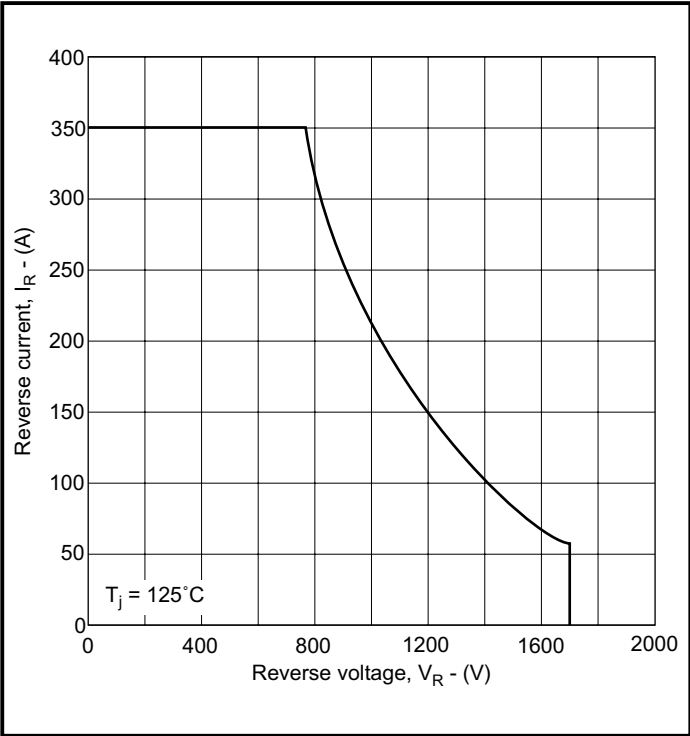


Fig. 9 Diode reverse bias safe operating area

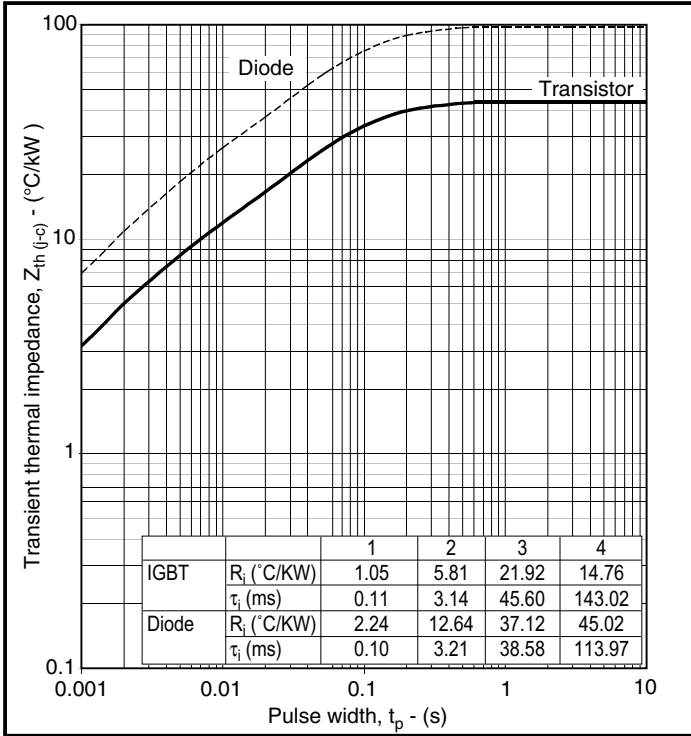
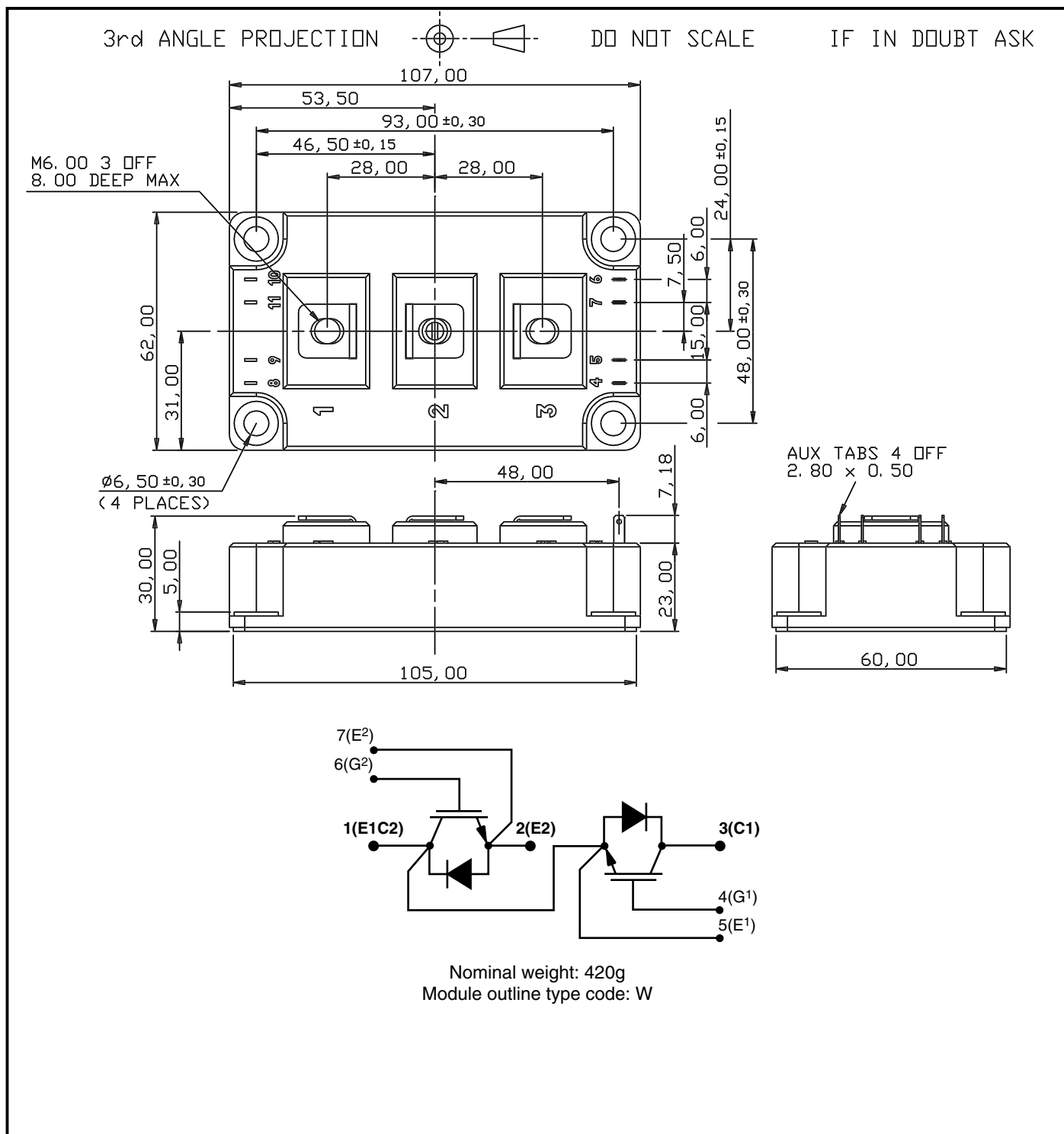


Fig. 10 Transient thermal impedance

## PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



**Fig. 11 Package details**

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

## POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

## HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



<http://www.dynexsemi.com>

**e-mail: [power\\_solutions@dynexsemi.com](mailto:power_solutions@dynexsemi.com)**

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