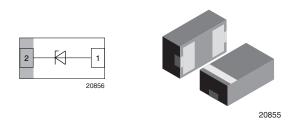
Vishay Semiconductors

Low Capacitance, Single-Line ESD-Protection Diode



MARKING (example only)

Y = type code (see table below)

Bar = cathode marking

X = date code

SHA



FEATURES

- Ultra compact LLP1006-2L package
 Low package height < 0.4 mm
- 1-line ESD-protection
- Low leakage current < 0.1 μA
- Low load capacitance CD = 0.6 pF
- ESD-protection to IEC 61000-4-2 ± 9 kV contact discharge ± 9 kV air discharge



- High surge current acc. IEC61000-4-5 $I_{PP} > 2 A$
- Soldering can be checked by standard vision inspection. No X-ray necessary
- Pin plating NiPdAu (e4) no whisker growth
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATION					
DEVICE NAME ORDERING CODE		TAPED UNITS PER REEL (8 MM TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VBUS051CD-HD1	VBUS051CD-HD1-G-08	8000	8000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS051CD-HD1	LLP1006-2L	Т	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
ARAMETER TEST CONDITIONS		SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5; t _p = 8/20 μs; single shot	I _{PPM}	2	А		
Peak pulse power	Acc. IEC 61000-4-5; t _p = 8/20 μs; single shot	P _{PP}	28	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 9	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 9	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T _{STG}	- 40 to + 150	°C		

** Please see document "Vishay Material Category Policy": <u>www.vishay.com/doc?99902</u>

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of line which can be protected	N _{lines}	-	-	1	lines
Reverse stand-off voltage	at I _R = 1 μA pin 2 to pin 1	V _{RWM}	5.5	-	-	V
Reverse current	at V _R = V _{RWM} = 5.5 V; pin 2 to pin 1	I _R	-	< 0.01	0.1	μΑ
Reverse breakdown voltage	at I _R = 1 mA pin 2 to pin 1	V_{BR}	6.5	7.6	8.5	V
Reverse clamping voltage	at I _{PP} = 2 A; acc. IEC 61000-4-5 pin 2 to pin 1	V _C			14	V
Forward clamping voltage	at I _F = 2 A; acc. IEC 61000-4-5 pin 1 to pin 2	V _F		2.6	3.5	V
Capacitance	at V _R = 0 V; f = 1 MHz pin 2 to pin 1	CD		0.6	0.8	pF

Note

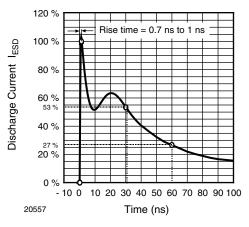
• BiAs mode: each input (pin 1; 3 - pin 6) to ground (pin 2).

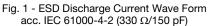
APPLICATION NOTE

The VBUS051CD-HD1 is an ESD-protection device with the characteristic of a Z-diode with a high ESD-immunity and a very low capacitance which makes it usable for high frequency applications like USB2.0 or HDMI.

With the VBUS051CD-HD1 one high speed data line can be protected against transient voltage signals like ESD (Electro Static Discharge). Connected to the data line (pin 2) and to ground (pin 1) negative transients will be clamped close below the ground level while positive transients will be clamped clos above the 5.5 V working range. The clamping behaviour of the VBUS051CD-HD1 is bidirectional but asymmetrical (BiAs) and so it offers the best protection for applications running up to 5 V.

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)





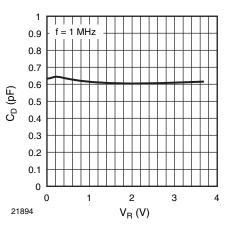


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5



VBUS051CD-HD1

Low Capacitance, Single-Line ESD-Protection Diode **Vishay Semiconductors**

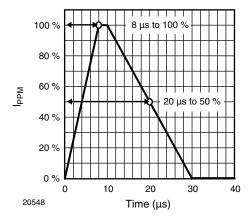


Fig. 3 - Typical Capacitance C_D vs. Reverse Voltage V_R

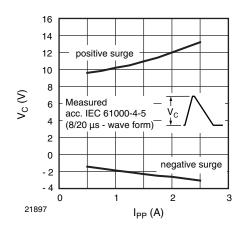


Fig. 4 - Typical Forward Current I_{F} vs. Forward Voltage V_{F}

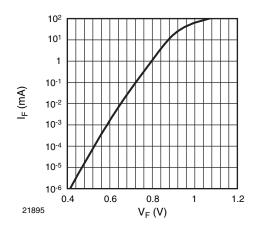


Fig. 5 - Typical Reverse Voltage V_{R} vs. Reverse Current I_{R}

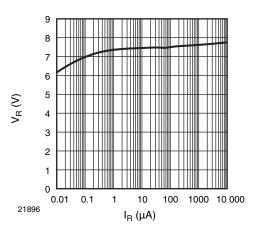


Fig. 6 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

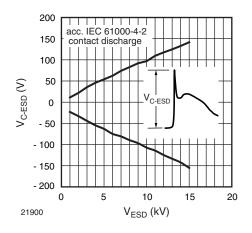


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

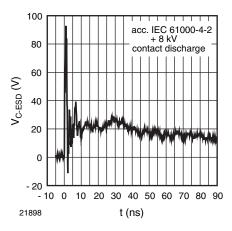
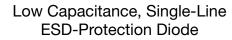


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)



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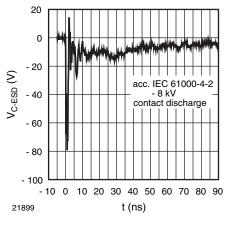
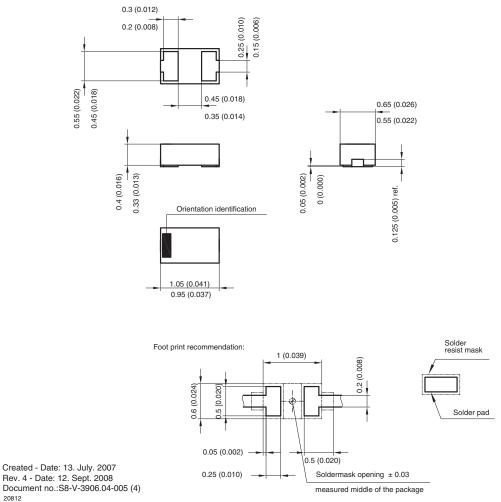


Fig. 9 - Typical Peak Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L



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